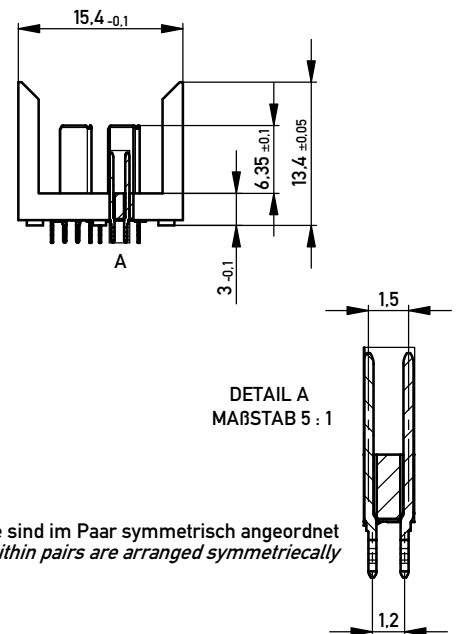
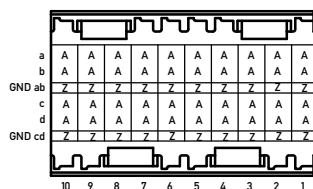


Lochbild für Leiterplatte
(Sicht auf Bestückungsseite)



Leiterplattenoberfläche Tochterkarte
Top surface of daughter card

9 x 2.5 = (22.5)

alle Löcher - all holes

2.5

1)

1.35

1.2

3.6

4.5

1.2

10 9 8 7 6 5 4 3 2 1

GND a

GND b

GND c

GND d

Ø 0.05

- Metal plating of plated-through hole
see drawing 384191*

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Information: A+Z siehe Bestückungsplan <i>see contact layout</i>				Tolerances		 All Dimensions in mm		Scale 2:1				
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								ERmet ZDplus Messerleiste 2-10 <i>ERmet ZDplus Male Con. 2-10</i>				
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