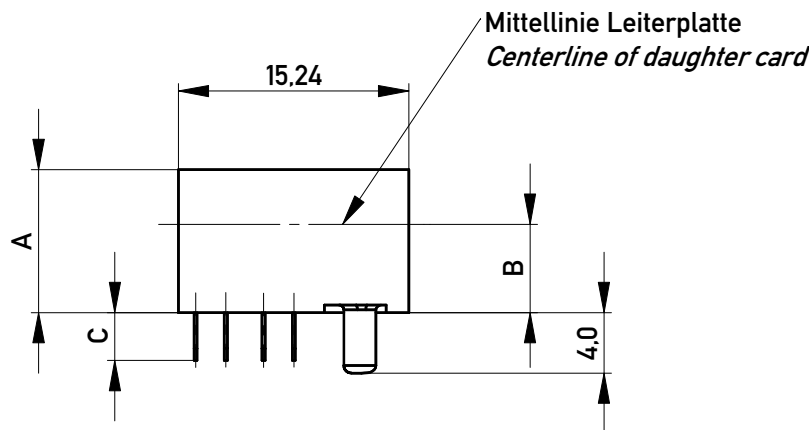
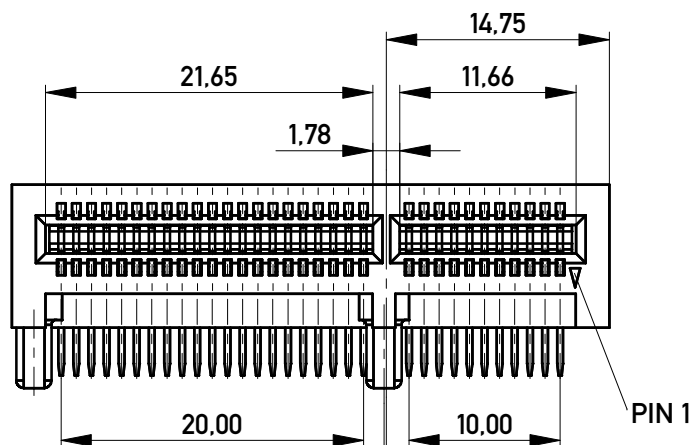
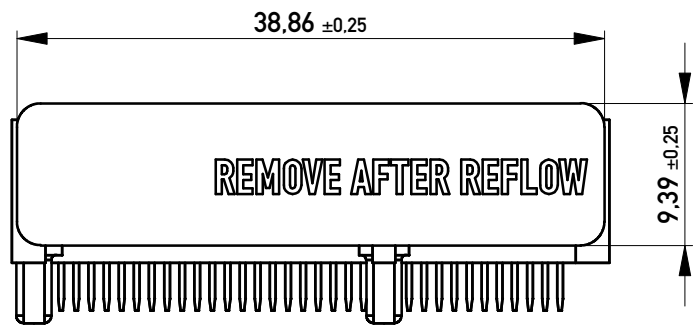
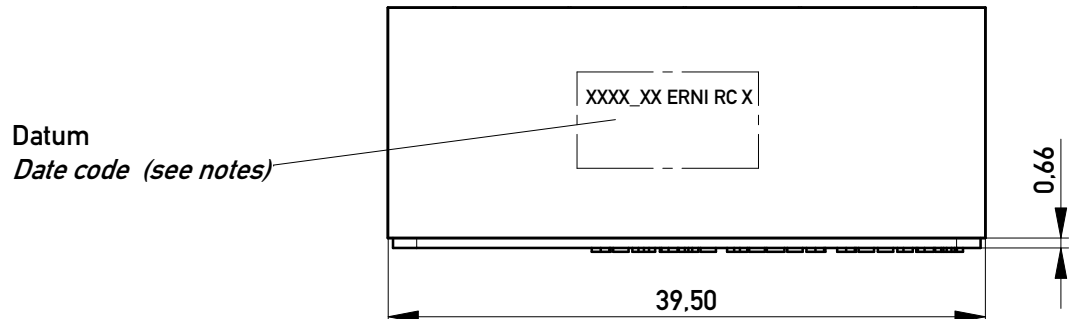


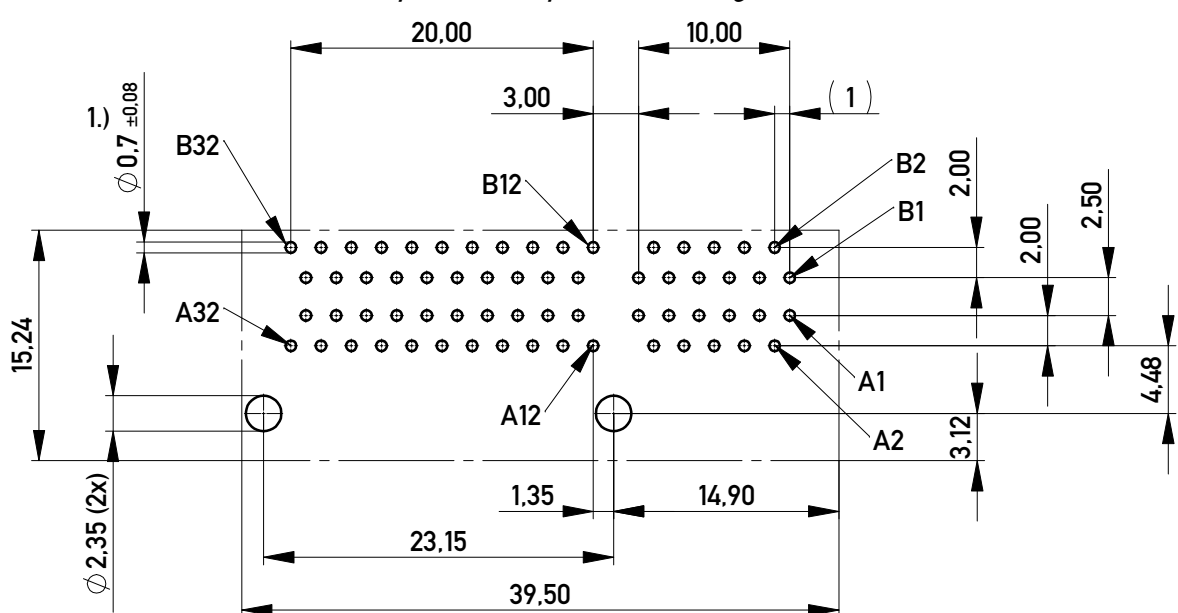
view with dust cover



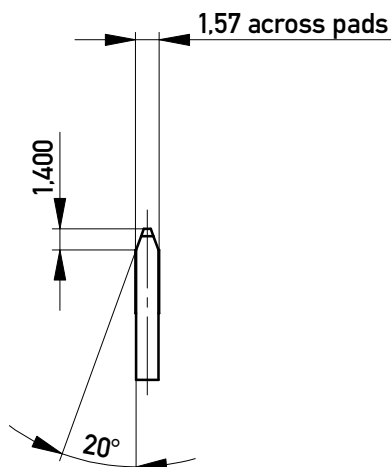
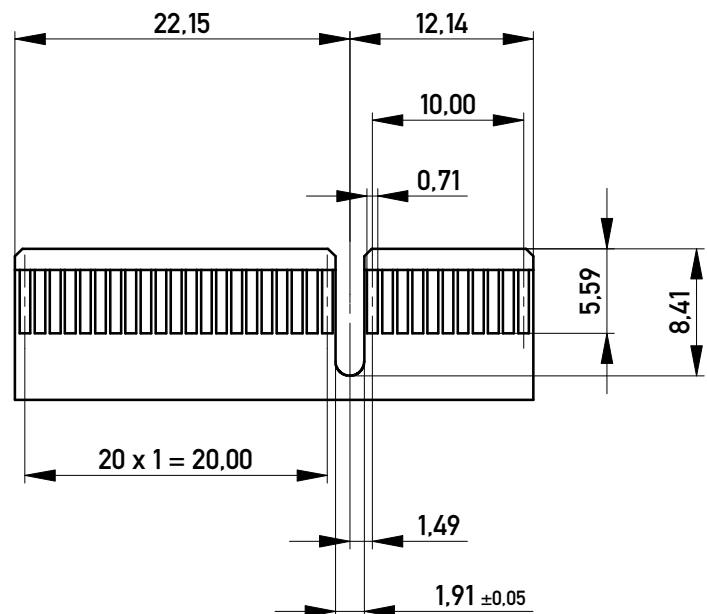
Offset von Führungssteg zu Referenzzapfen 0,15
Offset from guiding bar to reference peg



Lochbild für Leiterplatte (Bestückungsseite)
Board hole pattern (component mounting side)



daughter card layout



Layout proposal is in accordance with the PCI Express spezifikation

NOTES:

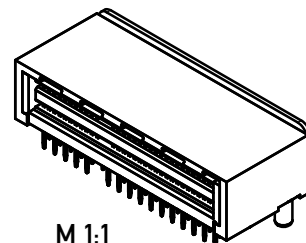
Material: LCP GF 30 schwarz
Material: LCP GF 30 black

Galvanik: min. 1.27 µm Unternickelung
über gesamten PIN - Bereich
Gold - Flash im Lötbereich
Plating: min. 1.27 µm Ni entire contact
Gold - Flash plating on contact tails

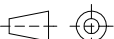
1.) Fertiglochdurchmesser
finished viadiameter (PTH)

Package: Tray (20 pcs.)

Connector printing / Labeling:
according to drawing 294775



85 Ohm	009804	14.7	11.18	3.18	min. 0.8 µm Au
	009704	14.7	11.18	3.18	min. 0.127 µm Au
	009604	14.7	11.18	2.54	min. 0.8 µm Au
	009504	14.7	11.18	2.54	min. 0.127 µm Au
	009404	9.4	5.84	3.18	min. 0.8 µm Au
	009304	9.4	5.84	3.18	min. 0.127 µm Au
	009204	9.4	5.84	2.54	min. 0.8 µm Au
100 Ohm	009104	9.4	5.84	2.54	min. 0.127 µm Au
	009065	20.3	16.69	3.18	min. 0.8 µm Au
	009064	14.7	11.18	3.18	min. 0.8 µm Au
	009063	9.4	5.84	3.18	min. 0.8 µm Au
	009045	20.3	16.69	2.54	min. 0.8 µm Au
	009044	14.7	11.18	2.54	min. 0.8 µm Au
	009043	9.4	5.84	2.54	min. 0.8 µm Au
	009025	20.3	16.69	3.18	min. 0.127 µm Au
	009024	14.7	11.18	3.18	min. 0.127 µm Au
	009023	9.4	5.84	3.18	min. 0.127 µm Au
	009005	20.3	16.69	2.54	min. 0.127 µm Au
	009004	14.7	11.18	2.54	min. 0.127 µm Au
	009003	9.4	5.84	2.54	min. 0.127 µm Au
Ident.-Nr. Part - No.	Maß "A" Dim "A"	Maß "B" Dim "B"	Maß "C" Dim "C"	Oberfläche Steckbereich plating contact area	

Information / Tolerances: Sofern nicht anderes angegeben / unless otherwise noted			Tolerances		Scale 2:1				
<table><tr><td>X ±0.254 [0.010"]</td><td>XX ±0.127 [0.005"]</td><td>XXX ±0.05 [0.002"]</td></tr></table>			X ±0.254 [0.010"]	XX ±0.127 [0.005"]	XXX ±0.05 [0.002"]	 All Dimensions in mm			
X ±0.254 [0.010"]	XX ±0.127 [0.005"]	XXX ±0.05 [0.002"]							
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					Abgew. PCI Express STV 64pol. <i>RT. Angle PCI Express 64 pins</i>				
					www.ERNI.com		284627		
h	16.02.2016				I				
Index	Date				A2				
			Class		PCIEXP				

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