



SPECIFICATIONS:

ELECTRICAL CHARACTERISTICS:

DIELECTRIC	NPO
RATED VOLTAGE	50 VDC
CAPACITANCE RANGE (EIA)	24 pFd (240)
CAPACITANCE TOLERANCE	J ($\pm 5\%$)
TEST PARAMETERS	1 MHz \pm 50 Hz, Values \leq 100pF 1 KHz \pm 50Hz, Values > 100pF @ 1.0 VRMS, 25°C
TEMPERATURE COEFFICIENT	0 \pm 30ppm / °C (-55°C TO +125°C)
DISSIPATION FACTOR	0.1% Max, 25°C
INSULATION RESISTANCE	> 1000 Ohm-Farad OR 100 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement)
DIELECTRIC STRENGTH	> 2.5 x WVDC, 50 mA max

MECHANICAL CHARACTERISTICS:

LENGTH "L"	WIDTH "W"	THICKNESS "T"	ENDBAND "EB"
.063 \pm .010	.032 \pm .010	.035 MAX	.010 \pm .005

TERMINATION DESCRIPTION:

TERMINATION: V - NICKEL BARRIER WITH TIN OVERPLATE

PACKAGING DESCRIPTION:

PACKAGE: T - 7" PAPER TAPE/REEL

MARKING DESCRIPTION:

4 - UNMARKED

						15191 BLEDSOE ST SYLMAR, CA 91342	
				TITLE			
MULTILAYER CHIP CAPACITOR							
REV	DESCRIPTION	APPROVED	DATE	PART NO.			DWG SCALE
				500R14N240JV4T			NONE
ENG. MGR.	DATE	Q.A. MGR.	DATE	DWG. NO.			SHEET
O Altaher	3/3/2005			A-6000-00-31			RE V 1 OF 1

Mouser Electronics

Authorized Distributor

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