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High-Speed, 6A Single MOSFET Drivers

General Description

The MAX4420, MAX4429 and MXT429 are single-output MOSFET drivers designed to translate TTL/CMOS inputs to high-voltage/high-current outputs. The low 1.5Ω output impedance and 6A peak current output allow them to rapidly switch high-capacitance power MOSFETs, improving efficiency.

A 40ns delay time and a 25ns rise or fall time (while driving 2500pF to 18V) minimize power losses during MOSFET switching transitions.

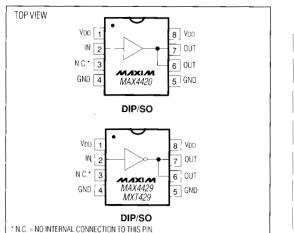
The MAX4420/MAX4429/MXT429 interface easily with either CMOS or bipolar switch-mode controllers because their logic inputs draw under 10 μ A. The outputs swing to within 25mV of GND or the power-supply rail, which can be 4.5V to 18V for the MAX4420/MAX4429, and 7V to 18V for the MXT429.

Power-supply quiescent current is typically 45μ A and 450μ A for logic input low and high, respectively. The MAX4420 has a non inverting output. The MAX4429 and MXT429 have inverting outputs.

For dual drivers, refer to the MAX626/MAX627/MAX628 and MAX4426/MAX4427/MAX4428 data sheets.

Applications

Switching Power Supplies DC-DC Converters Motor Controllers Pin-Diode Drivers



Pin Configurations

____ Features

- TTL/CMOS Compatible ($I_{IN} \le 10\mu A$)
- ♦ 4.5V to 18V Supply Range (MAX4420/MAX4429)
- 1.5Ω Output Resistance
- 6A Peak Output Current
- 40ns Delay Time
- 25ns Rise and Fall Times (2500pF Load)
- Output Swings to within 25mV of VDD and GND

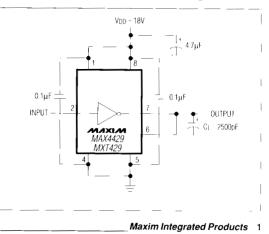
Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX4420CPA	0°C to +70°C	8 Plastic DIP
MAX4420CSA	0°C to +70°C	8 SO
MAX4420C/D	0°C to +70°C	_ Dice*
MAX4420EPA	-40°C to +85°C	8 Plastic DIP
MAX4420ESA	-40°C to +85°C	8 SO
MAX4420MJA	-55°C to +125°C	8 CERDIP**
MAX4429CPA	0°C to +70°C	8 Plastic DIP
MAX4429CSA	0°C to +70°C	8 SO
MAX4429C/D	0°C to +70°C	Dice*
MAX4429EPA	-40°C to +85°C	8 Plastic DIP
MAX4429ESA	-40°C to +85°C	8 SO
MAX4429MJA	-55°C to +125°C	8 CERDIP**

Ordering information continued on last page.

 Dice are specified at T_A = +25°C.
 **Contact factory for availability and processing to MIL-STD-883 and DESC-SMD.

Typical Operating Circuit



ABSOL	UTE I	MAXIMUM	RATINGS

 Supply Voltage Vpp to GND
 +20V

 Input Voltage ViN
 -0.3V to (Vpp + 0.3V)
 Input Voltage V_{IN} -Continuous Power Dissipation ($T_A = +70^{\circ}C$)

Plastic DIP (derate 9.09mW/°C above +70°C) 727mW SO (derate 5.88mW/°C above +70°C)

 Operating Temperature Ranges:
 0°C to +70°C

 MAX442_C___, MXT429C___
 0°C to +85°C

 MAX442_E__, MXT429E__
 -40°C to +85°C

 MAX442_MJA, MXT429MJA
 -55°C to +125°C

 Storage Temperature Range
 -65°C to +160°C

 Lead Temperature (soldering, 10sec)
 +300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(MAX4420/MAX4429 VDD = +4.5V to +18V, MXT429 VDD = +7V to +18V, TA = TMIN to TMAX, unless otherwise noted.)

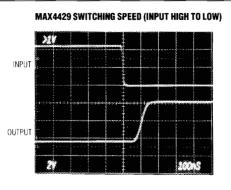
PARAMETER SYME	SYMBOL	CONDITIONS		MAX4420/MAX4429			MXT429			INUTO
PARAMETER SYMBOL		CONDITIONS		MIN TYP		MAX	MIN	ТҮР	MAX	UNITS
Operating Range	VDD			4.5		18	7		18	V
Power Supply Current	IDD	VIN = 3V	T _A = +25°C		0.45	1.5		0.45	5.0	mA
			TA = TMIN to TMAX			3.0			12.0	
			TA = +25°C		0.045	0.150		0.045	0.5	
		$V_{IN} = 0V$	$T_A = T_{MIN}$ to T_{MAX}			0.400			1.0	
Logic 1 Input Voltage	VIH			2.4			2.4			V
Logic 0 Input Voltage	VIL					0.8			0.8	V
IN Leakage Current	I IN	VIN = 0V to VDD				±10			±10	μA
Output High Voltage	Vон	No load		VDD	- 25		VD	o - 25		mV
Output Low Voltage	Vol	No load		•		25			25	mV
Peak Output Current	lout	VDD = 18V	$T_A = +25^{\circ}C$		6			6		A
Output Resistance ROUT		VDD = 18V, IOUT = 10mA, VIN = 0.8V or 2.4V	T _A = +25°C		1.5	2.5		1.5	2.5	
	Rout		TA = TMIN to TMAX			5.0			5.0	Ω
Rise Time (Note 1) t _R			T _A = +25°C		25	30		25	35	1
	^{IR}	Figure 1	T _A = T _{MIN} to T _{MAX}			60			70	ns
Fall Time (Note 1) tF		Figure 1	TA = +25°C		25	30		25	35	
	L LE		T _A = T _{MIN} to T _{MAX}			60			70	ns
Delay Time (Note 1)			T _A = +25°C	†.	35	60		35	75	
	tD1	tD1 Figure 1	$T_A = T_{MIN}$ to T_{MAX}			100			100	
			T _A = +25°C		40	60		40	75	ns
	tD2	Figure 1	$T_A = T_{MIN}$ to T_{MAX}			100			120	1

Note 1: Switching times guaranteed by design, not tested. See Figure 1 for timing measurement circuit, VDD = 18V.

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MAX4420/MAX4429/MXT429

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 $V_{DD} = 4.5 V, \ CL = 2500 pF, \ TIME = 100 ns/div, \ VIN = 5 V \ TO \ 0 V, \ T_A = +25^{\circ} C$

MAX4429 SWITCHING SPEED (INPUT HIGH TO LOW)

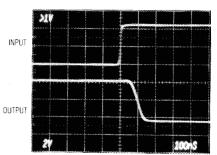


 $\begin{array}{l} V_{DD} = 18V, \ CL = 2500 pF, \ TIME = 100 ns/div, \\ V_{IN} - 5V \ TO \ 0V, \\ T_A = +25 \ C \end{array}$



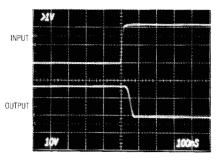
High-Speed, 6A Single MOSFET Drivers

MAX4429 SWITCHING SPEED (INPUT LOW TO HIGH)

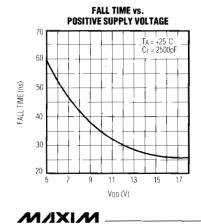


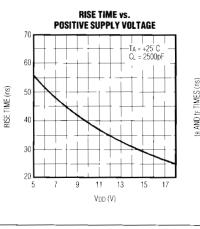
 $V_{DD}=4.5V,\ CL=2500pF,\ TIME=100ns/div \ VIN=0V\ TO\ 5V, \ TA=+25^{\circ}C$

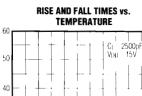
MAX4429 SWITCHING SPEED (INPUT LOW TO HIGH)

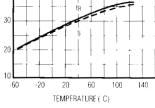


 $V_{DD}=$ 18V, CL = 2500pF, TIME = 100ns/div, $V_{IN}=$ 0V TO 5V. TA = +25 $^{\circ}\mathrm{C}$



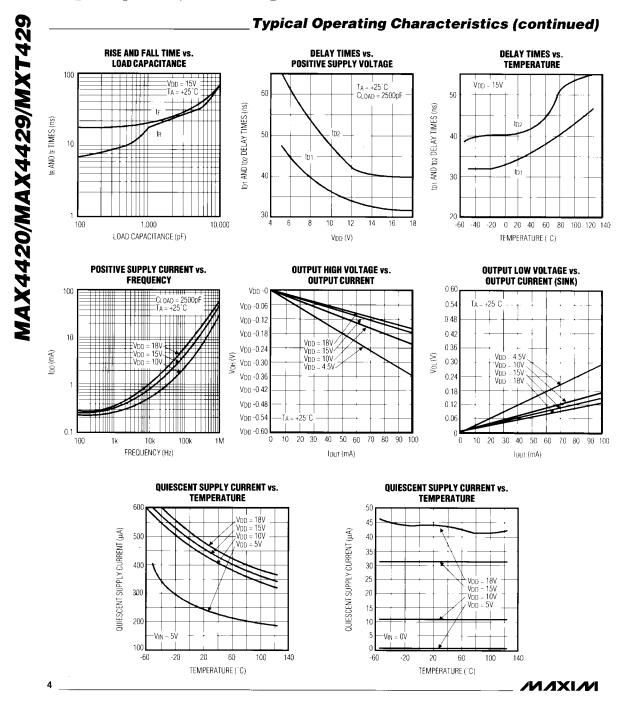






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MAX4420/MAX4429/MXT429



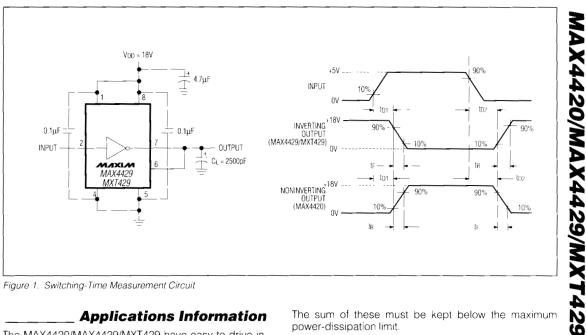


Figure 1. Switching-Time Measurement Circuit

Applications Information

The MAX4420/MAX4429/MXT429 have easy-to-drive inputs. However, the input must not be allowed to stav between VIH and VIL for more than 500ns. The power supply (VDD) inputs must always be tied together, as should the outputs (OUT).

Supply bypassing and grounding are extremely important, as the peak supply and output currents can be greater than 6A. Ground drops are a form of negative feedback with inverters, and therefore will degrade the delay and transition time. Ringing may also be a problem with large $\Delta V/\Delta t$ and/or large AC currents.

Suggested bypass capacitors are a 4.7µF (low ESR) capacitor in parallel with 0.1µF ceramic capacitors, mounted as close as possible to the device. Use a ground plane if possible, or separate ground returns for inputs and outputs. Ringing can be minimized with a 5Ω resistor in series with the output, but this will degrade output transition time.

Power Dissipation

Power dissipation of the MAX4420/MAX4429/MXT429 consists of:

1) input inverter losses

- 2) crowbar current through the output devices
- 3) output current (either capacitive or resistive).

The sum of these must be kept below the maximum power-dissipation limit.

The DC input inverter losses are typically 45μ A when the input is low and 450µA when the input is high.

The crowbar current through an output device making a transition is approximately 100mA for a few nanoseconds. This is a small portion of the total supply current, except for high switching frequencies or a small load capacitance (100pF).

The MAX4420/MAX4429/MXT429 power dissipation when driving a ground referenced resistive load is:

$P = D \times RON(max) \times ILOAD^2$

where D is the percentage of time the MAX4420/MAX4429/MXT429 output pulls high, RON(max) is the maximum on resistance of the device with the output high, and ILOAD is the load current of the MAX4420/MAX4429/MXT429.

For capacitive loads, the power dissipation is:

 $P = CLOAD \times VDD^2 \times FREQ$

where CLOAD is the capacitive load, VDD is the MAX4420/MAX4429/MXT429 supply voltage, and FREQ is the toggle frequency.

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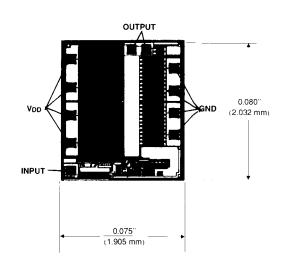
Ordering Information (continued)					
PART	TEMP. RANGE	PIN-PACKAGE			
MXT429CPA	0°C to +70°C	8 Plastic DIP			
MXT429CSA	0°C to +70°C	8 SO			
MXT429C/D	0°C to +70°C	Dice*			
MXT429EPA	-40°C to +85°C	8 Plastic DIP			
MXT429ESA	-40°C to +85°C	8 SO			
MXT429MJA	-55°C to +125°C	8 CERDIP**			

Dice are specified at $TA = +25^{\circ}C$.

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** Contact factory for availability and processing to MIL-STD-883 and DESC-SMD.

_Chip Topography



TRANSISTOR COUNT: 16; SUBSTRATE CONNECTED TO VDD.

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MAX4420/MAX4429/MXT429

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