



Typical Applications

Low noise MMIC VCO w/Buffer Amplifier for:

- VSAT Radio
- Point to Point/Multipoint Radio
- Test Equipment & Industrial Controls
- Military End-Use

HMC532LP4 / 532LP4E

MMIC VCO w/ BUFFER AMPLIFIER, 7.1 - 7.9 GHz

Features

Pout: +14 dBm Phase Noise: -103 dBc/Hz @100 KHz No External Resonator Needed Single Supply: +3V @ 85 mA 24 Lead 4x4mm QFN Package: 9 mm²

VTUNE N/C N/C Vcc Z \geq 24 23 21 20 19 22 N/C 1 18 N/C N/C 2 17 N/C N/C 3 16 RFOUT N/C 4 15 GND N/C N/C 5 14 N/C 6 13 N/C 7 12 8 10 11 9 PACKAGE N/C N/C N/C BASE GND TOP VIEW

General Description

The HMC532LP4 & HMC532LP4E are GaAs InGaP Heterojunction Bipolar Transistor (HBT) MMIC VCOs with integrated resonators, negative resistance devices, varactor diodes, and buffer amplifiers. Covering 7.1 to 7.9 GHz, the VCO's phase noise performance is excellent over temperature, shock and vibration due to the oscillator's monolithic structure. Power output is +14 dBm typical from a single supply of +3V @ 85 mA. The voltage controlled oscillator is packaged in a leadless QFN 4 x 4 mm surface mount package.

Electrical Specifications, $T_{A} = +25^{\circ} C$, Vcc = +3V

Parameter	Min.	Тур.	Max.	Units
Frequency Range		7.1 - 7.9		
Power Output	12	14	17	dBm
SSB Phase Noise @ 100 kHz Offset, Vtune= +5V @ RF Output		-101		dBc/Hz
Tune Voltage (Vtune)	1		13	V
Supply Current (Icc) (Vcc = +3V)	60	85	100	mA
Tune Port Leakage Current			10	μA
Output Return Loss		15		dB
Harmonics 2nd 3rd		-14 -25		dBc dBc
Pulling (into a 2.0:1 VSWR)		28		MHz pp
Pushing @ Vtune= +5V		78		MHz/V
Frequency Drift Rate		0.85		MHz/°C

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VCOS - SMT

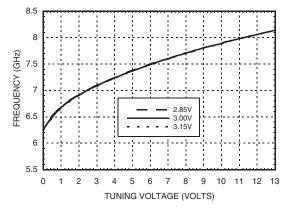
12

Functional Diagram

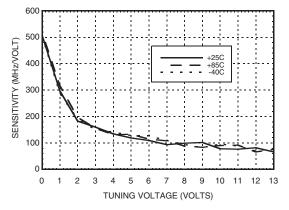




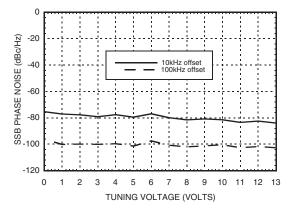
Frequency vs. Tuning Voltage, T= 25°C



Sensitivity vs. Tuning Voltage, Vcc= +3V

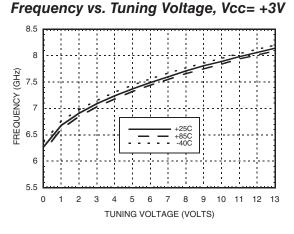


Phase Noise vs. Tuning Voltage

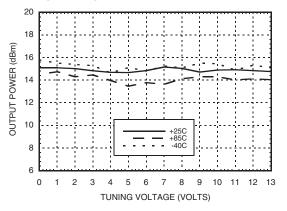


HMC532LP4 / 532LP4E

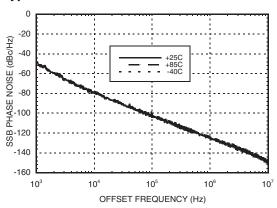
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Output Power vs. Tuning Voltage, Vcc= +3V



Typical SSB Phase Noise @ Vtune= +5V



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Absolute Maximum Ratings

Vcc	+3.5 Vdc
Vtune	0 to +15V
Channel Temperature	135 °C
Continuous Pdiss (T = 85°C) (derate 6.07 mW/°C above 85°C)	303 mW
Thermal Resistance (junction to ground paddle)	165 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

Typical Supply Current vs. Vcc

Vcc (V)	Icc (mA)
2.75	74
3.0	85
3.25	96

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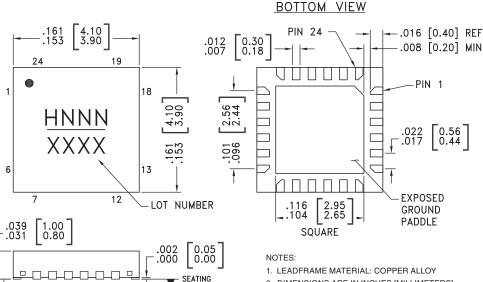
Note: VCO will operate over full voltage range shown above.



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

Outline Drawing

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PLANE

-C-

- 2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
- 3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE.
- 4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM. PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
- 6. ALL GROUND LEADS AND GROUND PADDLE MUST BE
- SOLDERED TO PCB RF GROUND. 7. REFER TO HITTITE APPLICATION NOT FOR SUGGESTED
 - LAND PATTERN.

Package Information

.003[0.08] C

[Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
	HMC532LP4	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	H532 XXXX
	HMC532LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>H532</u> XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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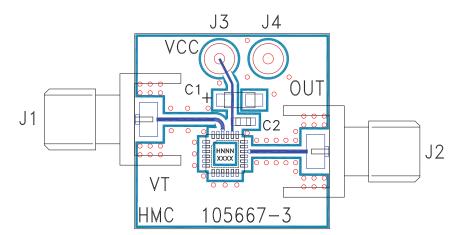
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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1- 14, 17 - 19, 21, 23, 24	N/C	No Connection	
15	GND	This pin must be connected to RF & DC ground. Package bottom has an exposed metal paddle that must be RF & DC grounded.	
16	RFOUT	RF output (AC coupled)	○ RFOUT
20	Vcc	Supply Voltage Vcc= 3V	VccO
22	VTUNE	Control Voltage Input. Modulation port bandwidth dependent on drive source impedance. See "Determining the FM Bandwidth of a Wideband Varactor Tuned VCO" application note.	VTUNE \bigcirc

Evaluation PCB



List of Materials for Evaluation PCB 105706 [1]

Item	Description	
J1 - J2	PCB Mount SMA RF Connector	
J3 - J4	DC Pin	
C1	4.7 µF Tantalum Capacitor	
C2	10,000 pF Capacitor, 0603 Pkg.	
U1	HMC532LP4 / HMC532LP4E VCO	
PCB [2]	105667 Eval Board	

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

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