

ADC Logic Probe Adapter Board

DESCRIPTION

Demonstration circuit 1321 allows a logic analyzer to be directly connected to a DC890 compatible ADC demo circuit for capturing data. DC1321A-B has 100Ω termination resistors installed, for use with LVDS output ADCs and logic analyzer inputs that do not have termination. DC1321A-A has no termination resistors installed, for use with CMOS

output ADCs or with LVDS output ADCs, should the logic analyzer have built-in termination.

Design files for this circuit board are available at
<http://www.linear.com/demo/DC1321>

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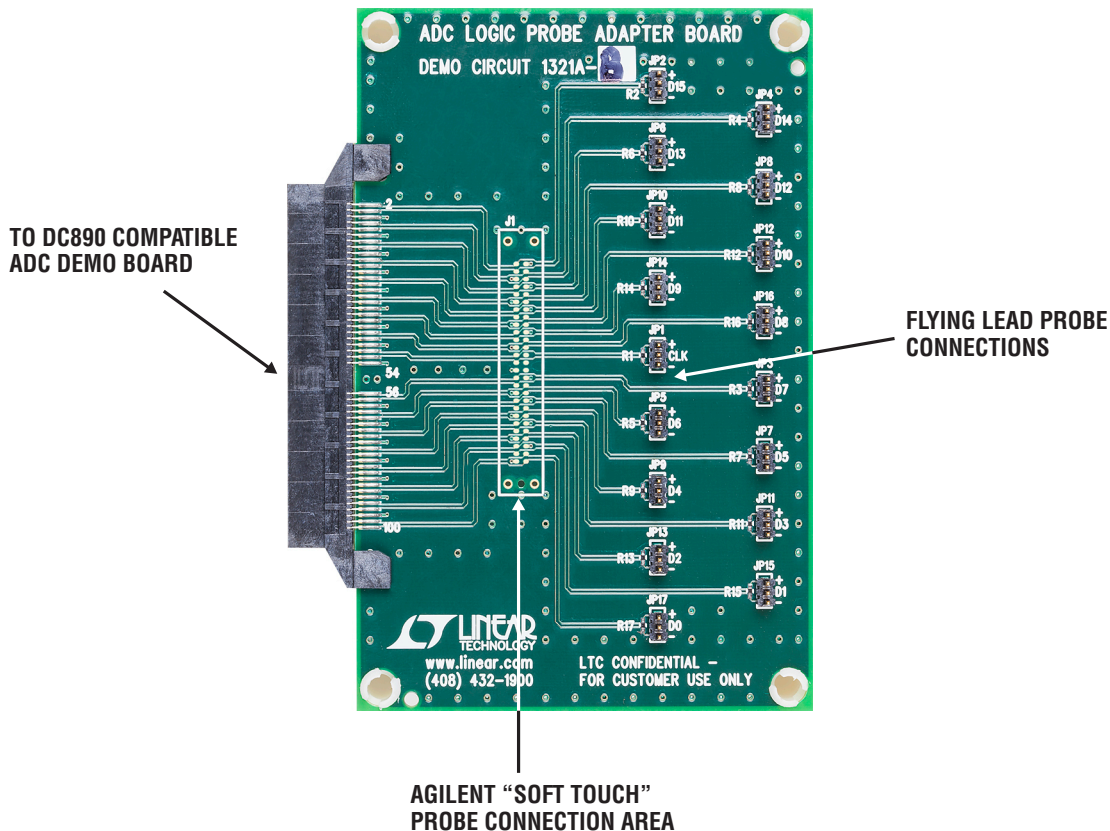


Figure 1. DC1321

DEMO MANUAL DC1321

PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
1	1	P1	SOCKET, 0.8MM DUAL EDGE MOUNT	SAMTEC, MEC8-150-02-L-D-EM2 (PbF)
2	17	JP1-JP17	HEADER, 3x1 .050" PIN SPACING	SAMTEC, HTMS-103-02-S-S (PbF)
3	1	R18	RES., CHIP 1k 5% 0402	VISHAY, CRCW04021K00JNED
4	4	FOR 4 CORNERS	STAND-OFF, NYLON (SNAP ON), 0.625" TALL	KEYSTONE, 8834 (PbF)

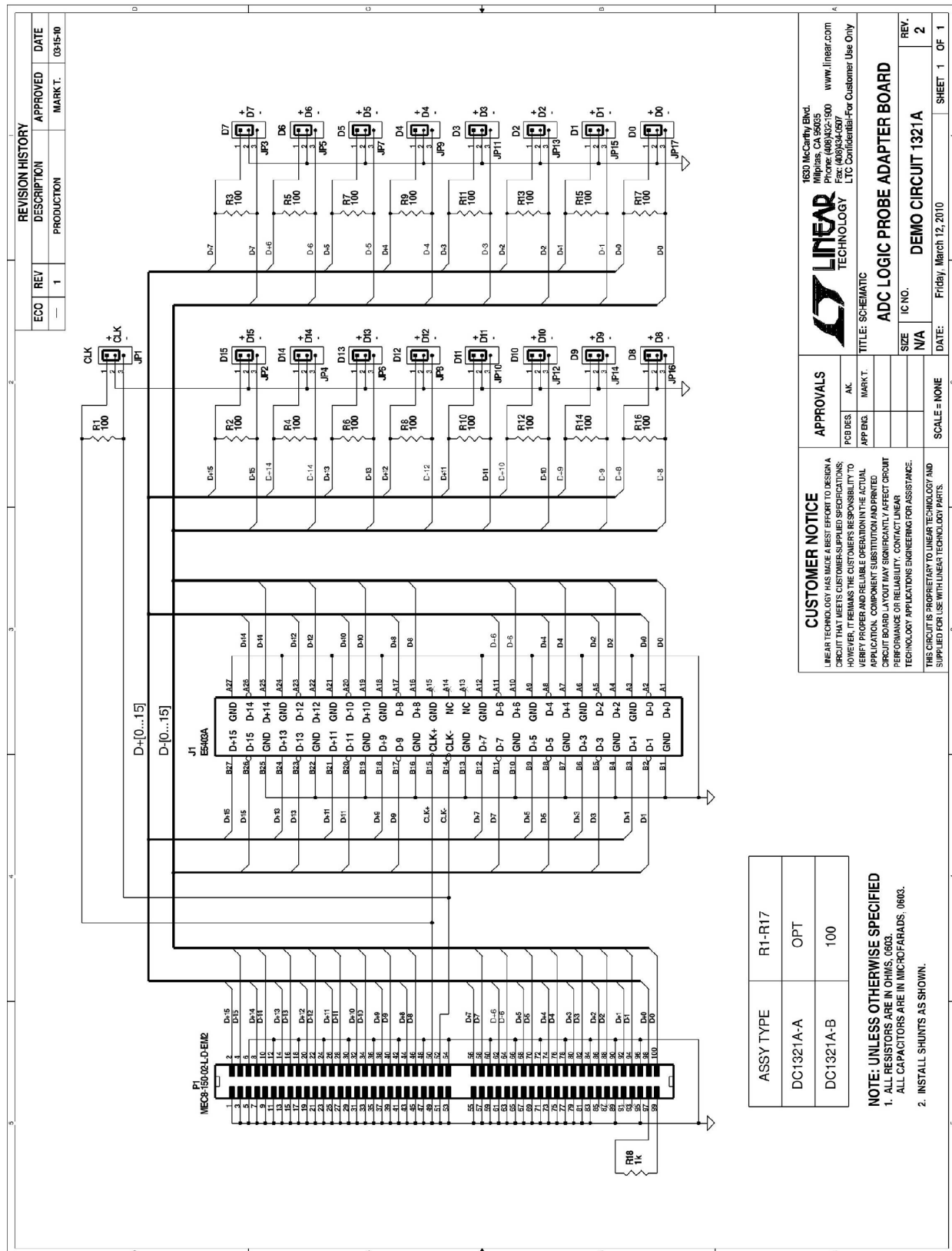
DC1321A-A

5	0	R1-R17	OPT	
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DC1321A-B

5	17	R1-R17	RES., CHIP 100 Ω 5% 0402	VISHAY, CRCW0402100RJNED
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SCHEMATIC DIAGRAM



1520 McCarty Blvd.
 Milpitas, CA 95035
 Phone: (408)332-1500
 Fax: (408)334-0607
 LTC Confidential-For Customer Use Only

LINEAR TECHNOLOGY

TITLE: SCHEMATIC

ADC LOGIC PROBE ADAPTER BOARD

IC NO. DEMO CIRCUIT 1321A

SIZE N/A

DATE: Friday, March 12, 2010

SHEET 1 OF 1

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THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	AK
APP ENG.	MARK T.

SCALE = NONE

DEMO MANUAL DC1321

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This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

Mailing Address:

Linear Technology
1630 McCarthy Blvd.
Milpitas, CA 95035

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dc1321f

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