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JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	XX/XX	X.X.

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

* SEE ASSEMBLY INSTRUCTIONS

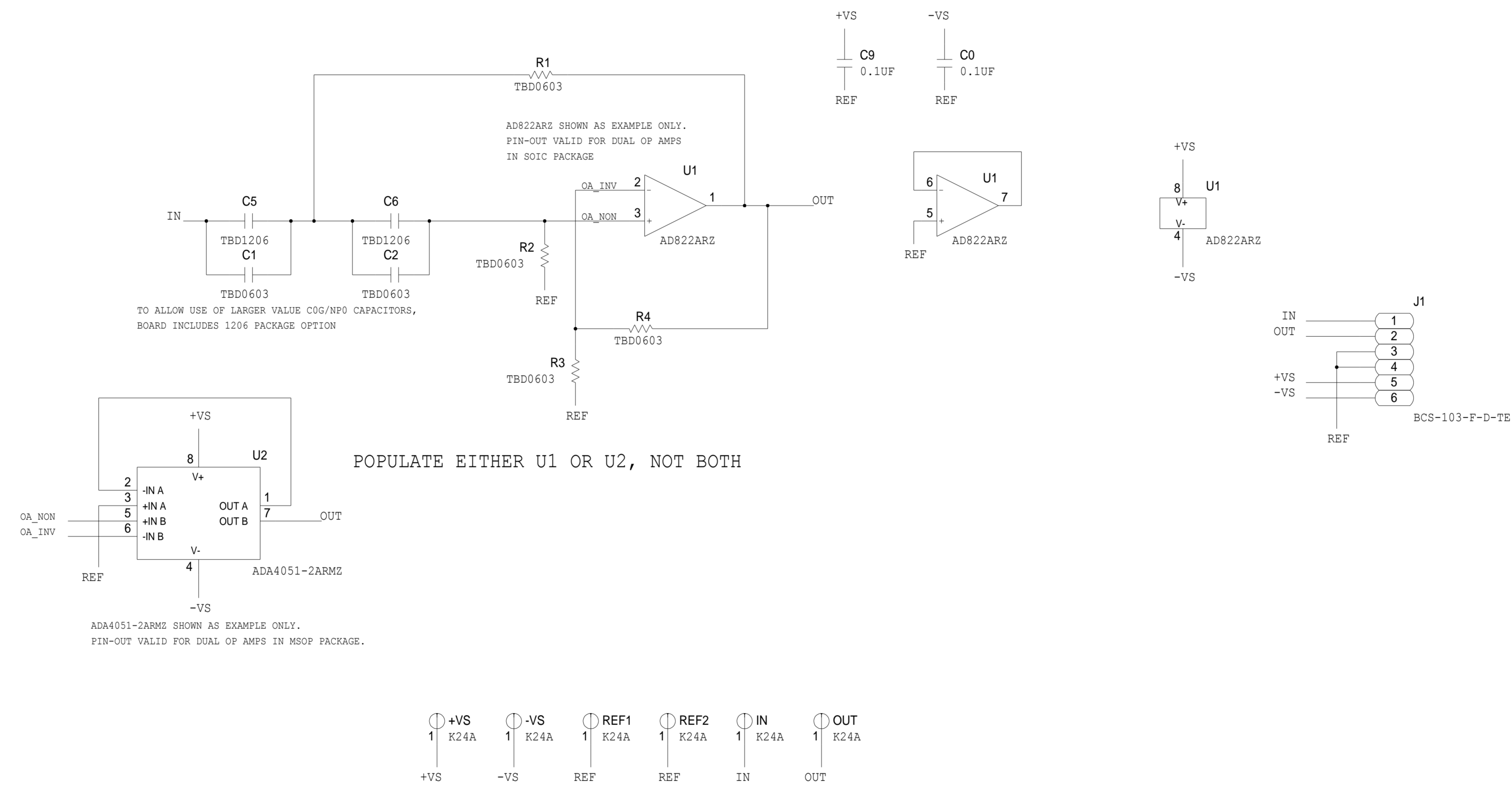


P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER
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<table border="1" style="width:100%;"> <tr><td>TEMPLATE ENGINEER</td><td>DATE</td></tr> <tr><td>-</td><td></td></tr> <tr><td>HARDWARE SERVICES</td><td></td></tr> <tr><td>-</td><td></td></tr> <tr><td>HARDWARE SYSTEMS</td><td></td></tr> <tr><td>-</td><td></td></tr> <tr><td>TEST ENGINEER</td><td></td></tr> <tr><td>-</td><td></td></tr> <tr><td>COMPONENT ENGINEER</td><td></td></tr> <tr><td>-</td><td></td></tr> <tr><td>TEST PROCESS</td><td></td></tr> <tr><td>-</td><td></td></tr> <tr><td>HARDWARE RELEASE</td><td></td></tr> <tr><td>-</td><td></td></tr> <tr><td>DESIGNER</td><td>MASTER PROJECT TEMPLATE</td></tr> <tr><td>LABRANCHE</td><td><DRAWING_TITLE6></td></tr> <tr><td>PTD ENGINEER</td><td>TESTER TEMPLATE</td></tr> <tr><td><PTD ENGINEER></td><td>-</td></tr> <tr><td>CHECKER</td><td></td></tr> <tr><td>-</td><td></td></tr> </table>	TEMPLATE ENGINEER	DATE	-		HARDWARE SERVICES		-		HARDWARE SYSTEMS		-		TEST ENGINEER		-		COMPONENT ENGINEER		-		TEST PROCESS		-		HARDWARE RELEASE		-		DESIGNER	MASTER PROJECT TEMPLATE	LABRANCHE	<DRAWING_TITLE6>	PTD ENGINEER	TESTER TEMPLATE	<PTD ENGINEER>	-	CHECKER		-		<h1 style="margin: 0;">SCHEMATIC</h1>	
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SALLEN KEY HIGH PASS FILTER STAGE FOR DUAL OP AMPS



	<p align="center">SCHMATIC</p>		
	<p>EVAL BOARD SALLEN KEY HIGH PASS DUAL <PRODUCT_1></p>		
	<p>DESIGN VIEW <DESIGN_VIEW></p>	<p>DRAWING NO. 02-034899</p>	<p>REV A</p>
	<p>PTD ENGINEER <PTD_ENGINEER></p>	<p>SIZE D</p>	<p>SCALE <SCALE></p>
<p align="right">SHEET 2 OF 2</p>		<p align="right">1</p>	

Mouser Electronics

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[EVAL-FW-HPSK2](#)