

# MAXIM

## 2.5Ω, Dual, SPST, CMOS Analog Switches

**MAX4667/MAX4668/MAX4669**

### General Description

The MAX4667/MAX4668/MAX4669 dual analog switches feature low on-resistance of 2.5Ω max. On-resistance is matched between switches to 0.5Ω max and is flat (0.5Ω max) over the specified signal range. Each switch can handle Rail-to-Rail® analog signals. The off-leakage current is only 5nA max at +85°C. These analog switches are ideal in low-distortion applications and are the preferred solution over mechanical relays in automatic test equipment or applications where current switching is required. They have low power requirements, use less board space, and are more reliable than mechanical relays.

The MAX4667 has two normally closed (NC) switches, the MAX4668 has two normally open (NO) switches, and the MAX4669 has one NC and one NO switch that guarantee break-before-make operation.

These switches operate from a +4.5V to +36V single supply or from ±4.5V to ±20V dual supplies. All digital inputs have +0.8V and +2.4V logic thresholds, ensuring TTL/CMOS-logic compatibility when using ±15V supplies or a single +12V supply.

### Applications

|                        |                      |
|------------------------|----------------------|
| Reed Relay Replacement | PBX, PABX Systems    |
| Test Equipment         | Audio-Signal Routing |
| Communication Systems  | Avionics             |

### Features

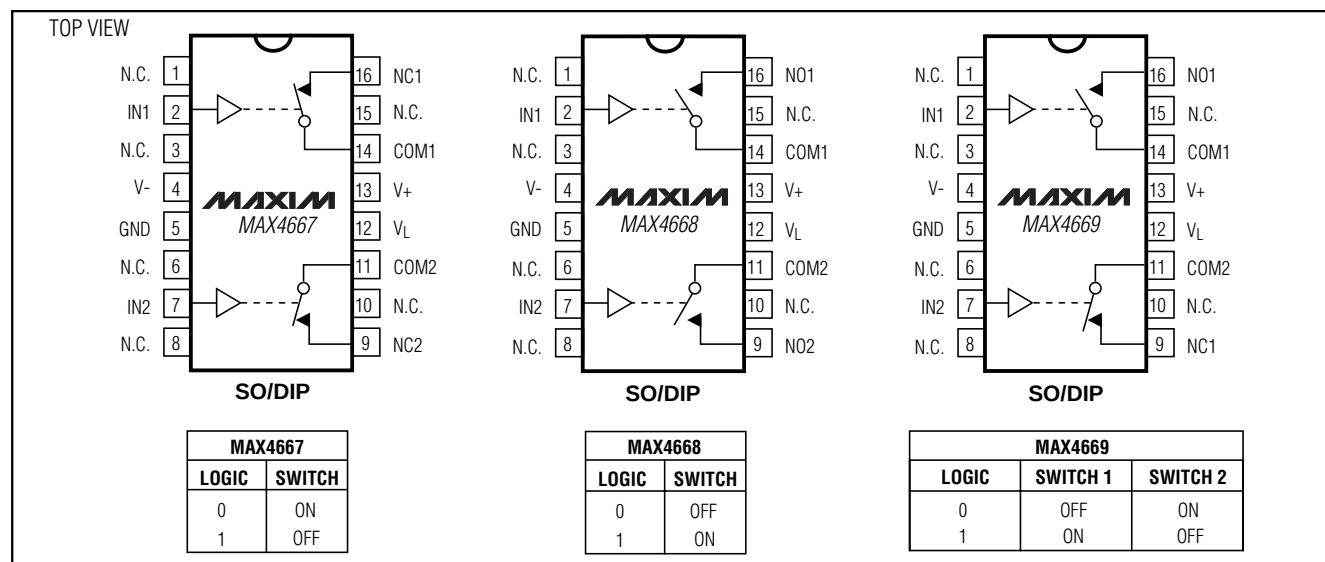
- ♦ Low On-Resistance (2.5Ω max)
- ♦ Guaranteed  $R_{ON}$  Match Between Channels (0.5Ω max)
- ♦ Guaranteed  $R_{ON}$  Flatness over Specified Signal Range (0.5Ω max)
- ♦ Guaranteed Break-Before-Make (MAX4669)
- ♦ Rail-to-Rail Signal Handling
- ♦ Guaranteed ESD Protection >2kV per Method 3015.7
- ♦ +4.5V to +36V Single-Supply Operation  
±4.5V to ±20V Dual-Supply Operation
- ♦ TTL/CMOS-Compatible Control Inputs

### Ordering Information

| PART       | TEMP. RANGE    | PIN-PACKAGE    |
|------------|----------------|----------------|
| MAX4667CSE | 0°C to +70°C   | 16 Narrow SO   |
| MAX4667CPE | 0°C to +70°C   | 16 Plastic DIP |
| MAX4667ESE | -40°C to +85°C | 16 Narrow SO   |
| MAX4667EPE | -40°C to +85°C | 16 Plastic DIP |

Ordering Information continued at end of data sheet.

### Pin Configurations/Functional Diagrams/Truth Tables



Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

**MAXIM**

Maxim Integrated Products 1

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For small orders, phone 1-800-835-8769.

# 2.5Ω, Dual, SPST, CMOS Analog Switches

## ABSOLUTE MAXIMUM RATINGS

V+ to GND .....-0.3V to +44V  
 V- to GND .....+0.3V to -44V  
 V+ to V-.....-0.3V to +44V  
 VL to GND .....(GND - 0.3V) to (V+ + 0.3V)  
 All Other Pins to GND (Note 1).....(V- - 0.3V) to (V+ + 0.3V)  
 Continuous Current (COM\_, NO\_, NC\_).....±100mA  
 Peak Current (COM\_, NO\_, NC\_).....±300mA  
 (pulsed at 1ms, 10% duty cycle)

Continuous Power Dissipation (TA = +70°C)  
 Narrow SO (derate 8.70mW/°C above +70°C) .....696mW  
 Plastic DIP (derate 10.53mW/°C above +70°C) .....842mW  
 Operating Temperature Ranges  
 MAX466\_C\_E .....0°C to +70°C  
 MAX466\_E\_E .....-40°C to +85 °C  
 Storage Temperature Range .....-65°C to +150°C  
 Lead Temperature (soldering, 10sec) .....+300°C

**Note 1:** Signals on NC\_, NO\_, COM\_, or IN\_ exceeding V+ or V- will be clamped by internal diodes. Limit the forward diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS—Dual Supplies

(V+ = +15V, V- = -15V, VL = +5V, VIN\_H = +2.4V, VIN\_L = +0.8V, TA = TMIN to TMAX, unless otherwise noted. Typical values are at TA = +25°C.) (Note 2)

| PARAMETER                                                                        | SYMBOL                                                     | CONDITIONS                                                                              |                                                       | MIN  | TYP   | MAX | UNITS |
|----------------------------------------------------------------------------------|------------------------------------------------------------|-----------------------------------------------------------------------------------------|-------------------------------------------------------|------|-------|-----|-------|
| ANALOG SWITCH                                                                    |                                                            |                                                                                         |                                                       |      |       |     |       |
| Input Voltage Range<br>(Note 3)                                                  | V <sub>COM_</sub> , V <sub>NO_</sub> ,<br>V <sub>NC_</sub> |                                                                                         |                                                       | V-   |       | V+  | V     |
| COM_ to NO_, COM_ to NC_<br>On-Resistance                                        | R <sub>ON</sub>                                            | I <sub>COM_</sub> = 10mA,<br>V <sub>NO_</sub> or V <sub>NC_</sub> = ±10V                | T <sub>A</sub> = +25°C                                | 1.6  | 2.5   | Ω   |       |
|                                                                                  |                                                            |                                                                                         | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | 3    |       |     |       |
| COM_ to NO_, COM_ to NC_<br>On-Resistance Match Between<br>Channels (Notes 3, 4) | ΔR <sub>ON</sub>                                           | I <sub>COM_</sub> = 10mA; V <sub>NO_</sub><br>or V <sub>NC_</sub> = -5V, 0, 5V          | T <sub>A</sub> = +25°C                                | 0.05 | 0.4   | Ω   |       |
|                                                                                  |                                                            |                                                                                         | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | 0.5  |       |     |       |
| COM_ to NO_, COM_ to NC_<br>On-Resistance Flatness<br>(Notes 3, 5)               | R <sub>FLAT(ON)</sub>                                      | I <sub>COM_</sub> = 10mA,<br>V <sub>NO_</sub> or V <sub>NC_</sub> = ±10V                | T <sub>A</sub> = +25°C                                | 0.1  | 0.4   | Ω   |       |
|                                                                                  |                                                            |                                                                                         | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | 0.5  |       |     |       |
| Off-Leakage Current<br>(NO_ or NC_) (Note 6)                                     | I <sub>NO_</sub> , I <sub>NC_</sub>                        | V <sub>COM_</sub> = ±10V,<br>V <sub>NO_</sub> or V <sub>NC_</sub> = ∓10V                | T <sub>A</sub> = +25°C                                | -0.5 | 0.01  | 0.5 | nA    |
|                                                                                  |                                                            |                                                                                         | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | -5   | 5     |     |       |
| COM_ Off-Leakage Current<br>(Note 6)                                             | I <sub>COM_(OFF)</sub>                                     | V <sub>COM_</sub> = ±10V,<br>V <sub>NO_</sub> or V <sub>NC_</sub> = ∓10V                | T <sub>A</sub> = +25°C                                | -0.5 | 0.01  | 0.5 | nA    |
|                                                                                  |                                                            |                                                                                         | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | -5   | 5     |     |       |
| COM_ On-Leakage Current<br>(Note 6)                                              | I <sub>COM_(ON)</sub>                                      | V <sub>COM_</sub> = ±10V,<br>V <sub>NO_</sub> or V <sub>NC_</sub> = ±10V<br>or floating | T <sub>A</sub> = +25°C                                | -1   | 0.02  | 1   | nA    |
|                                                                                  |                                                            |                                                                                         | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | -20  | 20    |     |       |
| LOGIC INPUT                                                                      |                                                            |                                                                                         |                                                       |      |       |     |       |
| Input Current with Input Voltage<br>High                                         | I <sub>IN_H</sub>                                          | IN_ = 2.4V, all others = 0.8V                                                           |                                                       | -0.5 | 0.001 | 0.5 | μA    |
| Input Current with Input Voltage<br>Low                                          | I <sub>IN_L</sub>                                          | IN_ = 0.8V, all others = 2.4V                                                           |                                                       | -0.5 | 0.001 | 0.5 | μA    |
| Logic Input Voltage High                                                         | V <sub>IN_H</sub>                                          |                                                                                         |                                                       | 2.4  |       |     | V     |
| Logic Input Voltage Low                                                          | V <sub>IN_L</sub>                                          |                                                                                         |                                                       |      |       | 0.8 | V     |

# 2.5Ω, Dual, SPST, CMOS Analog Switches

MAX4667/MAX4668/MAX4669

## ELECTRICAL CHARACTERISTICS—Dual Supplies (continued)

(V+ = +15V, V- = -15V, VL = +5V, VIN\_H = +2.4V, VIN\_L = +0.8V, TA = TMIN to TMAX, unless otherwise noted. Typical values are at TA = +25°C.) (Note 2)

| PARAMETER                              | SYMBOL | CONDITIONS                               |                   | MIN  | TYP   | MAX   | UNITS |
|----------------------------------------|--------|------------------------------------------|-------------------|------|-------|-------|-------|
| POWER SUPPLY                           |        |                                          |                   |      |       |       |       |
| Power-Supply Range                     |        |                                          |                   | ±4.5 |       | ±20.0 | V     |
| Positive Supply Current                | I+     | VIN = 0 or 5V                            | TA = +25°C        | -0.5 | 0.001 | 0.5   | μA    |
|                                        |        |                                          | TA = TMIN to TMAX | 5    |       | 5     |       |
| Negative Supply Current                | I-     | VIN = 0 or 5V                            | TA = +25°C        | -0.5 | 0.001 | 0.5   | μA    |
|                                        |        |                                          | TA = TMIN to TMAX | 5    |       | 5     |       |
| Logic Supply Current                   | IL     | VIN = 0 or 5V                            | TA = +25°C        | -0.5 | 0.001 | 0.5   | μA    |
|                                        |        |                                          | TA = TMIN to TMAX | 5    |       | 5     |       |
| Ground Current                         | IGND   | VIN = 0 or 5V                            | TA = +25°C        | -0.5 | 0.001 | 0.5   | μA    |
|                                        |        |                                          | TA = TMIN to TMAX | 5    |       | 5     |       |
| SWITCH DYNAMIC CHARACTERISTICS         |        |                                          |                   |      |       |       |       |
| Turn-On Time                           | tON    | VCOM_ = ±10V, Figure 2                   | TA = +25°C        |      | 130   | 275   | ns    |
|                                        |        |                                          | TA = TMIN to TMAX |      |       | 400   |       |
| Turn-Off Time                          | tOFF   | VCOM_ = ±10V, Figure 2                   | TA = +25°C        |      | 90    | 175   | ns    |
|                                        |        |                                          | TA = TMIN to TMAX |      |       | 300   |       |
| Break-Before-Make Time Delay (MAX4669) |        | VCOM = 10V                               |                   | 5    | 30    |       | ns    |
| Charge Injection                       | Q      | CL = 1.0nF, VGEN = 0, RGEN = 0, Figure 3 |                   |      | 450   |       | pC    |
| Off-Isolation (Note 7)                 | VISO   | RL = 50Ω, CL = 5pF, f = 1MHz, Figure 4   |                   |      | -60   |       | dB    |
| Crosstalk (Note 8)                     | VCT    | RL = 50Ω, CL = 5pF, f = 1MHz, Figure 5   |                   |      | -66   |       | dB    |
| NC_ or NO_ Capacitance                 | COFF   | f = 1MHz, Figure 6                       |                   |      | 65    |       | pF    |
| COM_ Off-Capacitance                   | CCOM   | f = 1MHz, Figure 6                       |                   |      | 65    |       | pF    |
| On-Capacitance                         | CCOM   | f = 1MHz, Figure 7                       |                   |      | 290   |       | pF    |

## 2.5Ω, Dual, SPST, CMOS Analog Switches

### ELECTRICAL CHARACTERISTICS—Single Supply

(V+ = +12V, V- = 0, VL = +5V, VIN\_H = +2.4V, VIN\_L = +0.8V, TA = TMIN to TMAX, unless otherwise noted. Typical values are at TA = +25°C.) (Note 2)

| PARAMETER                                                                        | SYMBOL                          | CONDITIONS                                                                   |                                                      | MIN  | TYP   | MAX | UNITS    |
|----------------------------------------------------------------------------------|---------------------------------|------------------------------------------------------------------------------|------------------------------------------------------|------|-------|-----|----------|
| ANALOG SWITCH                                                                    |                                 |                                                                              |                                                      |      |       |     |          |
| Input Voltage Range<br>(Note 3)                                                  | $V_{COM\_}, V_{NO\_}, V_{NC\_}$ |                                                                              |                                                      | 0    |       | V+  | V        |
| COM_ to NO_, COM_ to NC_<br>On-Resistance                                        | $R_{ON}$                        | $I_{COM\_} = 10mA;$<br>$V_{NO\_}$ or $V_{NC\_} = 10V$                        | $T_A = +25^{\circ}C$<br>$T_A = T_{MIN}$ to $T_{MAX}$ | 3    | 4     | 5   | $\Omega$ |
| COM_ to NO_, COM_ to NC_<br>On-Resistance Match Between<br>Channels (Notes 3, 4) | $\Delta R_{ON}$                 | $I_{COM\_} = 10mA;$<br>$V_{NO\_}$ or $V_{NC\_} = 10V$                        | $T_A = +25^{\circ}C$<br>$T_A = T_{MIN}$ to $T_{MAX}$ | 0.05 | 0.4   | 0.5 | $\Omega$ |
| COM_ to NO_, COM_ to NC_<br>On-Resistance Flatness<br>(Note 5)                   | $R_{FLAT(ON)}$                  | $I_{COM\_} = 10mA;$<br>$V_{NO\_}$ or $V_{NC\_} = 3V,$<br>6V, 0V              | $T_A = +25^{\circ}C$<br>$T_A = T_{MIN}$ to $T_{MAX}$ | 0.05 | 0.5   | 0.5 | $\Omega$ |
| Off-Leakage Current<br>(NO_ or NC_) (Notes 6, 9)                                 | $I_{NO\_}, I_{NC\_}$            | $V_{COM\_} = 1V, 10V;$<br>$V_{NO\_}$ or $V_{NC\_} = 1V,$<br>10V              | $T_A = +25^{\circ}C$<br>$T_A = T_{MIN}$ to $T_{MAX}$ | -0.5 | 0.01  | 0.5 | nA       |
| COM Off-Leakage Current<br>(Notes 6, 9)                                          | $I_{COM\_}(OFF)$                | $V_{COM\_} = 10V, 1V;$<br>$V_{NO\_}$ or $V_{NC\_} = 1V,$<br>10V              | $T_A = +25^{\circ}C$<br>$T_A = T_{MIN}$ to $T_{MAX}$ | -0.5 | 0.01  | 0.5 | nA       |
| COM On-Leakage Current<br>(Notes 6, 9)                                           | $I_{COM\_}(ON)$                 | $V_{COM\_} = 1V, 10V;$<br>$V_{NO\_}$ or $V_{NC\_} = 1V,$<br>10V, or floating | $T_A = +25^{\circ}C$<br>$T_A = T_{MIN}$ to $T_{MAX}$ | -1   | 0.01  | 1   | nA       |
| LOGIC INPUT                                                                      |                                 |                                                                              |                                                      |      |       |     |          |
| Input Current with<br>Input Voltage High                                         | $I_{IN\_H}$                     | $IN\_ = 2.4V,$ all others = 0.8V                                             |                                                      | -0.5 | 0.001 | 0.5 | $\mu A$  |
| Input Current with<br>Input Voltage Low                                          | $I_{IN\_L}$                     | $IN\_ = 0.8V,$ all others = 2.4V                                             |                                                      | -0.5 | 0.001 | 0.5 | $\mu A$  |
| Logic Input Voltage High                                                         | $V_{IN\_H}$                     |                                                                              |                                                      | 2.4  |       |     | V        |
| Logic Input Voltage Low                                                          | $V_{IN\_L}$                     |                                                                              |                                                      |      |       | 0.8 | V        |

# 2.5Ω, Dual, SPST, CMOS Analog Switches

MAX4667/MAX4668/MAX4669

## ELECTRICAL CHARACTERISTICS—Single Supply (continued)

(V<sub>+</sub> = +12V, V<sub>-</sub> = 0, V<sub>L</sub> = +5V, V<sub>IN\_H</sub> = +2.4V, V<sub>IN\_L</sub> = +0.8V, T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C.) (Note 2)

| PARAMETER                              | SYMBOL           | CONDITIONS                                                                   |                                                       | MIN  | TYP   | MAX  | UNITS |
|----------------------------------------|------------------|------------------------------------------------------------------------------|-------------------------------------------------------|------|-------|------|-------|
| POWER SUPPLY                           |                  |                                                                              |                                                       |      |       |      |       |
| Power-Supply Range                     |                  |                                                                              |                                                       | 4.5  |       | 36.0 | V     |
| Positive Supply Current                | I <sub>+</sub>   | V <sub>IN</sub> = 0 or 5V                                                    | T <sub>A</sub> = +25°C                                | -0.5 | 0.001 | 0.5  | μA    |
|                                        |                  |                                                                              | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | -5   |       | 5    |       |
| Logic Supply Current                   | I <sub>L</sub>   | V <sub>IN</sub> = 0 or 5V                                                    | T <sub>A</sub> = +25°C                                | -0.5 | 0.001 | 0.5  | μA    |
|                                        |                  |                                                                              | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | -5   |       | 5    |       |
| Ground Current                         | I <sub>GND</sub> | V <sub>IN</sub> = 0 or 5V                                                    | T <sub>A</sub> = +25°C                                | -0.5 | 0.001 | 0.5  | μA    |
|                                        |                  |                                                                              | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> | -5   |       | 5    |       |
| SWITCH DYNAMIC CHARACTERISTICS         |                  |                                                                              |                                                       |      |       |      |       |
| Turn-On Time (Note 3)                  | t <sub>ON</sub>  | V <sub>COM_</sub> = 10V, Figure 2                                            | T <sub>A</sub> = +25°C                                |      | 200   | 400  | ns    |
|                                        |                  |                                                                              | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> |      |       | 500  |       |
| Turn-Off Time (Note 3)                 | t <sub>OFF</sub> | V <sub>COM_</sub> = 10V, Figure 2                                            | T <sub>A</sub> = +25°C                                |      | 100   | 200  | ns    |
|                                        |                  |                                                                              | T <sub>A</sub> = T <sub>MIN</sub> to T <sub>MAX</sub> |      |       | 300  |       |
| Break-Before-Make Time Delay (MAX4669) |                  | V <sub>COM</sub> = 10V                                                       |                                                       | 10   |       |      | ns    |
| Charge Injection                       | Q                | C <sub>L</sub> = 1.0nF, V <sub>GEN</sub> = 0, R <sub>GEN</sub> = 0, Figure 3 |                                                       |      | 50    |      | pC    |
| Crosstalk (Note 8)                     | V <sub>CT</sub>  | R <sub>L</sub> = 50Ω, C <sub>L</sub> = 5pF, f = 1MHz, Figure 5               |                                                       |      | -66   |      | dB    |
| NC or NO Capacitance                   | C <sub>OFF</sub> | f = 1MHz, Figure 6, T <sub>A</sub> = +25°C                                   |                                                       |      | 105   |      | pF    |
| COM Off-Capacitance                    | C <sub>COM</sub> | f = 1MHz, Figure 6, T <sub>A</sub> = +25°C                                   |                                                       |      | 105   |      | pF    |
| On-Capacitance                         | C <sub>COM</sub> | f = 1MHz, Figure 7, T <sub>A</sub> = +25°C                                   |                                                       |      | 185   |      | pF    |

**Note 2:** The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.

**Note 3:** Guaranteed by design.

**Note 4:** ΔRON = RON(MAX) - RON(MIN).

**Note 5:** Flatness is defined as the difference between the maximum and minimum values of on-resistance as measured over the specified analog signal range.

**Note 6:** Leakage parameters are 100% tested at maximum-rated hot temperature and guaranteed by correlation at +25°C.

**Note 7:** Off-isolation = 20log<sub>10</sub> [V<sub>COM</sub> / (V<sub>NC</sub> or V<sub>NO</sub>)], V<sub>COM</sub> = output, V<sub>NC</sub> or V<sub>NO</sub> = input to off switch.

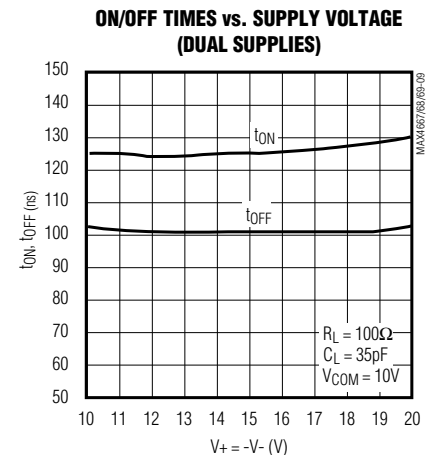
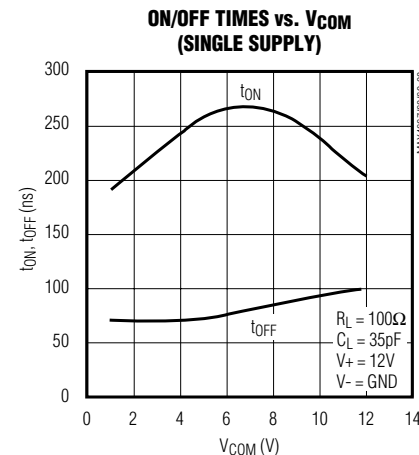
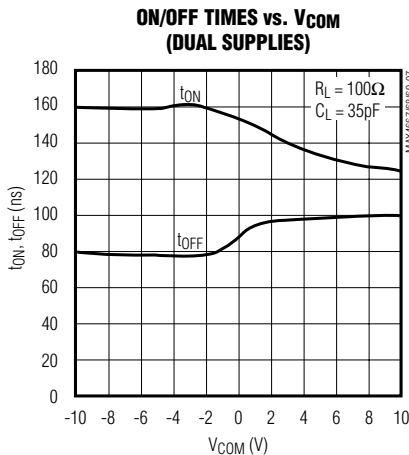
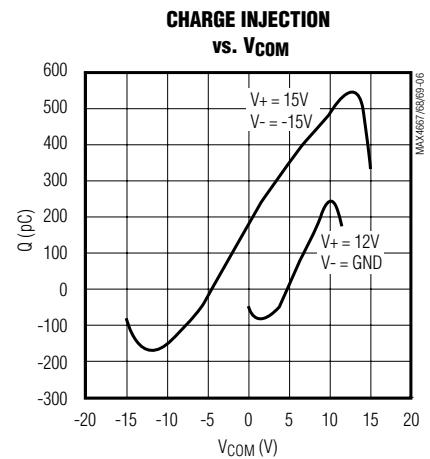
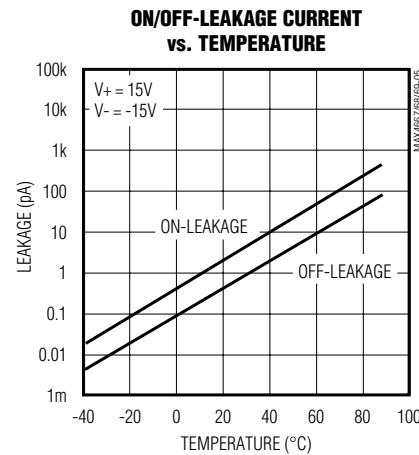
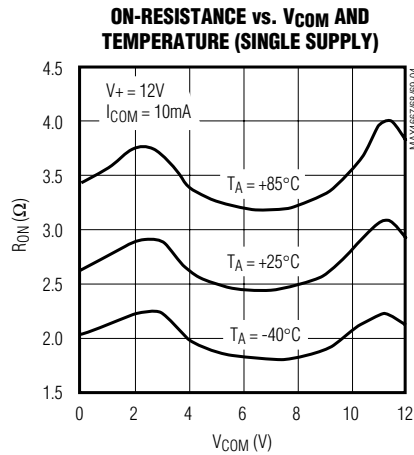
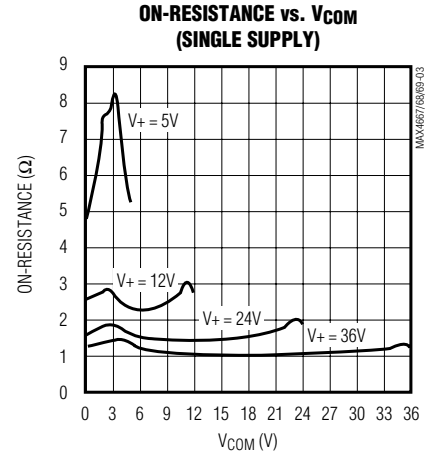
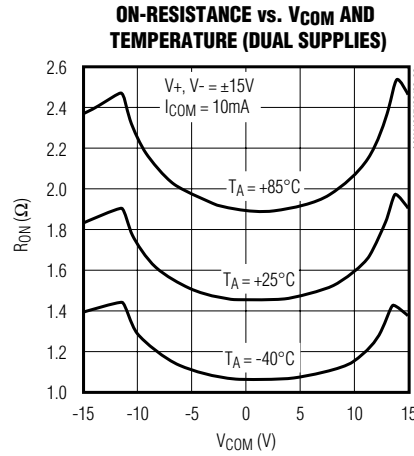
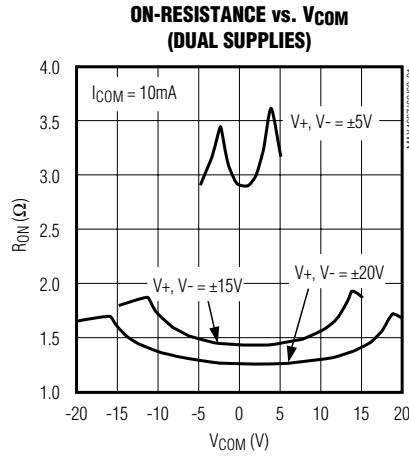
**Note 8:** Between any two switches.

**Note 9:** Leakage testing at single supply is guaranteed by testing with dual supplies.

# 2.5 $\Omega$ , Dual, SPST, CMOS Analog Switches

## Typical Operating Characteristics

( $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

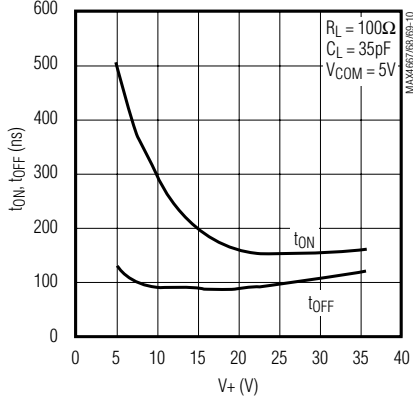


# 2.5 $\Omega$ , Dual, SPST, CMOS Analog Switches

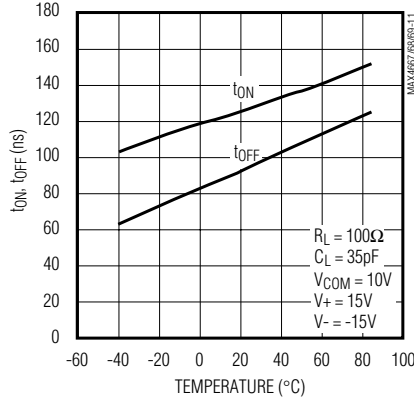
## Typical Operating Characteristics (continued)

( $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

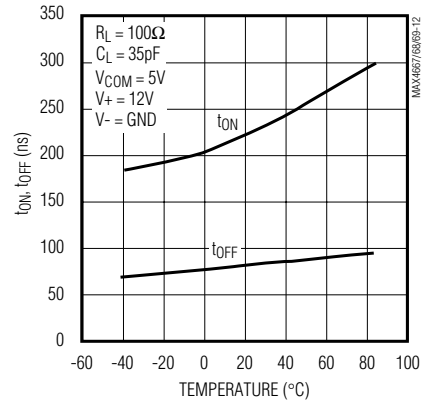
**ON/OFF TIMES vs. SUPPLY VOLTAGE  
(SINGLE SUPPLY)**



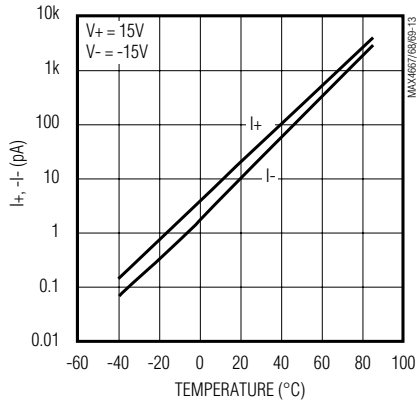
**ON/OFF TIMES vs. TEMPERATURE  
(DUAL SUPPLIES)**



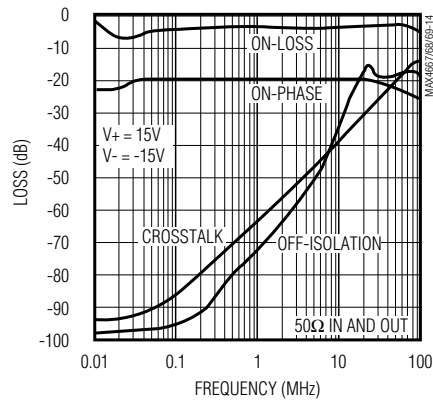
**ON/OFF TIMES vs. TEMPERATURE  
(SINGLE SUPPLY)**



**SUPPLY CURRENT  
vs. TEMPERATURE**



**FREQUENCY RESPONSE**



MAX4667/MAX4668/MAX4669

## 2.5Ω, Dual, SPST, CMOS Analog Switches

### Pin Description

| PIN                |                    |                    | NAME           | FUNCTION                                                                                              |
|--------------------|--------------------|--------------------|----------------|-------------------------------------------------------------------------------------------------------|
| MAX4667            | MAX4668            | MAX4669            |                |                                                                                                       |
| 1, 3, 6, 8, 10, 15 | 1, 3, 6, 8, 10, 15 | 1, 3, 6, 8, 10, 15 | N.C.           | No Connection. Not internally connected. Connect to GND as low impedance to improve on/off-isolation. |
| 2, 7               | 2, 7               | 2, 7               | IN2, IN2       | Logic-Control Digital Inputs                                                                          |
| 4                  | 4                  | 4                  | V-             | Negative Analog Supply Voltage Input. Connect to GND for single-supply operation.                     |
| 5                  | 5                  | 5                  | GND            | Ground                                                                                                |
| 12                 | 12                 | 12                 | V <sub>L</sub> | Logic Supply Input                                                                                    |
| 13                 | 13                 | 13                 | V+             | Positive Analog Supply Voltage Input                                                                  |
| 14, 11             | 14, 11             | 14, 11             | COM1, COM2     | Analog Switch, Common Terminals                                                                       |
| 16, 9              | —                  | —                  | NC1, NC2       | Analog Switch, Normally Closed Terminals                                                              |
| —                  | 16, 9              | —                  | NO1, NO2       | Analog Switch, Normally Open Terminals                                                                |
| —                  | —                  | 9                  | NC1            | Analog Switch, Normally Closed Terminal                                                               |
| —                  | —                  | 16                 | NO1            | Analog Switch, Normally Open Terminal                                                                 |

### Applications Information

#### Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings because stresses beyond the listed ratings can cause permanent damage to the devices. Always sequence V<sub>+</sub> on first, then V<sub>-</sub>, followed by the logic inputs, NO, or COM. If power-supply sequencing is not possible, add two small signal diodes (D1, D2) in series with supply pins (Figure 1). Adding diodes reduces the analog signal range to one diode drop below V<sub>+</sub> and one diode drop above V<sub>-</sub>, but does not affect the devices' low switch resistance and low leakage characteristics. Device operation is unchanged, and the difference between V<sub>+</sub> and V<sub>-</sub> should not exceed 44V. These protection diodes are not recommended when using a single supply.

#### Off-Isolation at High Frequencies

With the N.C. pins connected to GND, the high-frequency on-response of these parts extends from DC to above 100MHz with a typical loss of -2dB. When the switch is turned off, however, it behaves like a capacitor, and off-isolation decreases with increasing frequency. (Above 300MHz, the switch actually passes more signal turned off than turned on.) This effect is more pronounced with higher source and load impedances.

Above 5MHz, circuit-board layout becomes critical and it becomes difficult to characterize the response of the switch independent of the circuit. The graphs shown in the *Typical Operating Characteristics* were taken using a 50Ω source and load connected with BNC connectors to a circuit board deemed "average"; that is, designed with isolation in mind, but not using stripline or other special RF circuit techniques. For critical applications above 5MHz, use the MAX440, MAX441, and MAX442, which are fully characterized up to 160MHz.

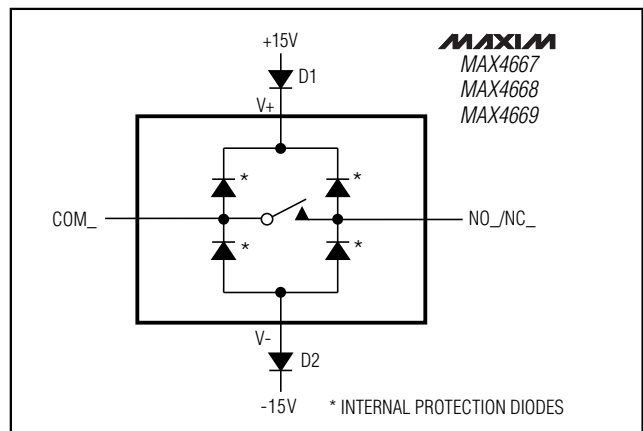


Figure 1. Overvoltage Protection Using External Blocking Diodes

# 2.5Ω, Dual, SPST, CMOS Analog Switches

## Test Circuits/Timing Diagrams

MAX4667/MAX4668/MAX4669

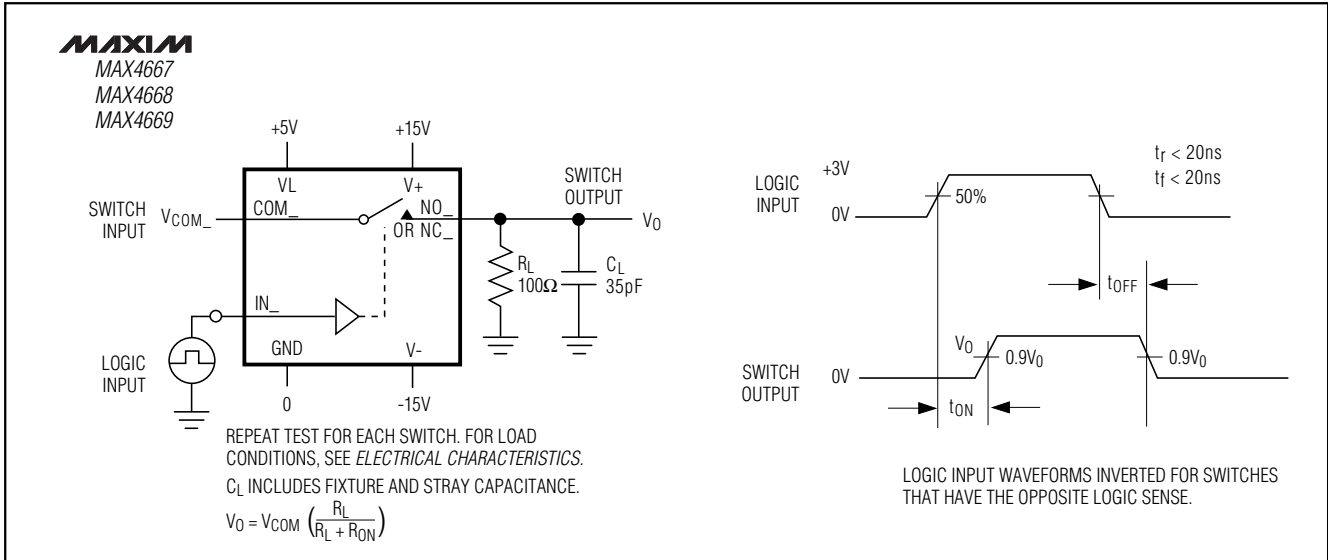


Figure 2. Switching-Time Test Circuit

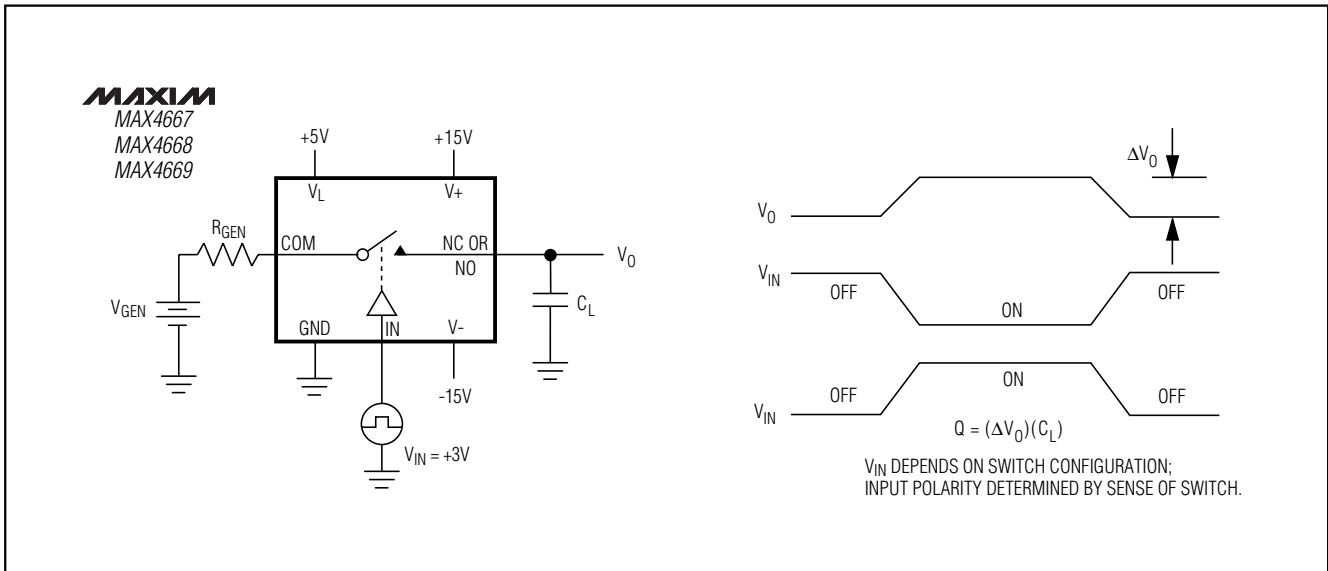


Figure 3. Charge-Injection Test Circuit

# 2.5Ω, Dual, SPST, CMOS Analog Switches

## Test Circuits/Timing Diagrams (continued)

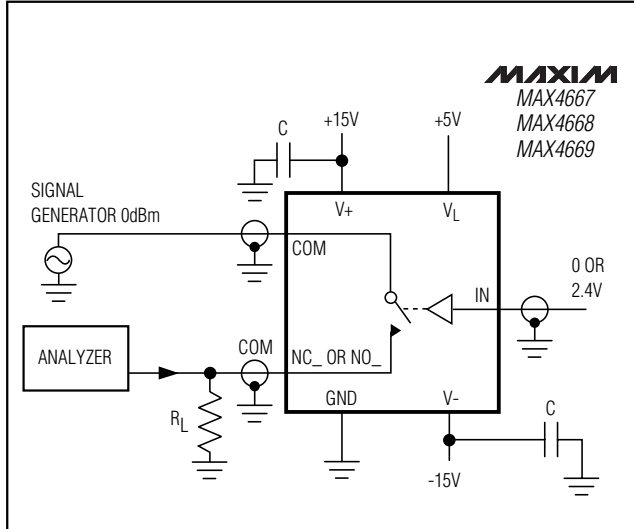


Figure 4. Off-Isolation Test Circuit

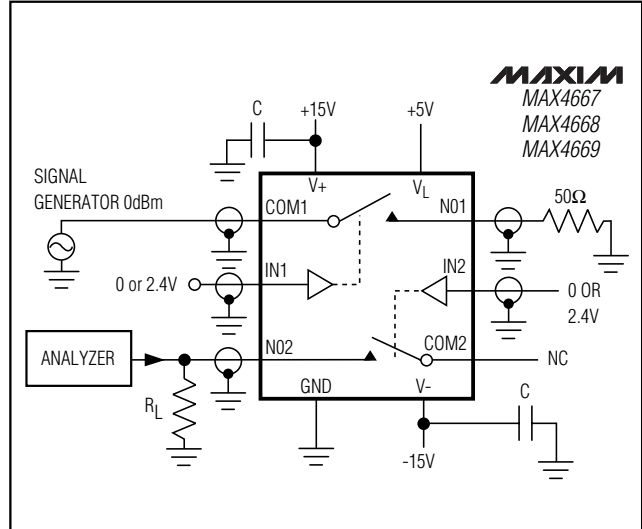


Figure 5. Crosstalk Test Circuit

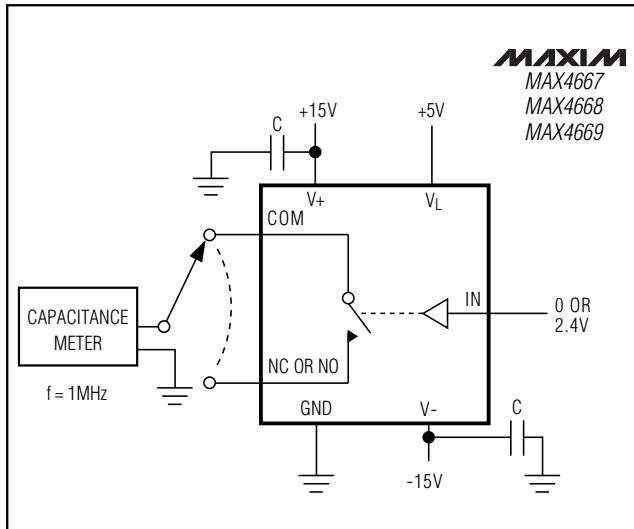


Figure 6. Switch Off-Capacitance Test Circuit

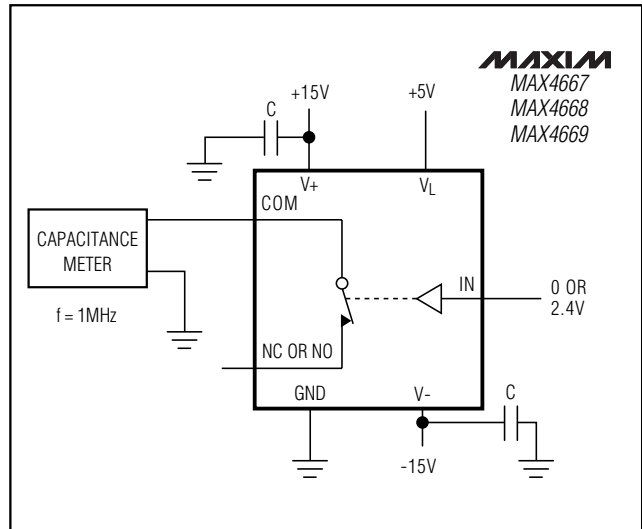


Figure 7. Switch On-Capacitance Test Circuit

# 2.5Ω, Dual, SPST, CMOS Analog Switches

MAX4667/MAX4668/MAX4669

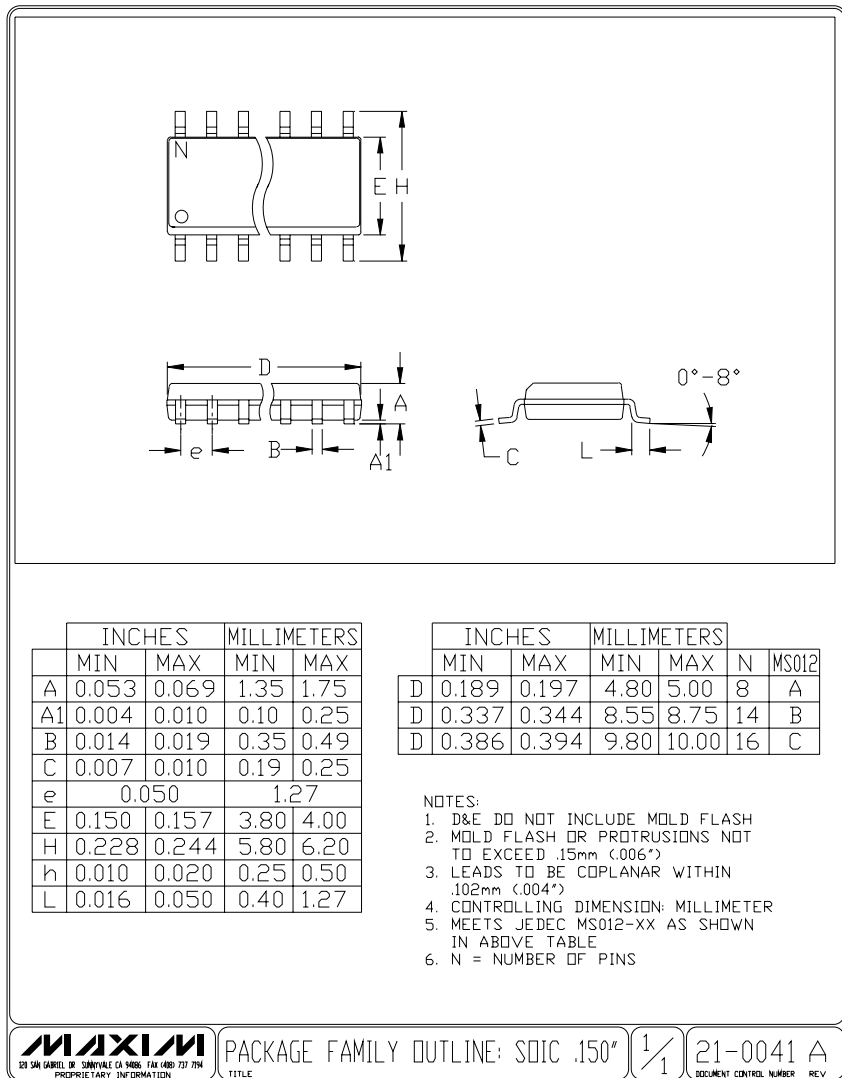
## Ordering Information (continued)

| PART       | TEMP. RANGE    | PIN-PACKAGE    |
|------------|----------------|----------------|
| MAX4668CSE | 0°C to +70°C   | 16 Narrow SO   |
| MAX4668CPE | 0°C to +70°C   | 16 Plastic DIP |
| MAX4668ESE | -40°C to +85°C | 16 Narrow SO   |
| MAX4668EPE | -40°C to +85°C | 16 Plastic DIP |
| MAX4669CSE | 0°C to +70°C   | 16 Narrow SO   |
| MAX4669CPE | 0°C to +70°C   | 16 Plastic DIP |
| MAX4669ESE | -40°C to +85°C | 16 Narrow SO   |
| MAX4669EPE | -40°C to +85°C | 16 Plastic DIP |

## Chip Information

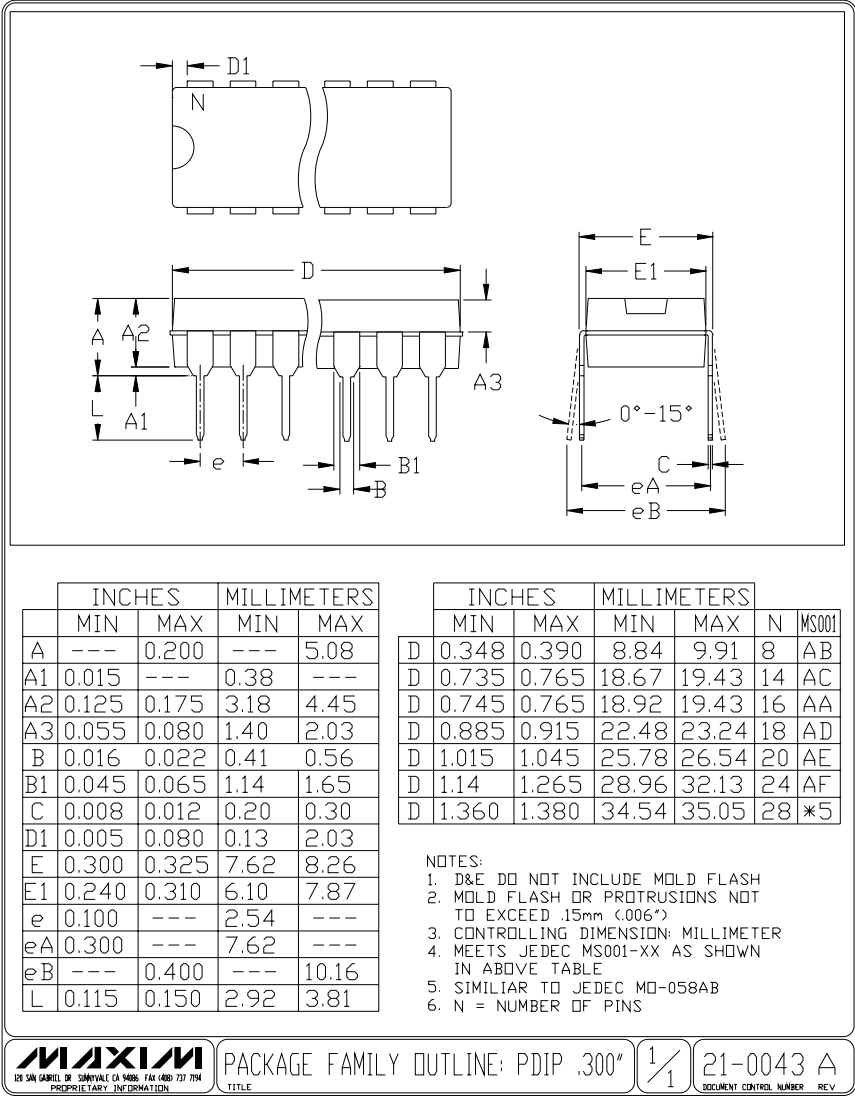
TRANSISTOR COUNT: 108

## Package Information



2.5Ω, Dual, SPST,  
CMOS Analog Switches

Package Information (continued)



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