

FEATURES

General

- Low power HDMI transmitter ideal for portable applications
- CEC controller and expanded message buffer (3 messages) reduces system overhead
- HDMI Version 1.4a features supported
- 3D video
- Extended colorimetry

Compatible with DVI 1.0

- Optional embedded HDCP keys to support HDCP 1.3
- Video/audio inputs accept logic levels from 1.8 V to 3.3 V

Digital video

- 150 MHz operation supports all video and graphics resolutions from 480i to 1080p
- Programmable 2-way color-space converter
- Supports RGB, YCrCb, and DDR
- Supports ITU656-based embedded syncs
- Automatic input video format timing detection (CEA-861E)

Digital audio

- Supports standard S/PDIF for stereo LPCM or compressed audio up to 192 kHz
- 2-channel, uncompressed LPCM I²S audio up to 192 kHz

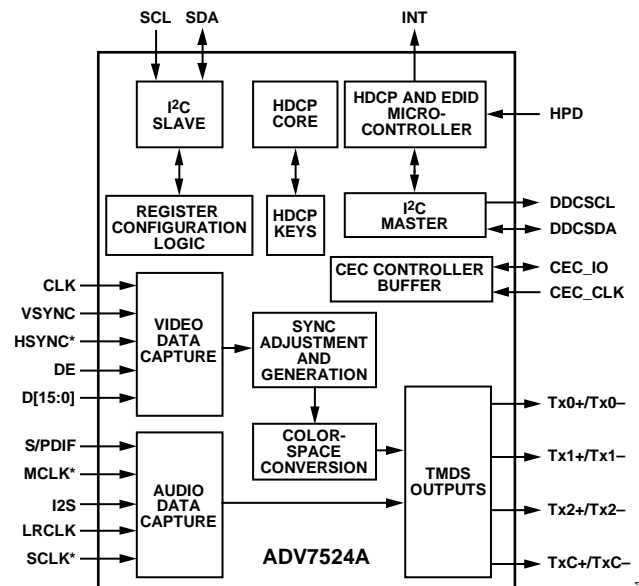
Special features for easy system design

- On-chip MPU with I²C master to perform EDID reading and HDCP operations; reports HDMI events through interrupts and registers
- 5 V tolerant I²C and HPD I/Os, no extra device needed
- No audio master clock needed for supporting S/PDIF and I²S
- Compatible with AD9394 HDMI companion chip
- 5 V generator for Hot Plug detection in portable applications

APPLICATIONS

Cellular handsets
 Digital video cameras
 Digital still cameras
 Personal media players
 Gaming
 DVD players and recorders
 Digital set-top boxes
 HDMI repeater

FUNCTIONAL BLOCK DIAGRAM



*BOTH HSYNC AND SCLK CAN BE RECONFIGURED AS MCLK.

Figure 1.

GENERAL DESCRIPTION

The ADV7524A is a 150 MHz, High-Definition Multimedia Interface (HDMI®) transmitter with expanded CEC buffer. It supports HDTV formats up to 1080p and computer graphic resolutions up to SXGA at 75 Hz.

With the optional inclusion of embedded HDCP keys, the ADV7524A allows the secure transmission of protected content, as specified by the HDCP 1.3 protocol.

The ADV7524A supports 3D video and extended colorimetry.

The ADV7524A supports both S/PDIF and 2-channel I²S audio. Its high fidelity, 2-channel I²S can transmit stereo up to a 192 kHz sampling rate. The S/PDIF can carry stereo LPCM audio or compressed audio including Dolby® digital and DTS®.

The ADV7524A helps to reduce system design complexity and cost by incorporating such features as an I²C master for EDID reading, and 5 V tolerance on the I²C and Hot Plug™ detect pins.

Fabricated in an advanced CMOS process, the ADV7524A is available in a space saving, 49-ball, WLCSP surface-mount package. This package is RoHS compliant and specified to operate from -25°C to +85°C.

For more information on the ADV7524A, contact Analog Devices, Inc., at: ATV_VideoTx_Apps@analog.com.

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NOTES

I²C refers to a communications protocol originally developed by Philips Semiconductors (now NXP Semiconductors).
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