

LT3460/LT3460-1

1.3 MHz/650kHz Step-Up DC/DC Converter in SC70, ThinSOT and DFN DESCRIPTION

FEATURES

- 1.3MHz (LT3460) Switching Frequency
- 650kHz (LT3460-1) Switching Frequency
- High Output Voltage: Up to 36V
- 300mA Integrated Switch (LT3460)
- 180mA Integrated Switch (LT3460-1)
- Wide Input Range: 2.5V to 16V
- Uses Small Surface Mount Components
- Low Shutdown Current: <1µA
- Low Profile (1mm) SC70 (LT3460 and LT3460-1). SOT-23 (ThinSOT™) (LT3460) and 2mm × 2mm DFN (LT3460-1) Packages

APPLICATIONS

- **Digital Cameras**
- CCD Bias Supply
- **XDSL Power Supply**
- **TFT-LCD Bias Supply**
- Local 5V or 12V Supply
- Medical Diagnostic Equipment
- Battery Backup

The LT®3460/LT3460-1 are general purpose step-up DC/ DC converters. The LT3460/LT3460-1 switch at 1.3MHz/ 650kHz, allowing the use of tiny, low cost and low height capacitors and inductors. The constant frequency results in low, predictable output noise that is easy to filter.

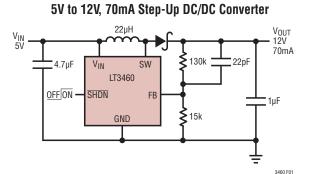
The high voltage switches in the LT3460/LT3460-1 are rated at 38V, making the device ideal for boost converters up to 36V. The LT3460 can generate 12V at up to 70mA from a 5V supply.

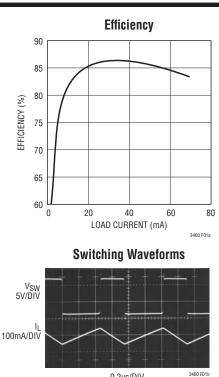
The low 1mA quiescent current and 650kHz switching frequency of LT3460-1 make it ideal for lower current applications.

The LT3460 is available in SC70 and SOT-23 packages. The LT3460-1 is available in SC70 and 2mm × 2mm DFN packages.

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TYPICAL APPLICATION





0.2µs/DIV

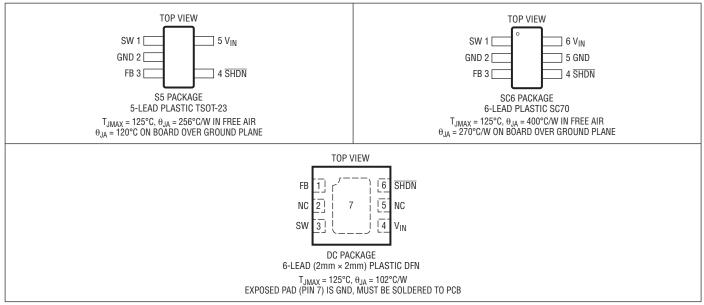
LT3460/LT3460-1

ABSOLUTE MAXIMUM RATINGS (Note 1)

Input Voltage (V _{IN})	16V
SW Voltage	
FB Voltage	.5V
SHDN Voltage	16V

Operating Ambient	
Temperature Range (Note 2)	40°C to 85°C
Maximum Junction Temperature	125°C
Storage Temperature Range	–65°C to 150°C
Lead Temperature (Soldering, 10 sec)	300°C
Storage Temperature Range	–65°C to 150°C

PIN CONFIGURATION



ORDER INFORMATION

LEAD FREE FINISH	TAPE AND REEL	PART MARKING	PACKAGE DESCRIPTION	TEMPERATURE RANGE
LT3460ES5#PBF	LT3460ES5#TRPBF	LTB1	5-Lead Plastic TSOT-23	-40°C to 85°C
LT3460ESC6#PBF	LT3460ESC6#TRPBF	LAAF	6-Lead Plastic SC70	-40°C to 85°C
LT3460ESC6-1#PBF	LT3460ESC6-1#TRPBF	LDJV	6-Lead Plastic SC70	-40°C to 85°C
LT3460EDC-1#PBF	LT3460EDC-1#TRPBF	LDNB	6-Lead (2mm × 2mm) Plastic DFN	-40°C to 85°C

Consult LTC Marketing for parts specified with wider operating temperature ranges. Consult LTC Marketing for information on non-standard lead based finish parts.

For more information on lead free part marking, go to: http://www.linear.com/leadfree/

For more information on tape and reel specifications, go to: http://www.linear.com/tapeandreel/



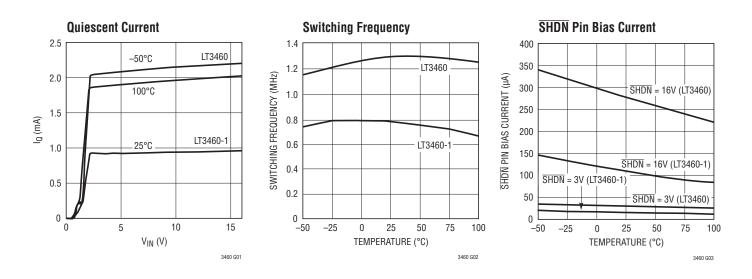
ELECTRICAL CHARACTERISTICS The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C, V_{IN} = 3V, V_{SHDN} = 3V, unless otherwise noted.

		- ,	LT3460				LT3460-1			
PARAMETER	CONDITIONS		MIN	ТҮР	MAX	MIN	ТҮР	MAX	UNITS	
Minimum Operating Voltage			2.5			2.5			V	
Maximum Operating Voltage					16			16	V	
Feedback Voltage		•	1.235 1.225	1.255	1.275 1.280	1.235 1.225	1.255	1.275 1.280	V V	
Feedback Line Regulation	2.5V < V _{IN} < 16V			0.015			0.015		%/V	
FB Pin Bias Current		•	5	25	80	0	25	80	nA	
Supply Current	SHDN = 0V			2.0 0.1	3.0 0.5		1.0 0.1	1.5 0.5	mA μA	
Switching Frequency			1.0	1.3	1.7	0.35	0.65	1.0	MHz	
Maximum Duty Cycle			85	90		80	90		%	
Switch Current Limit			300	420	600	180	260	380	mA	
Switch V _{CESAT}	I _{SW} = 250mA (LT3460), I _{SW} = 100mA (LT3460-1)			320	450		220	350	mV	
Switch Leakage Current	V _{SW} = 5V			0.01	1		0.01	1	μA	
SHDN Voltage High			1.5			1.5			V	
SHDN Voltage Low					0.4			0.4	V	
SHDN Pin Bias Current				40			15		μA	

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

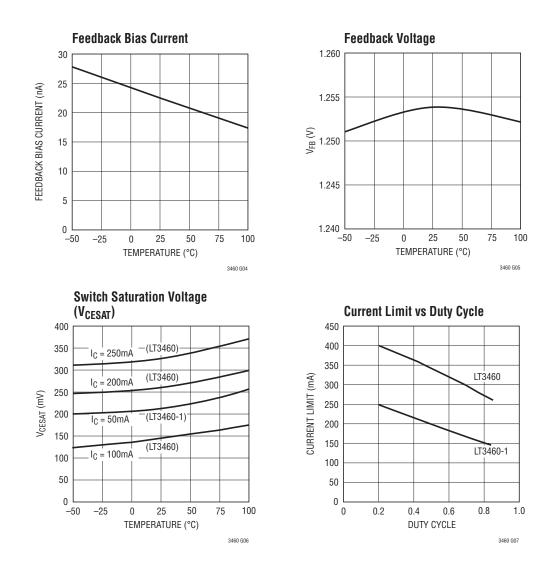
Note 2: The LT3460E/LT3460-1E is guaranteed to meet specifications from 0°C to 70°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

TYPICAL PERFORMANCE CHARACTERISTICS





TYPICAL PERFORMANCE CHARACTERISTICS



PIN FUNCTIONS (ThinSOT/SC70/DFN Packages)

SW (Pin 1/Pin 1/Pin 3): Switch Pin. Connect inductor/diode here. Minimize trace at this pin to reduce EMI.

GND (Pin 2/Pins 2 and 5/Exposed Pad Pin 7): Ground Pin. Tie directly to local ground plane.

FB (**Pin 3/Pin 3/Pin 1**): Feedback Pin. Reference voltage is 1.255V. Connect resistor divider tap here. Minimize trace area at FB. Set V_{OUT} according to $V_{OUT} = 1.255V (1 + R1/R2)$.

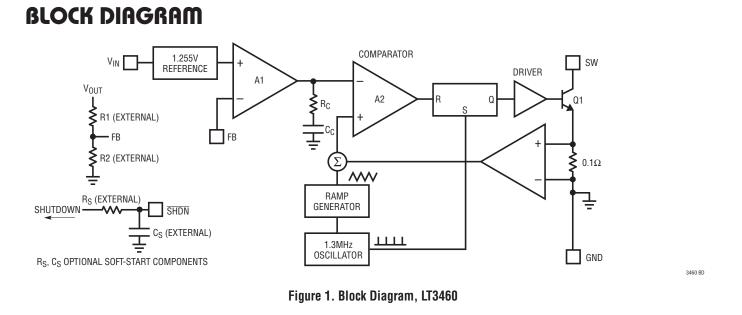
SHDN (Pin 4/Pin 6): Shutdown Pin. Tie to 1.5V or higher to enable device; 0.4V or less to disable device. Also functions as soft-start. Use RC filter (47k, 47nF typ) as shown in Figure 1.

V_{IN} (**Pin 5/Pin 6/Pin 4):** Input Supply Pin. Must be locally bypassed.

NC (NA/NA/Pins 2, 5): No-Connects. These pins are not connected to internal circuitry. They should be tied to ground to improve thermal and electrical performance.







OPERATION

The LT3460/LT3460-1 uses a constant frequency, current mode control scheme to provide excellent line and load regulation. Operation can be best understood by referring to the block diagram in Figure 1. At the start of each oscillator cycle, the SR latch is set, which turns on the power switch Q1. A voltage proportional to the switch current is added to a stabilizing ramp and the resulting sum is fed into the positive terminal of the PWM comparator A2. When this voltage exceeds the level at the negative input of A2, the SR latch is reset turning off the power switch. The level at the negative input of A2 is set by the error amplifier A1, and is simply an amplified version of the difference between the feedback voltage and the reference voltage of 1.255V. In this manner, the error amplifier sets the correct peak current level to keep the output in regulation. If the error amplifier's output increases, more current is delivered to the output; if it decreases, less current is delivered.

Feedback Loop Compensation

The LT3460/LT3460-1 has an internal feedback compensation network as shown in Figure 1 (R_C and C_C). However, because the small signal characteristics of a boost converter change with operation conditions, the internal compensation network cannot satisfy all applications. A properly designed external feed forward capacitor from V_{OUT} to

FB (C_F in Figure 2) will correct the loop compensation for most applications.

The LT3460/LT3460-1 uses peak current mode control. The current feedback makes the inductor very similar to a current source in the medium frequency range. The power stage transfer function in the medium frequency range can be approximated as:

$$G_{P(s)} = \frac{K1}{s \bullet C2}$$

where C2 is the output capacitance, and K1 is a constant based on the operating point of the converter. In continuous current mode, K1 increases as the duty cycle decreases.

The internal compensation network $R_{C},\ C_{C}$ can be approximated as follows in medium frequency range:

$$G_{C(s)} = K2 \bullet \frac{s \bullet R_C \bullet C_C + 1}{s \bullet C_C}$$

The zero

$$f_Z = \frac{1}{2 \bullet \pi \bullet R_C \bullet C_C}$$

is about 70kHz.



OPERATION

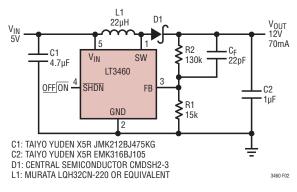


Figure 2. 5V to 12V Step-Up Converter

The feedback loop gain $T(s) = K3 \cdot G_P(s) \cdot G_C(s)$. If it crosses over 0dB far before f_Z , the phase margin will be small. Figure 3 is the Bode plot of the feedback loop gain measured from the converter shown in Figure 2 without the feedforward capacitor C_F . The result agrees with the previous discussion: Phase margin of about 20° is insufficient.

In order to improve the phase margin, a feed-forward capacitor C_F in Figure 2 can be used.

Without the feed-forward capacitor, the transfer function from V_{OUT} to FB is:

$$\frac{FB}{V_{OUT}} = \frac{R1}{R1 + R2}$$

With the feed-forward capacitor C_{F} , the transfer function becomes:

$$\frac{FB}{V_{OUT}} = \frac{R1}{R1 + R2} \bullet \frac{s \bullet R2 \bullet C_F + 1}{s \bullet \frac{R1 \bullet R2}{R1 + R2}} \bullet C_F + 1$$

The feed-forward capacitor C_F generates a zero and a pole. The zero always appears before the pole. The frequency distance between the zero and the pole is determined only by the ratio between V_{OUT} and FB. To give maximum phase margin, C_F should be chosen so that the midpoint frequency between the zero and the pole is at the cross over frequency.

With $C_F = 20 pF$, the feedback loop Bode plot is reshaped as shown in Figure 4. The phase margin is about 60°.

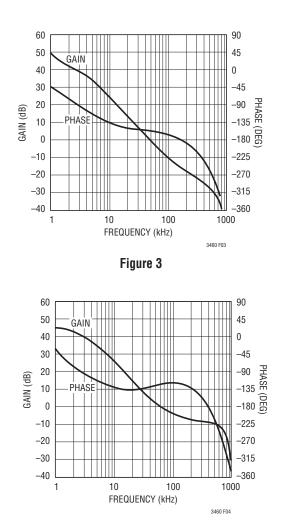


Figure 4

The feed-forward capacitor increases the gain at high frequency. The feedback loop therefore needs to have enough attenuation at the switching frequency to reject the switching noise. Additional internal compensation components have taken this into consideration.

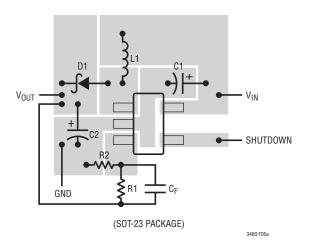
For most of the applications of LT3460/LT3460-1, the output capacitor ESR zero is at very high frequency and can be ignored. If a low frequency ESR zero exists, for example, when a high-ESR Tantalum capacitor is used at the output, the phase margin may be enough even without a feed-forward capacitor. In these cases, the feed-forward capacitor should not be added because it may cause the feedback loop to not have enough attenuation at the switching frequency.

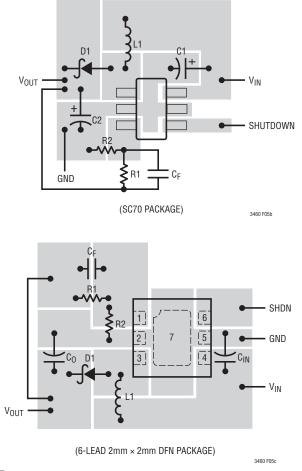


OPERATION

Layout Hints

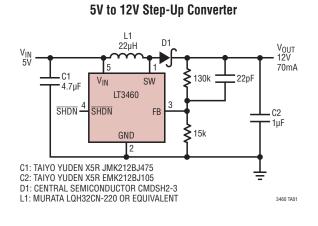
The high speed operation of the LT3460/LT3460-1 demands careful attention to board layout. You will not get advertised performance with careless layout. Figure 5 shows the recommended component placement.

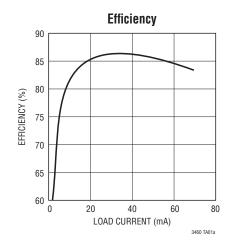






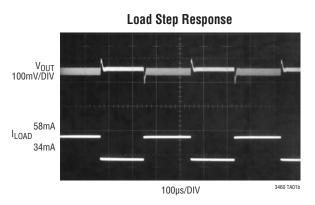
TYPICAL APPLICATIONS



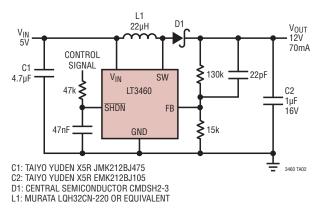




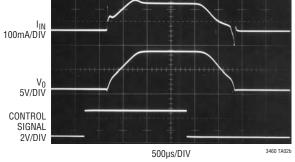
TYPICAL APPLICATIONS



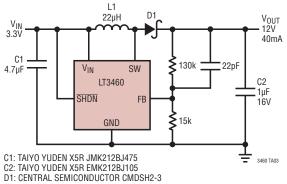
5V to 12V with Soft-Start Circuit



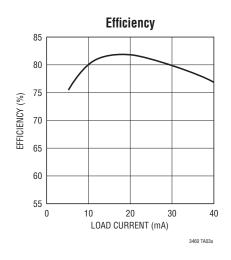
Input Current and Output Voltage



5V to 12V Step-Up Converter



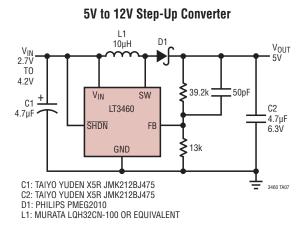
L1: MURATA LQH32CN-220 OR EQUIVALENT

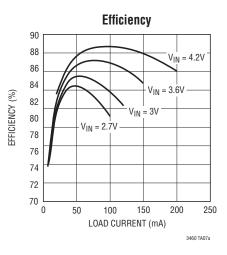




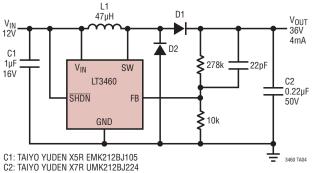
LT3460/LT3460-1

TYPICAL APPLICATIONS





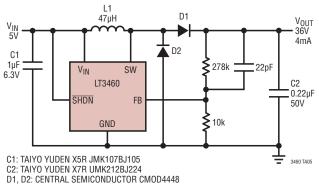
12V to 36V Step-Up Converter



D1, D2: CENTRAL SEMICONDUCTOR CMOD4448

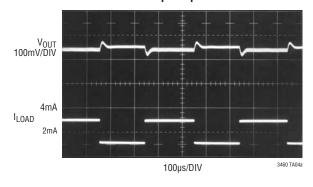
L1: TAIYO YUDEN LB2012

5V to 36V Step-Up Converter

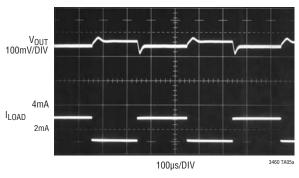


L1: TAIYO YUDEN LB2012

Load Step Response

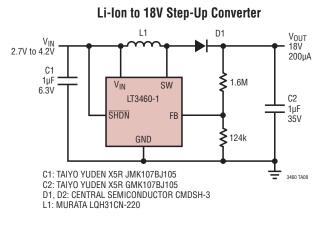






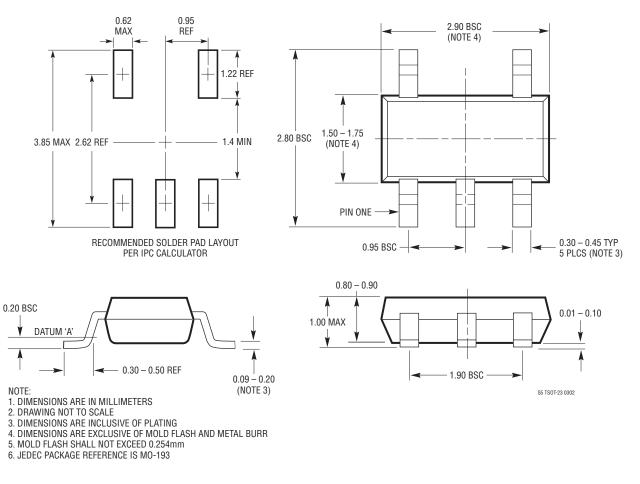


APPLICATIONS INFORMATION



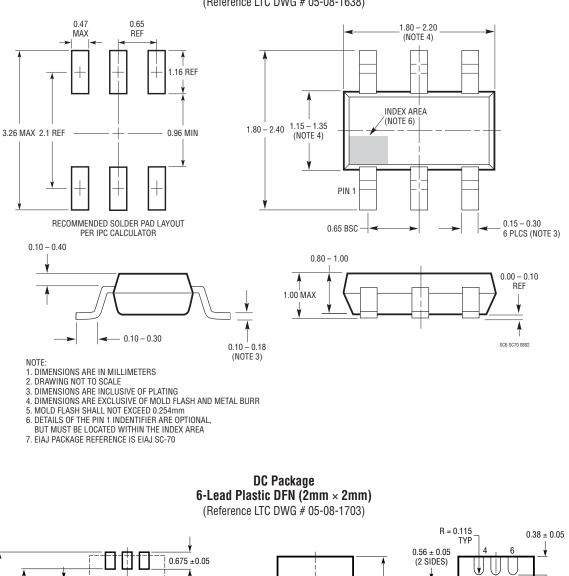
PACKAGE DESCRIPTION

S5 Package 5-Lead Plastic TSOT-23 (Reference LTC DWG # 05-08-1635)

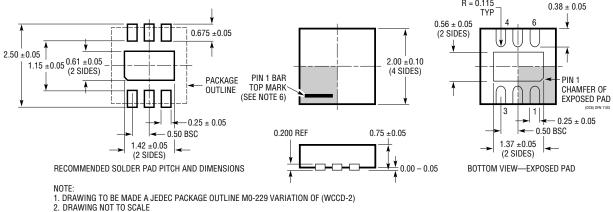




PACKAGE DESCRIPTION



SC6 Package 6-Lead Plastic SC70 (Reference LTC DWG # 05-08-1638)



3. ALL DIMENSIONS ARE IN MILLIMETERS

4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE

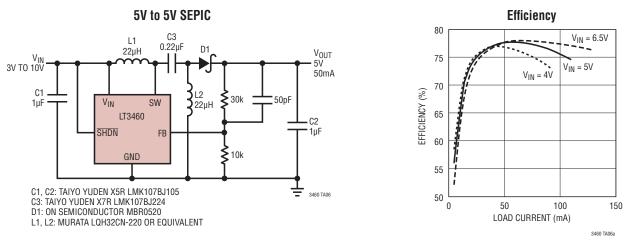
- MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE 5. EXPOSED PAD SHALL BE SOLDER PLATED
- 6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON THE

TOP AND BOTTOM OF PACKAGE



Information furnished by Linear Technology Corporation is believed to be accurate and reliable. However, no responsibility is assumed for its use. Linear Technology Corporation makes no representation that the interconnection of its circuits as described herein will not infringe on existing patent rights.

TYPICAL APPLICATIONS



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1613	550mA (I _{SW}), 1.4MHz, High Efficiency Step-Up DC/DC Converter	V_{IN} : 0.9V to 10V, $V_{OUT(MAX)}$ = 34V, I_Q = 3mA, $I_{SD}{<}1\mu\text{A},$ ThinSOT Package
LT1615/LT1615-1	300mA/80mA (I _{SW}), Constant Off-Time, High Efficiency Step- Up DC/DC Converter	V_{IN} : 1.2V to 15V, $V_{\text{OUT}(\text{MAX})}$ = 34V, I_{Q} = 20µA, $I_{\text{SD}} < 1\mu\text{A},$ ThinSOT Package
LT1944/LT1944-1	Dual Output 350mA/100mA (I _{SW}), Constant Off-Time, High Efficiency Step-Up DC/DC Converter	V_{IN} : 1.2V to 15V, $V_{\text{OUT}(\text{MAX})}$ = 34V, I_{Q} = 20µA, $I_{\text{SD}}{<}1\mu\text{A},$ MS Package
LT1945	Dual Output, Pos/Neg, 350mA (I _{SW}), Constant Off-Time, High Efficiency Step-Up DC/DC Converter	$V_{IN}\!\!:$ 1.2V to 15V, $V_{OUT(MAX)}$ = ±34V, I_Q = 20µA, $I_{SD}\!<\!1\mu\text{A},$ MS Package
LT1961	1.5A (I _{SW}), 1.25MHz, High Efficiency Step-Up DC/DC Converter	V_{IN} : 3V to 25V, $V_{\text{OUT}(\text{MAX})}$ = 35V, I_{Q} = 0.9mA, $I_{\text{SD}}{<}6\mu\text{A},$ MS8E Package
LTC3400/LTC3400B	600mA (I _{SW}), 1.2MHz, Synchronous Step-Up DC/DC Converter	V_{IN} : 0.85V to 5V, $V_{\text{OUT}(\text{MAX})}$ = 5V, I_{Q} = 19µA/300µA, $I_{\text{SD}}{<}1\mu\text{A},$ ThinSOT Package
LTC3401/LTC3402	1A/2A (I _{SW}), 3MHz, Synchronous Step-Up DC/DC Converter	V_{IN} : 0.5V to 5V, $V_{OUT(MAX)}$ = 6V, I_Q = 38µA, I_{SD} <1µA, MS Package
LT3461/LT3461A	0.3A (I _{SW}), 1.3MHz/3MHz, High Efficiency Step-Up DC/DC Converter with Integrated Schottky	V_{IN} : 2.5V to 16V, $V_{\text{OUT}(\text{MAX})}$ = 38V, I_{Q} = 2.8mA, $I_{\text{SD}}{<}1\mu\text{A},$ SC70, ThinSOT Packages
LT3464	0.08A (I _{SW}), High Efficiency Step-Up DC/DC Converter with Integrated Schottky, Output Disconnect	V_{IN} : 2.3V to 10V, $V_{\text{OUT}(\text{MAX})}$ = 34V, I_{Q} = 25µA, $I_{\text{SD}}{<}1\mu\text{A},$ ThinSOT Package
LT3465/LT3465A	Constant Current, 1.2MHz/2.7MHz, High Efficiency White LED Boost Regulator with Integrated Schottky Diode	V_{IN} : 2.7V to 16V, $V_{\text{OUT}(\text{MAX})}$ = 30V, I_{Q} = 1.9mA, $I_{\text{SD}}{<}1\mu\text{A},$ ThinSOT Package





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