

### LTC1998

<u>OGY</u> 2.5µA, 1% Accurate SOT-23 Comparator and Voltage Reference for Battery Monitoring **DESCRIPTION** 

#### FEATURES

- High Accuracy Trip Voltage: 1% Max Error Using External 1% Resistors
- Adjustable Threshold Voltage and Hysteresis
- Quiescent Current: 2.5µA Typ
- Output Swings Rail-to-Rail
- Thresholds Programmable from 2.5V to 3.25V
- Output State Guaranteed for V<sub>BATT</sub> ≥1.5V
- Low Profile (1mm) ThinSOT<sup>™</sup> Package

#### **APPLICATIONS**

 Lithium-Ion Battery-Powered Equipment PDAs Cell Phones Handheld Instruments Battery Packs Pagers Palm Top Computers POS Terminals

The LTC<sup>®</sup>1998 is a micropower comparator and a precision adjustable reference in a 6-pin SOT-23 package that is optimized for lithium-ion low battery detection circuits.

The LTC1998 features a voltage detection circuit with an adjustable threshold voltage and hysteresis. The threshold voltage can be programmed from 2.5V to 3.25V with two external resistors. A 10mV to 750mV hysteresis can be added with a third external resistor.

# A proprietary internal architecture maintains 1% threshold voltage accuracy over temperature with low cost 1% external resistors.

A separate power supply pin,  $V_{LOGIC}$ , allows the battery-low logic output to operate below the battery voltage, allowing compatibility with low voltage microprocessors without a pull-up resistor. Power supply glitches are eliminated by preventing the cross-conducting current which occurs when the output changes state.

The LTC1998 operates with battery or supply voltages up to 5.5V and its battery-low output is valid for battery voltages above 1.5V.



### **BLOCK DIAGRAM**

#### Threshold Voltage Error vs Temperature



### **ABSOLUTE MAXIMUM RATINGS**

(Note 1)

Total Supply Voltage (BATT or V<sub>LOGIC</sub> to GND)......6V Voltage

| V <sub>TH.A</sub> , V <sub>H.A</sub> | BATT + 0.3V to GND – 0.3V               |
|--------------------------------------|---|
| BATTLO                               | V <sub>LOGIC</sub> + 0.3V to GND – 0.3V |
| Operating Tempera                    | ture Range (Note 3) –40°C to 85°C       |
| Specified Temperat                   | ure Range (Note 4)                      |
| ITC1998C                             | -40°C to 85°C                           |

| LTC1998I–40°C to 85                      | °C |
|--|----|
|  | _  |
| Storage Temperature Range65°C to 150     | °C |
| Lead Temperature (Soldering, 10 sec) 300 | °C |

### PIN CONFIGURATION



### ORDER INFORMATION

| LEAD FREE FINISH  | TAPE AND REEL    | PART MARKING | PACKAGE DESCRIPTION    | SPECIFIED TEMPERATURE RANGE |
|-------------------|------------------|--------------|------------------------|-----------------------------|
| LTC1998CS6#PBF    | LTC1998CS6#TRPBF | LTTY         | 6-Lead Plastic TSOT-23 | -40°C to 85°C               |
| LTC1998IS6#PBF    | LTC1998IS6#TRPBF | LTTY         | 6-Lead Plastic TSOT-23 | –40°C to 85°C               |
| LEAD BASED FINISH | TAPE AND REEL    | PART MARKING | PACKAGE DESCRIPTION    | SPECIFIED TEMPERATURE RANGE |
| LTC1998CS6        | LTC1998CS6#TR    | LTTY         | 6-Lead Plastic TSOT-23 | -40°C to 85°C               |
| LTC1998IS6        | LTC1998IS6#TR    | LTTY         | 6-Lead Plastic TSOT-23 | –40°C to 85°C               |

Consult LTC Marketing for parts specified with wider operating temperature ranges. \*The temperature grade is identified by a label on the shipping container.

For more information on lead free part marking, go to: http://www.linear.com/leadfree/

For more information on tape and reel specifications, go to: http://www.linear.com/tapeandreel/



## **ELECTRICAL CHARACTERISTICS** The $\bullet$ denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T<sub>A</sub> = 25°C. V<sub>GND</sub> = 0V, unless otherwise noted.

| Power Supply  |  |   |                   |            |                   |                |
|---|--|---|-------------------|------------|-------------------|----------------|
|   |  |   |                   |            |                   |                |
| Supply Voltage Range-BATT   |  | • | 1.5               |            | 5.5               | V              |
| Supply Voltage Range-V <sub>LOGIC</sub>                               |  | • | 1                 |            | V <sub>BATT</sub> | V              |
| Supply Current, V <sub>BATT</sub> = 3V,<br>V <sub>TH.A</sub> = 1.5V   | T <sub>A</sub> = 25°C<br>LTC1998CS6<br>LTC1998IS6  | • |                   | 2.5        | 3.5<br>4.2<br>4.5 | μΑ<br>μΑ<br>μΑ |
| Supply Current, V <sub>BATT</sub> = 5.5V,<br>V <sub>TH.A</sub> = 1.5V | T <sub>A</sub> = 25°C<br>LTC1998CS6<br>LTC1998IS6  | • |                   | 3          | 4.3<br>5.2<br>5.5 | μΑ<br>μΑ<br>μΑ |
| Monitor   |  |   | 1                 |            |                   | 1              |
| Threshold Accuracy  | $V_{BATT.Th}$ = 2.5V, Pin 3 Shorted to Ground  | • |                   | 0.6<br>0.8 | 0.85<br>1         | %              |
|   | V <sub>BATT.Th</sub> = 3V, Pin 3 Driven by Precision Voltage Source to 1V<br>LTC1998C<br>LTC1998I                                    | • |                   | 0.5<br>0.6 | 0.61<br>0.71      | %              |
|   | V <sub>BATT.Th</sub> = 3V, V <sub>TH.A</sub> = 1V (Note 5)<br>Programmed with 1% Max External Resistors<br>LTC1998C<br>LTC1998I      | • |                   | 0.8<br>0.9 | 1<br>1.1          | %              |
|   | V <sub>BATT.Th</sub> = 3.25V, Pin 3 Driven by Precision Voltage Source to 1.5V<br>LTC1998C<br>LTC1998I                               | • |                   | 0.6<br>0.7 | 0.65<br>0.85      | %              |
|   | V <sub>BATT.Th</sub> = 3.25V, V <sub>TH.A</sub> = 1.5V (Note 5)<br>Programmed with 1% Max External Resistors<br>LTC1998C<br>LTC1998I | • |                   | 0.9<br>1   | 1.1<br>1.3        | %              |
| Hysteresis Accuracy   | $V_{HYST} \le 250mV$<br>250mV $\le V_{HYST} \le 750mV$   | • | -5                | ±5         | 5                 | mV<br>mV       |
| Allowable Hysteresis Range (Note 2)                                   |  | • | 10                |            | 750               | mV             |
| Propagation Delay   | C <sub>OUT</sub> = 100pF<br>Overdrive = 10mV<br>Overdrive = 100mV  |   |                   | 350<br>150 |                   | μs<br>μs       |
| Threshold Adjust Pin Leakage, I <sub>TH.A</sub>                       | $V_{TH,A} \le 1.5V$  | • |                   | 0.01       | 1                 | nA             |
| Hysteresis Adjust Pin Leakage, I <sub>H.A</sub>                       | $V_{H,A} \le 1.5V$   | • |                   | 0.01       | 1                 | nA             |
| Output  |  |   |                   |            |                   |                |
| BATTLO High Voltage   | $I_{OUT} = -1mA$ , $V_{LOGIC} \ge 1.5V$  | • | $V_{LOGIC} - 0.3$ |            |                   | V              |
| BATTLO Low Voltage  | $I_{OUT} = 1mA, V_{BATT} \ge 2V$   |   |                   |            | 0.2               | V              |
| BATTLO Low Voltage  | I <sub>OUT</sub> = 0.25mA, V <sub>BATT</sub> = 1V  | • |                   | 0.3        |                   | V              |

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: Maximum allowable hysteresis depends on desired trip voltages. See application notes for details.

Note 3: LTC1998C and LTC1998I are guaranteed functional over the operating temperature range of -40°C to 85°C.

**4:** The LIC1998C is guaranteed to meet specified performance from 0°C to 70°C. The LTC1998C is designed, characterized and expected to meet specified performance from -40°C to 85°C but is not tested or QA sampled at these temperatures. The LTC1998I is guaranteed to meet specified performance from -40°C to 85°C.

Note 5: This parameter is not 100% tested.

### **TYPICAL PERFORMANCE CHARACTERISTICS**





### **TYPICAL PERFORMANCE CHARACTERISTICS**





### PIN FUNCTIONS

**BATT (Pin 1):** Battery Voltage to be monitored. Supply current is also drawn from this pin. Board layout should connect this pin to the battery(+) terminal, through a trace that does not conduct load current.

**GND (Pin 2):** Ground should be connected to the battery (–) terminal through a trace that does not conduct load return current.

**V<sub>TH.A</sub> (Pin 3):** Threshold Adjust Pin. Adjusts the low battery threshold voltage,  $V_{BATT.Th} = 2.5V + (V_{TH.A}/2)$ .  $V_{TH.A}$  can be supplied by a voltage source or a resistor divider.

**V<sub>H.A</sub> (Pin 4):** Hysteresis Adjust. Hysteresis threshold voltage V<sub>TH2</sub> =  $2.5V + (V_{H.A}/2)$ . V<sub>H.A</sub> can be supplied by a voltage source or resistor divider. V<sub>H.A</sub> must always be programmed to a higher potential than V<sub>TH.A</sub>. Hysteresis voltage, V<sub>HYST</sub> = V<sub>TH2</sub> - V<sub>BATT.Th</sub>.

**V<sub>LOGIC</sub> (Pin 5):** Positive Supply Voltage for Output Driver. This voltage can be driven from an external logic supply or tied to BATT.

**BATTLO (Pin 6):** Output of Comparator. Low for BATT <  $V_{BATT.Th}$  (low battery threshold voltage). Output state guaranteed for  $V_{BATT} \ge 1.5V$ .



#### **QUICK DESIGN GUIDE**

#### How to Calculate the External Resistor Values

The LTC1998 is a low battery warning indicator and is especially designed for monitoring the voltage of single-cell Lithium-Ion batteries. The LTC1998 compares its supply pin (BATT) to an accurate internal reference; if the battery voltage falls below the programmed low battery threshold voltage of the LTC1998, the battery low pin (BATTLO) will change state, from high to low, to indicate a low battery condition. The low battery threshold voltage is programmed via the voltage threshold adjust pin (V<sub>TH.A</sub>). A hysteresis adjust pin (V<sub>H.A</sub>) will add hysteresis to the programmed value of the low battery threshold voltage.

#### **Typical Application**

#### Table 1: Design Equations for R1, R2, R3, Figure 1

Choose desired values for:

- VBATT.Th: Low Battery Threshold Voltage
- V<sub>HYST</sub>: Hysteresis Voltage
- IR: Max Allowable Resistor Current

Solve:

$$R_{TOTAL} = R1 + R2 + R3 = \frac{4.2V}{I_R}$$

$$R1 = R_{TOTAL} \cdot \left(\frac{5V}{V_{BATT.Th} + V_{HYST}} - 1\right)$$

$$R2 = R_{TOTAL} \cdot \left(\frac{5V}{V_{BATT.Th}} - 1\right) - R1$$

$$R3 = R_{TOTAL} - R1 - R2$$

Example 1: A system using a 4.2V (fully charged) Lithiumlon battery requires a low battery threshold of 2.7V, 100mV hysteresis and can allow up to  $4.2\mu$ A maximum resistor current.

 $R_{TOTAL} = 1M\Omega$ , R1 = 786k, R2 = 66k and R3 = 148k Choose standard 1% values.

R1 = 787k, R2 = 66.5k, R3 = 147k



Figure 1. Low Battery Threshold Detector with Hysteresis



### **APPLICATIONS INFORMATION**

# LOW BATTERY THRESHOLD VOLTAGE AND HYSTERESIS ADJUST

#### Low Battery Threshold Voltage Adjustment, Pin 3

The low battery threshold voltage is the battery voltage which will trip the ( $\overline{BATTLO}$ ) pin high to low. It should be adjusted via the threshold adjust pin ( $V_{TH.A}$ ). This is a high input impedance pin that senses an externally applied voltage and programs the low battery threshold voltage ( $V_{BATT.Th}$ ). The  $V_{TH.A}$  pin is designed to accommodate voltages from 0V to 1.5V with respect to ground. This allows the low battery threshold voltage to be set to any voltage between 2.5V and 3.25V, that is:

$$V_{BATT.Th} = 2.5V + \frac{(V_{TH.A})}{2}$$

For instance, if the applied voltage at pin 3,  $V_{TH,A}$ , is 1V the LTC1998 will indicate a low battery condition when the battery voltage pin (BATT) falls below 3V.

The voltage at the threshold adjust pin ( $V_{TH,A}$ ) can be set with any voltage source. This pin allows a continuous time adjustment, that is, the low battery threshold voltage may be changed at any time. The high input impedance of the  $V_{TH,A}$  pin allows the use of a high valued resistive divider (to minimize current drain) from the battery to set the battery low threshold voltage, Figure 2.



Figure 2. Resistor Divider Sets Threshold

The simple calculations of resistor values R1 and R2 are illustrated below. Set a value for R1 + R2. This value will affect the max amount of current drawn from the battery when fully charged. For instance if R1 + R2 = 1M the resistive divider will draw 4.1µA when the battery voltage is 4.1V. Set the desired value of  $V_{BATT.Th}$  (this value should be between 2.5V and 3.25V) that is the value of the battery voltage that will trip the internal circuitry of the LTC1998 and change the state of the battery low pin (BATTLO).

Solve for R1=(R1+R2) 
$$\left(\frac{5V}{V_{BATT.Th}}-1\right)$$

Example: A Lithium-Ion battery is monitored and a battery low signal should be issued when it discharges to 2.85V, that is,  $V_{BATT.Th} = 2.85V$ ; if (R1 + R2) = 1M, then R1 = 754.38k and R2 = 245.62k. Choose the closest 1% value of R1 = 750k and R2 = 243k. Calculate the practical value for  $V_{BATT.Th}$  as it will be slightly different from 2.85V, due to the use of standard 1% resistor values.

$$V_{BATT.Th} = 5V \frac{R1 + R2}{R1 + (R1 + R2)} = 2.849V$$

The above low battery threshold of 2.849V is guaranteed to within 1% even though 1% resistors are used to program the  $V_{TH,A}$  voltage applied to Pin 3.

For sake of completeness, the voltage at Pin 3 (V<sub>TH.A</sub>) can be easily calculated by  $V_{TH.A} = V_{BATT.Th}$  (R2/(R1 + R2) = 0.6972V (when  $V_{BATTERY} = V_{BATT.Th}$ ).



#### **APPLICATIONS INFORMATION**

#### Hysteresis Adjustment, Pin 4.

The LTC1998 has an adjustable hysteresis ranging from 10mV to 0.75V. A large hysteresis is useful in the event that a low battery signal at the LTC1998's BATTLO pin causes the system to shed some battery load, thus inducing system confusion as the partially loaded battery recovers and changes the status of Pin 6 (BATTLO). The 2.5V to 3.25V programming window of low battery threshold voltage includes the hysteresis. If, for instance, the low battery threshold voltage is set to 2.5V, 750mV hysteresis can be added on top of the 2.5V. If the low battery threshold voltage is set to 3.15V, only 100mV hysteresis can be applied.

The programming of the hysteresis threshold adjust pin  $(V_{H,A})$  is similar to the programming of the voltage threshold adjust pin  $(V_{TH,A})$  already described in the previous paragraph. Pin 4 effectively adjusts the threshold voltage at which the low battery pin (BATTLO) changes state from low to high. This threshold  $(V_{TH2})$  is defined as:

$$V_{TH2} = 2.5V + \frac{(V_{H.A})}{2}$$

The actual hysteresis voltage is:

 $V_{HYST} = V_{TH2} - V_{BATT.Th}$ 

It is imperative that the hysteresis threshold adjust voltage at Pin 4 be set to a higher voltage than the low battery threshold adjust voltage at Pin 3, at all times, to avoid oscillation at the BATTLO output pin. The hysteresis threshold adjust pin may be set with a voltage source or with a resistor divider, just as with the low battery threshold adjust pin.

#### **Combined Control of Threshold and Hysteresis**

If a resistor divider is desired, then both threshold adjust dividers can be combined in order to save current. This simple technique also guarantees that the hysteresis threshold adjust voltage at Pin 4 is higher than the voltage at the  $V_{TH,A}$  pin, Figure 3.



Figure 3. Combined Resistor Divider



### **APPLICATIONS INFORMATION**

The calculation of the resistor values R1, R2 and R3 is quite straightforward and similar to the procedure outlined in the previous paragraph.

Choose a value for the sum of R1 + R2 + R3 as well as the values for low battery threshold and hysteresis.

Solve for resistor R1:

R1=(R1+R2+R3)
$$\left(\frac{5V}{V_{TH2}}-1\right)$$

Solve for the sum of

$$(R1+R2) = (R1+R2+R3)\left(\frac{5V}{V_{BATT.Th}}-1\right)$$

then solve for R2 and R3.

Example: A system needs to detect a low battery voltage of 3V ( $V_{BATT.Th} = 3V$ ) with 250mV hysteresis ( $V_{TH2} = 3.25V$ ). Set the value of the resistor divider (R1 + R2 + R3) = 1M.

R1 = 539k, R1 + R2 = 667k, R2 = 128k, R3 = 333k. Choose the closest 1% values, that is 536k, 332k, 127k. Figure 4 graphically shows the function of the LTC1998 as described above.

#### **VERSATILE OUTPUT DRIVER**

#### V<sub>LOGIC</sub>, BATTLO (Pins 5,6)

The LTC1998 uses a CMOS push-pull output stage to drive the low battery output signal. This output pin (BATTLO) has a separate supply pin, ( $V_{LOGIC}$ ) that can be used to provide an output voltage rail matching the  $V_{DD}$  logic of microprocessors. The  $V_{LOGIC}$  pin may be tied to a voltage lower than the voltage at the BATT pin. The  $V_{LOGIC}$ pin may also be tied to a voltage higher than  $V_{BATT}$  via a series resistor greater than 10k. The output will then act as an open-drain device.

In a given application, if it is possible for BATTLO to be shorted to GND or a supply, a series resistor should be added to limit the short-circuit current to 5mA.



Figure 4. LTC1998 Function Plot



#### PACKAGE DESCRIPTION



S6 Package 6-Lead Plastic TSOT-23



### **TYPICAL APPLICATIONS**

Single Li-Ion Cell Low Battery Detector



Micropower 2.9V  $V_{CC}$  Threshold Detector with 15mV Hysteresis



Low Battery Load Reduction Circuit



#### **Backup Battery Switchover Circuit** MBRM120 2.5V TO 4.2V VOUT **R**1 787k BAT54C R4 Si2301 1% 4 BATT 1 N VLOGIC /<sub>H.A</sub> R2 **≤**68.11 Si2301 Li-lon LTC1998 1% 3 BAT540 BATTLO TH.A R3 K3 ₹147k 1% GND 3V BACKUP 2 BATTERY SWITCHES TO BACKUP BATTERY WHEN PRIMARY FALLS BELOW 2.7V. SWITCHES BACK TO PRIMARY WHEN VOLTAGE RECOVERS TO $\ge 2.8V$ 1998 TA04 **High Accuracy Window Comparator** with Dual Hysteresis VIN V R1 619k 1% **₹**1M BATT VLOGIC V<sub>H.A</sub> R2 6.04k V<sub>OUT</sub> LTC1998 1% BATTI O -2N7002 $V_{OUT} = V^+ WHEN$ THA R3 383k 1% GND $2.6V \le V_{IN} \le 3.1V$ 2 ÷ R4 909k 1% BATT 5 NC

VLOGIC

BATTLO

LTC1998

GND

2 Ŧ

/<sub>H.A</sub>

TH.A

**R**5

**≷**6.98k

1%

R6

₹76.8k 1%

3

#### **RELATED PARTS**

| PART NUMBER             | DESCRIPTION                                  | COMMENTS  |
|-------------------------|--|---|
| LTC1440/LTC1540         | Micropower Comparator with 1% Reference      | 1.182V ±1% Reference, ±10mV (Max) Input Offset  |
| LTC1441/LTC1442         | Micropower Dual Comparator with 1% Reference | 1.182V ±1% Reference (LTC1442)  |
| LTC1443/LTC1444/LTC1445 | Micropower Quad Comparator with 1% Reference | LTC1443 has 1.182V Reference, LTC1444/LTC1445 have 1.221V Reference and Adjustable Hysteresis |

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1998fb

WINDOW LOW THRESHOLD = 2.6V

WINDOW HIGH THRESHOLD = 3.1V

1998 TA06

HYSTERESIS = 10mV

HYSTERESIS = 10mV

### **Mouser Electronics**

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