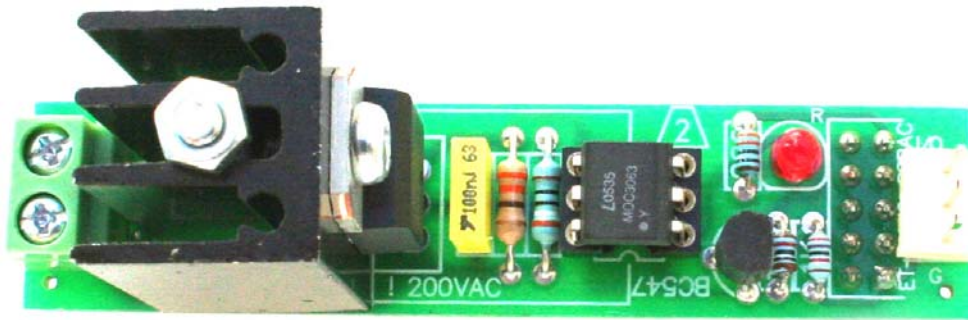


**MR-BusIO-SSRAC™ BusIO Solid State Relay BOARD
User Manual**

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MR-BusIO-SSRAC™ BusIO Solid State Relay BOARD User Manual

Description

The MR-BusIO-SSRAC is an experiment board for switching AC output voltage by using SSR (Solid State Relay). SSR is similar to mechanical relay that consists of inductor and surface contact. The internal structure of SSR uses semiconductor material to switching ON and OFF. The maximum controlling voltage is 400VAC at 6A. It is best for switching AC output voltage.

The board can be use with MR-BusIO-MAIN board or stand-alone. PCB size is 0.63" x 2.80"

Operation:

General structure of SSR is shown in figure 1. Voltage applied to the input of an SSR causes the LED to shine on the photo-sensitive diode. The voltage is causing the TRIAC to turn on.

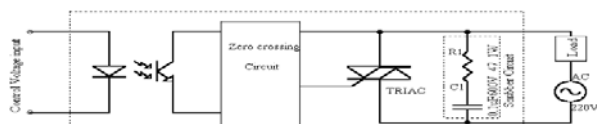
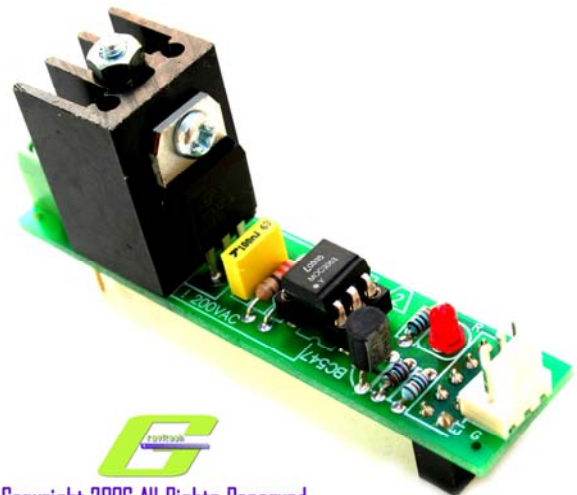


FIG 1: General structure of SSR

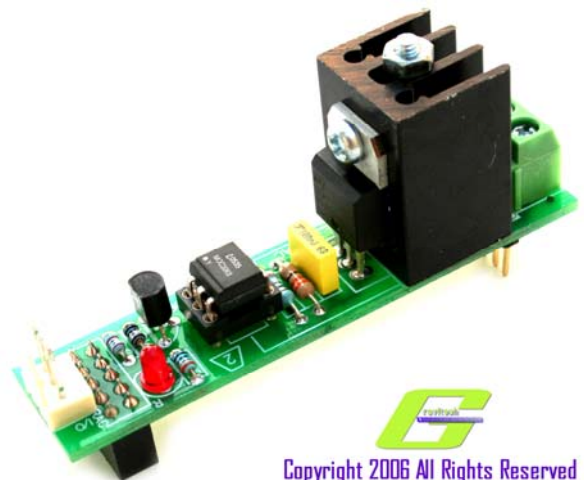
The board consists of MOC3042 IC which internally combined photo-coupler and zero crossing circuit together. The function of zero crossing circuit is to detect the voltage at zero point and supply current to the gate of the TRIAC. For 330ohm resistor and 0.1uF capacitor at the gate of the TRIAC, their function is sharing the current at the gate so they are not too high. Also, protect external noise into circuit.

The circuit is operating with AC voltage only. It is start when applying logic HIGH to an I/O pin. The indicator LED then illuminated which mean it is now close circuit.

User can connect AC load up to 400VAC at 6A through output 2-PIN terminal block. In case of interfacing with inductance load such as motor, user should add Snubber circuit by putting R1 47ohm 1W resistor and C1 0.1uF 600V capacitor as shown in Figure 1.



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MR-BusIO-MAIN

- **MR-BusIO-MAIN**
Experiment board which receives output signals from any microcontrollers. The signals then distribute to daughter boards for each experiment. It designed to connect directly with 10PIN MRconnect®. It is a quick and easy way to control up to 8 daughter boards.



FIG 2: MR-BusIO-SSRAC Board Layout

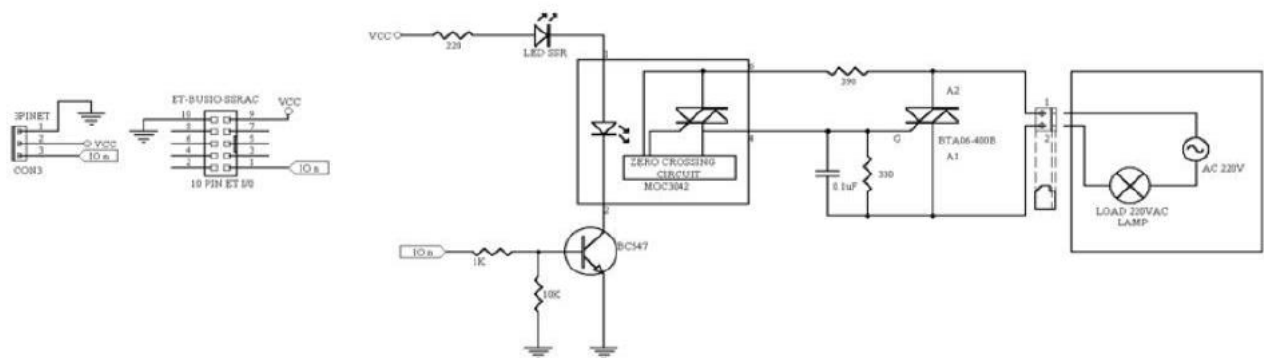


FIG 3: MR-BusIO-SSRAC Schematic

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Notes

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