TSL235R

Light-to-Frequency Converter

General Description

The TSL235R light-to-frequency converter combines a silicon photodiode and a current-to-frequency converter on a single monolithic CMOS integrated circuit. Output is a square wave (50% duty cycle) with frequency directly proportional to light intensity (irradiance) on the photodiode. The digital output allows direct interface to a microcontroller or other logic circuitry. The device has been temperature compensated for the ultraviolet-to-visible light range of 320nm to 700nm and responds over the light range of 320nm to 1050nm. The TSL235R is characterized for operation over the temperature range of -25°C to 70°C and is supplied in a 3-lead clear plastic side-looker package with an integral lens. When supplied in the lead (Pb) free package, the device is RoHS compliant.

Ordering Information and Content Guide appear at end of datasheet.

Key Benefits & Features

The benefits and features of the TSL235R Light-to-Frequency Converter, are listed below:

Figure 1:
Added Value of Using TSL235R

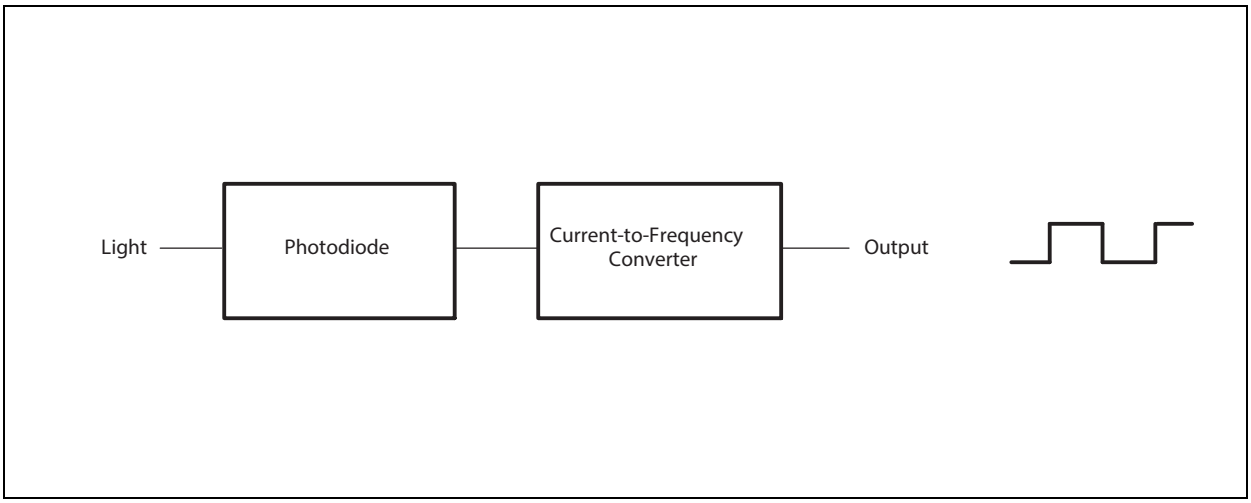
Benefits	Features
• Detects Light Intensity at a High Resolution	• 1M:1 Input Dynamic Range
• Provides Low Light Level Operation	• Low Dark Frequency of 0.4 Hz (Typ)
• Provides for High Sensitivity to Detect a Small Change in Light	• High Irradiance Responsivity 0.6kHz/(µW/cm^2) @ λp = 635nm
• Provides Additional Sensitivity Advantages	• 2x Gain Lense

- High-Resolution Conversion of Light Intensity to Frequency with no External Components
- Communicates Directly with a Microcontroller
- Compact Three-Leaded Clear-Plastic Package
- Single-Supply Operation Down to 2.7V
- Nonlinearity Error Typically 0.2% at 100kHz
- Stable 150ppm/°C Temperature Coefficient
- Single-Supply Operation

Block Diagram

The functional blocks of this device are shown below:

Figure 2:
TSL235R Block Diagram



Pin Assignments

The TSL235R pin assignments are described below:

Figure 3:
Pin Diagram of Package S Sidelooker (Front View)

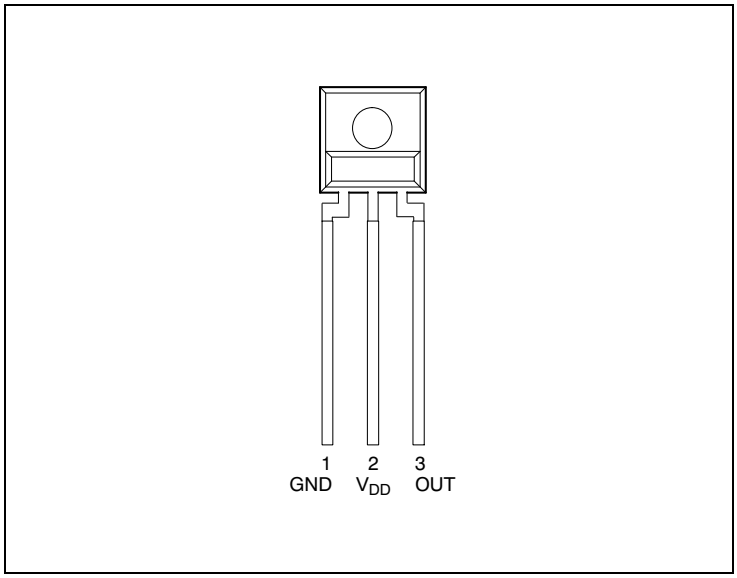


Figure 4:
Pin Diagram of Package SM Surface Mount Sidelooker (Front View)

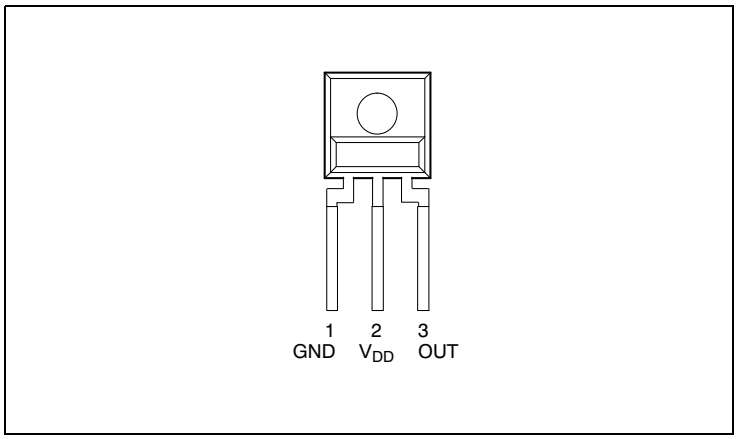


Figure 5:
Terminal Functions

Terminal		Type	Description
Name	No.		
GND	1		Power supply ground (substrate). All voltages are referenced to GND.
V_{DD}	2		Supply voltage
OUT	3	O	Output frequency

Absolute Maximum Ratings

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Figure 6:
Absolute Maximum Ratings over Operating Free-Air Temperature Range (unless otherwise noted)

Symbol	Parameter	Min	Max	Unit
V_{DD}	Supply voltage (1)		6	V
T_A	Operating free-air temperature range	-25	70	°C
T_{strg}	Storage temperature range	-25	85	°C
	Lead temperature 1.6mm (1/16 inch) from case for 10 seconds (S Package)		260	°C
	Reflow solder, in accordance with J-STD-020C or J-STD-020D (SM Package)		260	°C

Note(s):

1. All voltages are with respect to GND.

Electrical Characteristics

All limits are guaranteed. The parameters with min and max values are guaranteed with production tests or SQC (Statistical Quality Control) methods.

Figure 7:
Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Unit
V_{DD}	Supply voltage	2.7	5	5.5	V
T_A	Operating free-air temperature range	-25		70	°C

Figure 8:
Electrical Characteristics at T_A = 25°C, V_{DD} = 5V (unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{OH}	High-level output voltage	I_{OH} = -4mA	4	4.5		V
V_{OL}	Low-level output voltage	I_{OL} = 4mA		0.25	0.4	V
I_{DD}	Supply current			2	3	mA
	Full-scale frequency (1)		500			kHz
	Temperature coefficient of output frequency	Wavelength ≤ 700nm		±150		ppm/°C
k_{SVS}	Supply voltage sensitivity	V_{DD} = 5V ±10%		±0.5		%/V

Note(s):

1. Full-scale frequency is the maximum operating frequency of the device without saturation.

Figure 9:
Operating Characteristics at V_{DD} = 5V, T_A = 25°C

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
f_O	Output frequency	E_e = 430µW/cm^2, λ_p = 635nm	200	250	300	kHz
		E_e = 0µW/cm^2		0.4	10	Hz
	Nonlinearity (2)	f_O = 0kHz to 10kHz		±0.1%		%F.S.
		f_O = 0kHz to 100kHz		±0.2%		%F.S.
	Step response to full-scale step input		1 pulse of new frequency plus 1µs			

Note(s):

1. Full-scale frequency is the maximum operating frequency of the device without saturation.
2. Nonlinearity is defined as the deviation of fO from a straight line between zero and full scale, expressed as a percent of full scale.

Typical Operating Characteristics

Figure 10:
Output Frequency vs. Irradiance

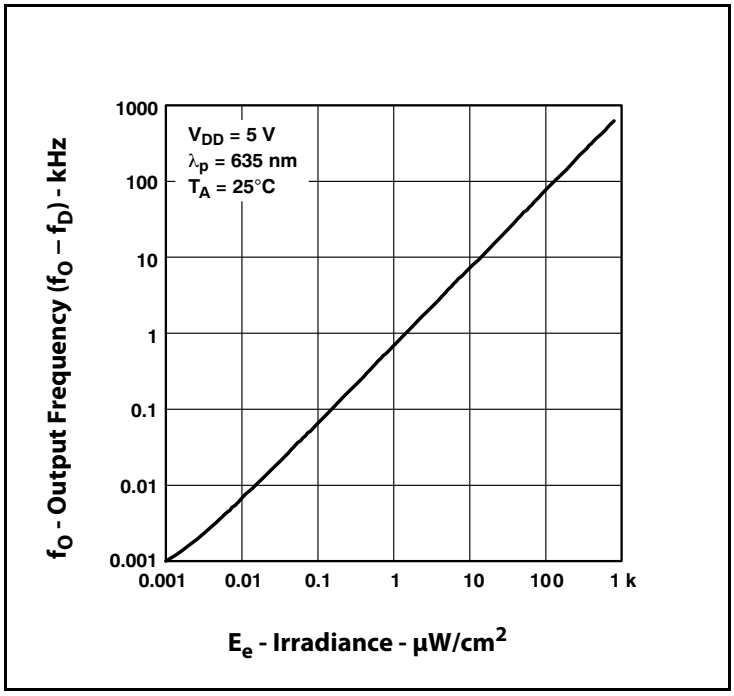


Figure 11:
Photodiode Spectral Responsivity

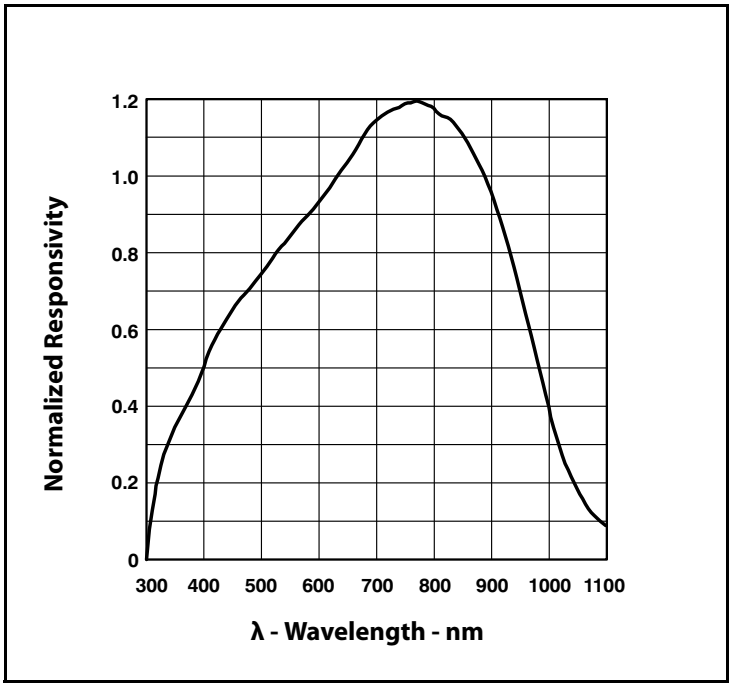


Figure 12:
Dark Frequency vs. Temperature

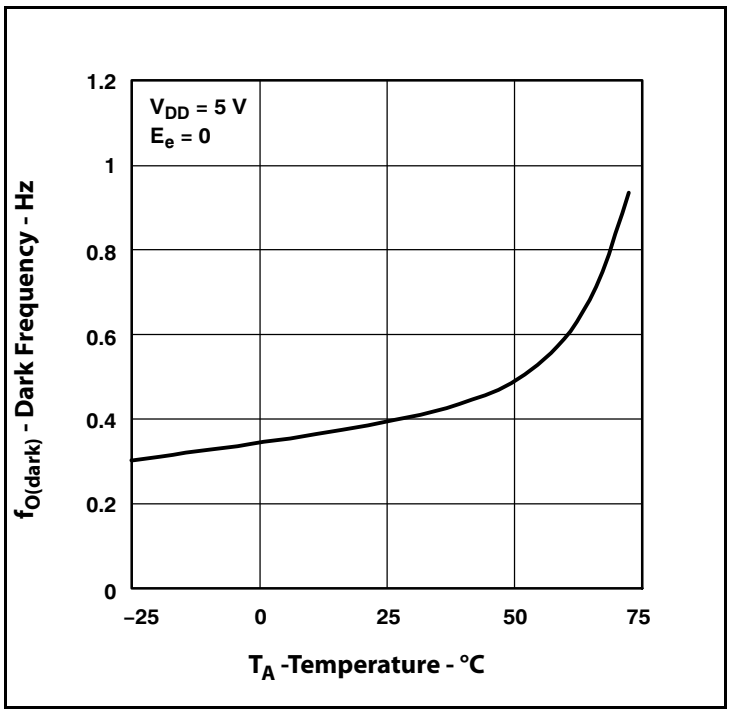


Figure 13:
Temperature Coefficient of Output Frequency vs. Wavelength of Incident Light

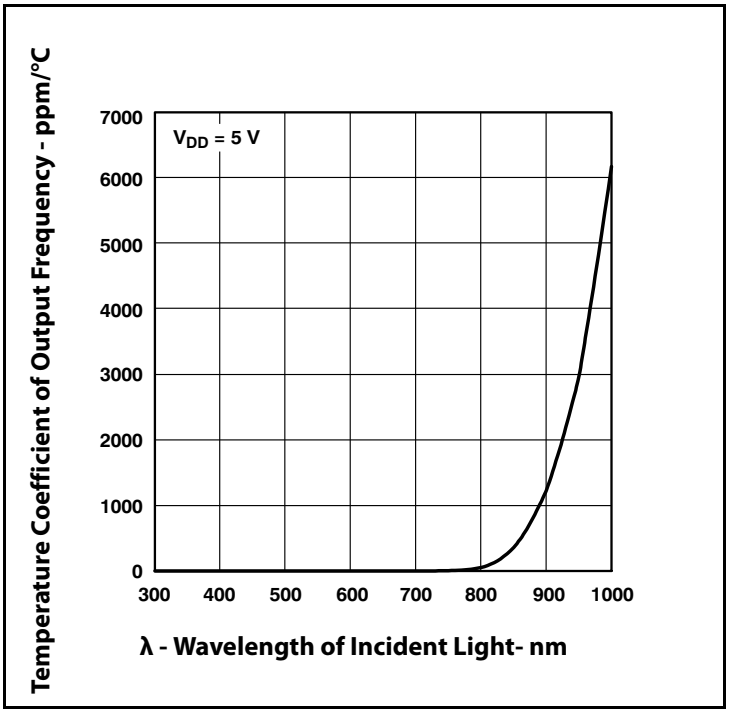
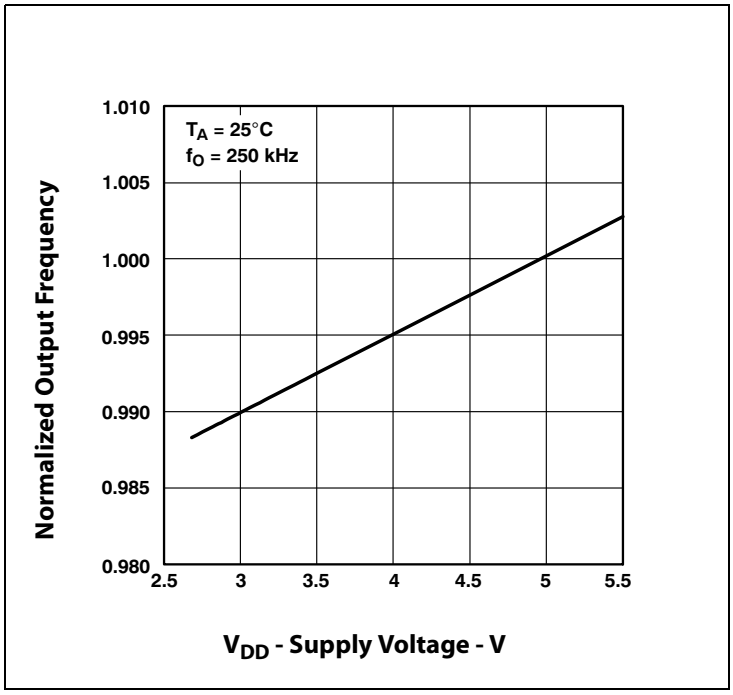


Figure 14:
Output Frequency vs. Supply Voltage



Application Information

Power-Supply Considerations

Power-supply lines must be decoupled by a 0.01µF to 0.1µF capacitor with short leads placed close to the TSL235R (Figure 15).

Output Interface

The output of the device is designed to drive a standard TTL or CMOS logic input over short distances. If lines greater than 12 inches are used on the output, a buffer or line driver is recommended.

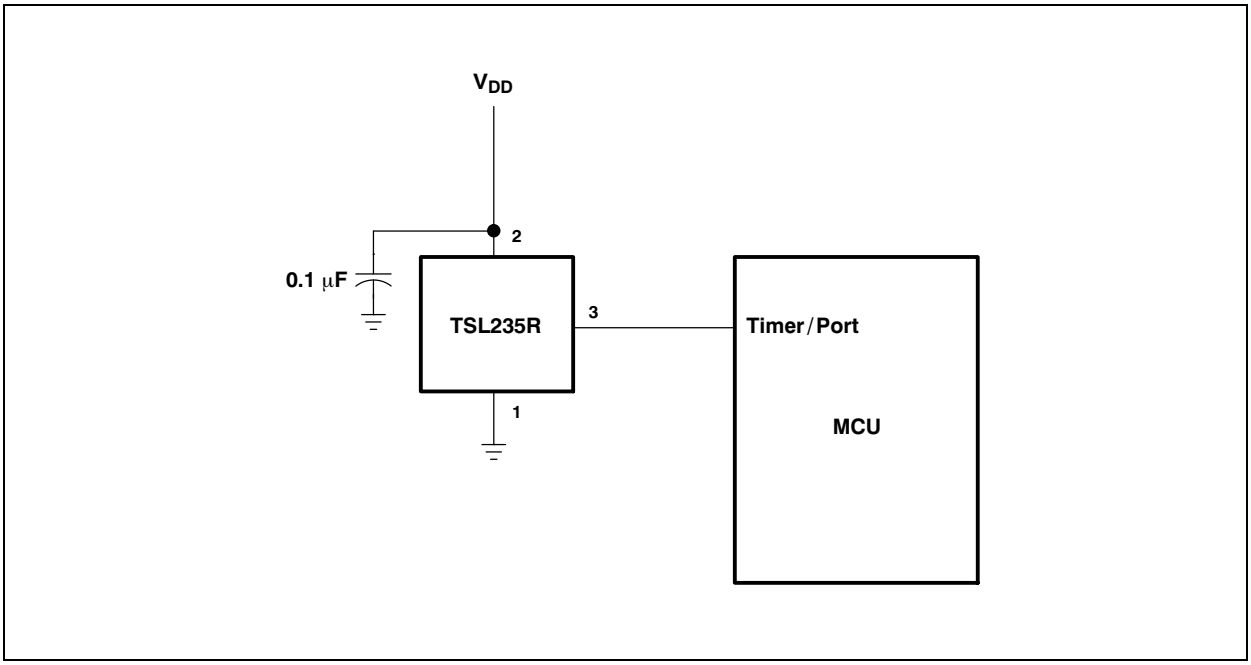
Measuring the Frequency

The choice of interface and measurement technique depends on the desired resolution and data acquisition rate. For maximum data-acquisition rate, period-measurement techniques are used.

Period measurement requires the use of a fast reference clock with available resolution directly related to reference-clock rate. The technique is employed to measure rapidly varying light levels or to make a fast measurement of a constant light source.

Maximum resolution and accuracy may be obtained using frequency-measurement, pulse-accumulation, or integration techniques. Frequency measurements provide the added benefit of averaging out random- or high-frequency variations (jitter) resulting from noise in the light signal. Resolution is limited mainly by available counter registers and allowable measurement time. Frequency measurement is well suited for slowly varying or constant light levels and for reading average light levels over short periods of time. Integration, the accumulation of pulses over a very long period of time, can be used to measure exposure - the amount of light present in an area over a given time period.

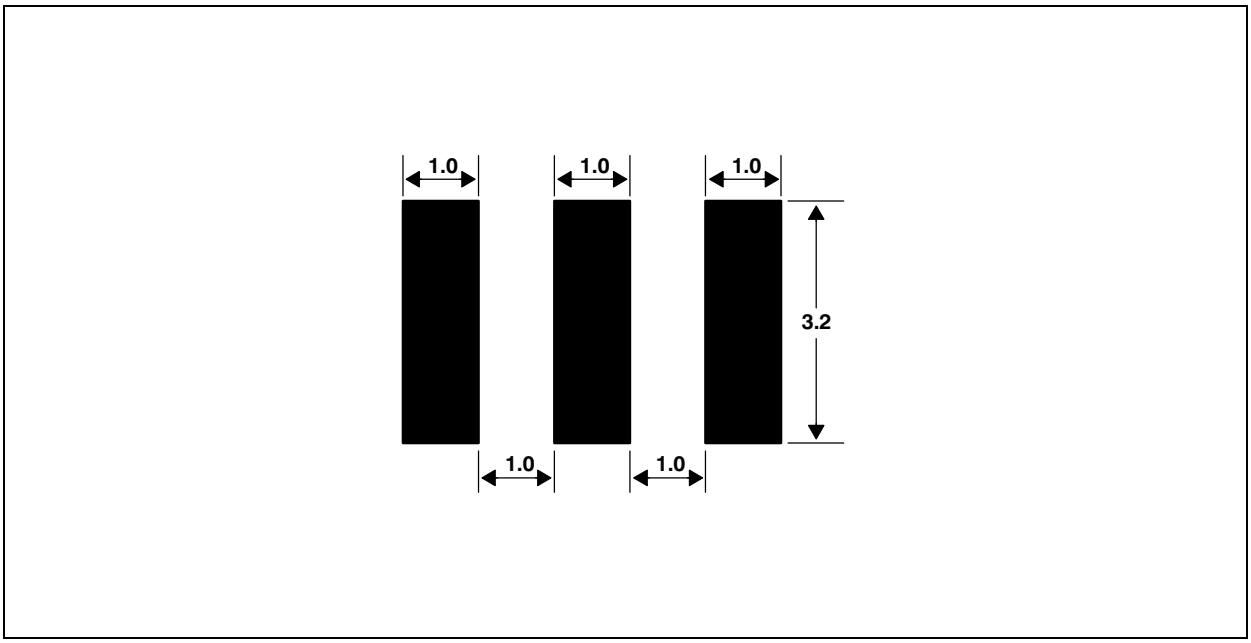
Figure 15:
Typical TSL235R Interface to a Microcontroller



PCB Pad Layout

Suggested PCB pad layout guidelines for the SM surface mount package are shown in Figure 16.

Figure 16:
Suggested SM Package PCB Layout



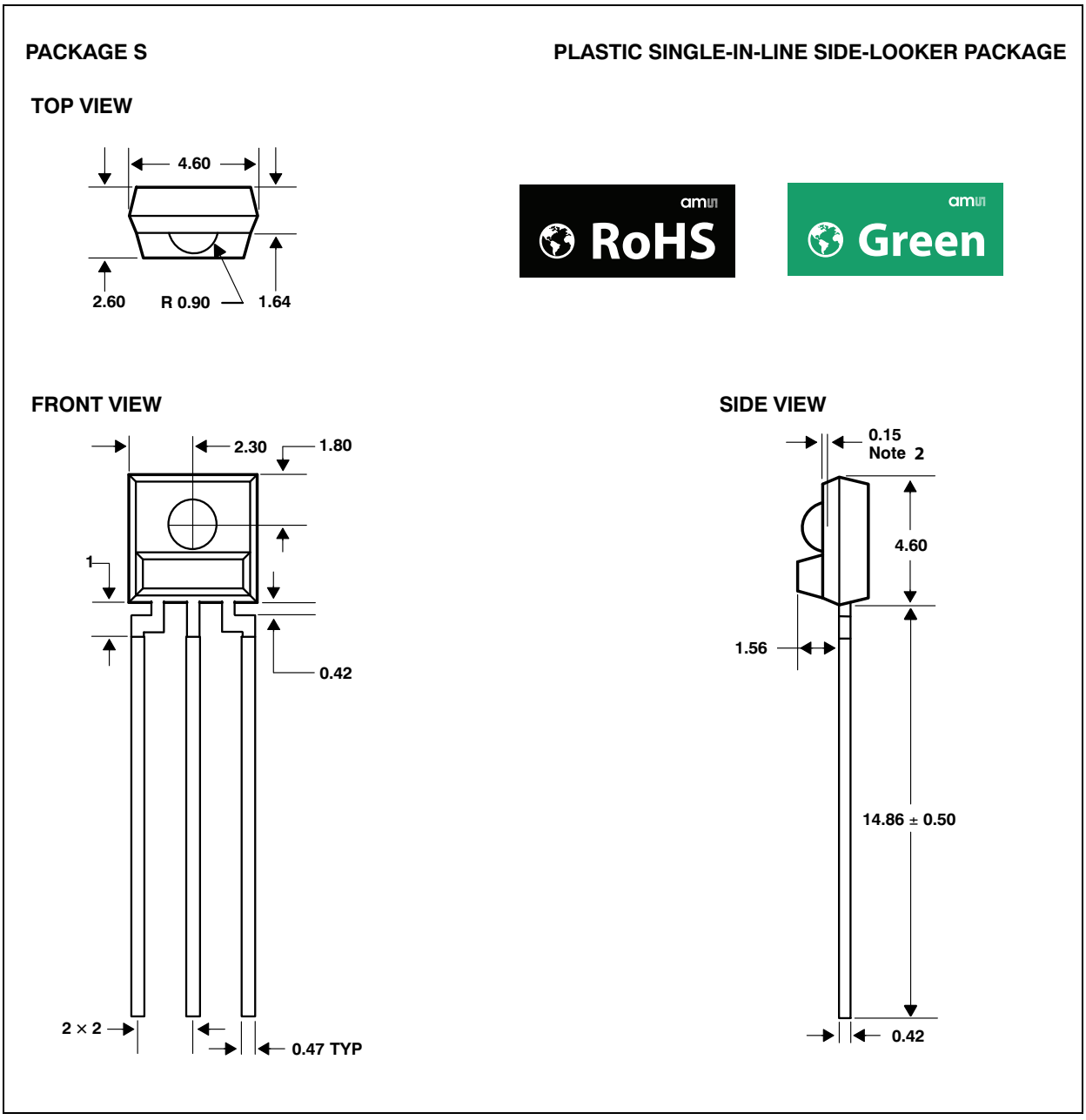
Note(s):

1. All linear dimensions are in millimeters.
2. This drawing is subject to change without notice.

Packaging Mechanical Data

The device is supplied in a clear plastic three-lead through-hole sidelooker package (S).

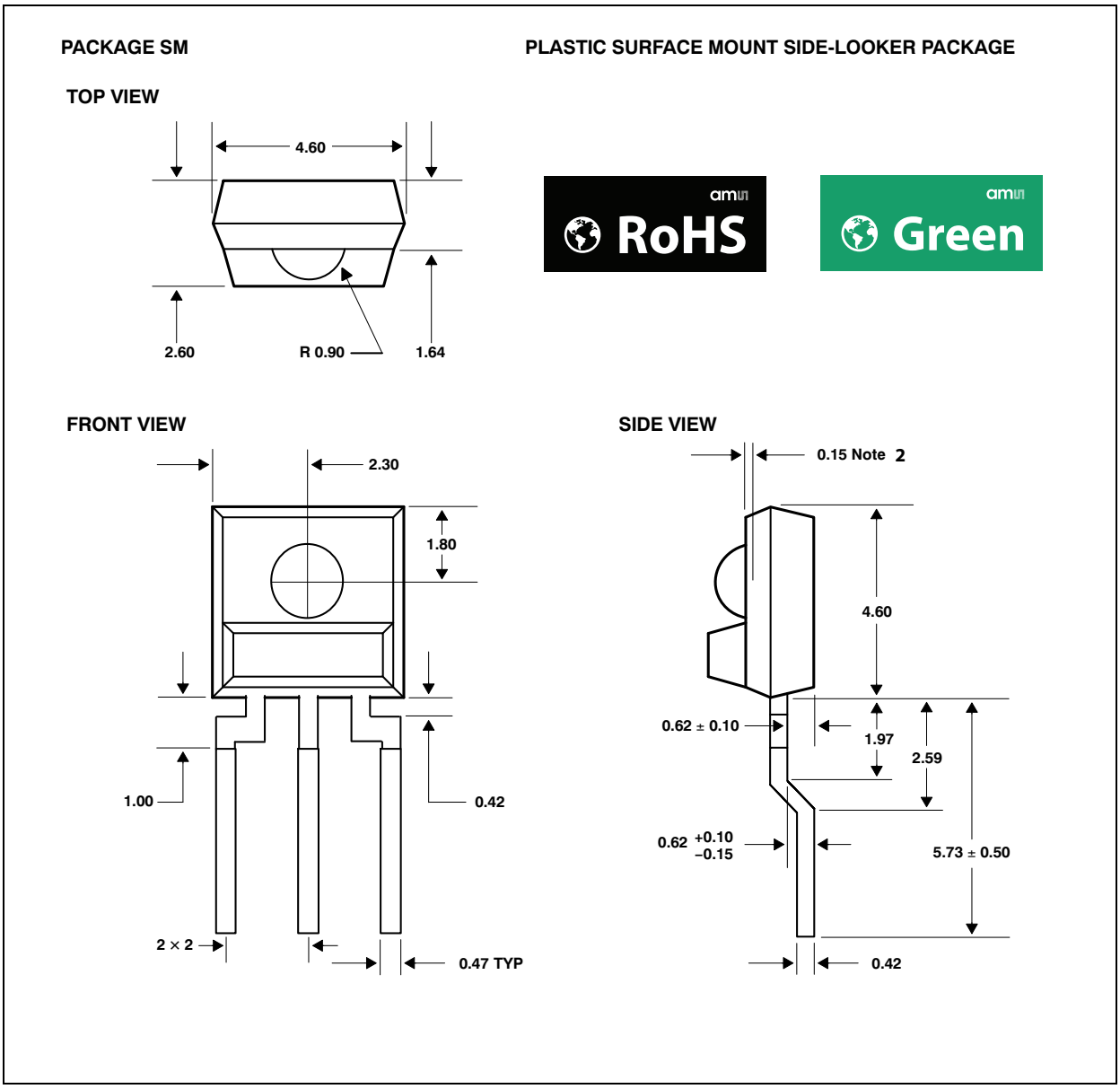
Figure 17:
Package S - Single-In-Line Side-Looker Package Configuration



Note(s):

1. All linear dimensions are in millimeters; tolerance is ±0.25mm unless otherwise stated.
2. Dimension is to center of lens arc, which is located below the package face.
3. The integrated photodiode active area is typically 0.92mm^2 in size and is located in the center of the lens and 0.97mm below the top of the lens surface.
4. Index of refraction of clear plastic is 1.55.
5. Lead finish for TSL235R-LF: solder dipped, 100% Sn.
6. This drawing is subject to change without notice.

Figure 18:
Package SM - Surface Mount Side-Looker Package Configuration



Note(s):

1. All linear dimensions are in millimeters; tolerance is ±0.25mm unless otherwise stated.
2. Dimension is to center of lens arc, which is located below the package face.
3. The integrated photodiode active area is typically located in the center of the lens and 0.97 mm below the top of the lens surface.
4. Index of refraction of clear plastic is 1.55.
5. Lead finish for TSL235RSM-LF: solder dipped, 100% Sn.
6. This drawing is subject to change without notice.

Ordering & Contact Information

Figure 19:
Ordering Information

Ordering Code	Device	T_A	Package - Leads	Package Designator
TSL235R-LF	TSL235R	-25°C to 70°C	3-lead Sidelooker - Lead (Pb) Free	S
TSL235RSM-LF	TSL235R	-25°C to 70°C	3-lead Surface-Mount Sidelooker - Lead (Pb) Free	SM

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Revision Information

Changes from TAOS038E (2007-Sep) to current revision 1-00 (2016-Mar-30)	Page
Content of TAOS datasheet was converted to the latest **ams** design	
Added Figure 1	1
Updated notes under Figure 17	11
Updated Figure 19	13

Note(s):

1. Page and figure numbers for the previous version may differ from page and figure numbers in the current revision
2. Correction of typographical errors is not explicitly mentioned.

Content Guide

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