

Metal Composite Power Inductor (Thin Film) Specification Sheet



CIGT201210UH1R5MNE (2012 / EIA 0805)

APPLICATION

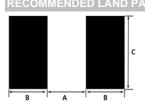
Smart phones, Tablet, Wearable devices, Power converter modules, etc.

FFATURES

Small power inductor for mobile devices
Low DCR structure and high efficiency inductor for power circuits.
Monolithic structure for high reliability
Free of all RoHS-regulated substances

Halogen free





	Unit : mm
TYPE	2012
Α	0.8
В	0.8
С	1.3

DIMENSION



TYPE	Dimension [mm]						
IIFE	L	W	Т	D			
2012	2.0±0.2	1.25±0.2	1.0 max	0.5±0.2			

DESCRIPTION

Part no	Part no. Size [inch/mm]		Inductance	Inductance tolerance	DC Resista	ance [mΩ]	Rated DC Cu	rrent (Isat) [A]	Rated DC C	urrent (Irms) \]
raitiio.				(%)	Max.	Тур.	Max.	Тур.	Max.	Тур.
CIGT201210UH1R5MNE	0805/2012	1.0	1.5	±20	97	81	2.6	2.9	2.1	2.4

- * Inductance : Measured with a LCR meter 4991A(Agilent) or equivalent (Test Freq. 1MHz, Level 0.1V)
- * DC Resistance : Measured with a Resistance HI-TESTER 3541(HIOKI) or equivalent
- * Maximum allowable DC current: Value defined when DC current flows and the initial value of inductance has decreased by 30% or

when current flows and temperature has risen to 40℃ whichever is smaller. (Reference: ambient temperature is 25℃±10)

(Isat): Allowable current in DC saturation: The DC saturation allowable current value is specified when the decrease of

the initial inductance value at 30% (Reference: ambient temperature is 25°C±10)

(Irms): Allowable current of temperature rise: The temperature rise allowable current value is specified when temperature of

the inductor is raised 40°C by DC current. (Reference: ambient temperature is 25°C±10)

- * Absolute maximum voltage : Absolute maximum voltage DC 20V.
- * Operating temperature range : -40 to +125°C (Including self-temperature rise)

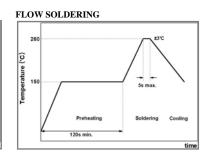
PRODUCT IDENTIFICATION

<u>CIG</u>	I	<u> 2012</u>	<u>10</u>	<u>UH</u>	<u>1R5</u>	<u>M</u>	<u>N</u>	<u>E</u>
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)

- (1) Power Inductor
- (3) Dimension (2012: 2.0mm × 1.25mm)
- (5) Remark (Characterization Code)
- (7) Toleranc (M:±20%)
- (8) Internal Code
- (9) Packaging (C:paper tape, E:embossed tape)
- (2) Type (T: Metal Composite Thin Film Type)
- (4) Thickness (10: 1.0mm)
- (6) Inductance (1R5: 1.5 uH)

RECOMMENDED SOLDERING CONDITION

REFLOW SOLDERING 280 230 180 180 Preheating 60s max. 60 ~ 120s 30 ~ 60s



IRON SOLDERING			
Temperature of	280°C max.		
Soldering Iron Tip	280 C max.		
Preheating	150 ℃ min.		
Temperature	130 Cmin.		
Temperature	ΔT≤130℃		
Differential	Δ1 ≥130 C		
Soldering Time	3sec max.		
Soldering Time	JSCC IIIax.		
Wattage	50W max		

PACKAGING

Packaging Style	Quantity(pcs/reel)
Embossed Taping	3000 pcs

Item	Specified Value	Test Condition		
Solderability	More than 90% of terminal electrode should be soldered newly.	After being dipped in flux for 4 ± 1 seconds, and preheated at $150\sim180$ °C for $2\sim3$ min, the specimen shall be immersed in solder at 245 ± 5 °C for 4 ± 1 seconds.		
Resistance to Soldering	No mechanical damage. Remaining terminal Electrode: 75% min. Inductance change to be within ±20% to the initial.	After being dipped in flux for 4 ± 1 seconds, and preheated at $150\sim180^{\circ}\mathrm{C}$ for $2\sim3$ min, the specimen shall be immersed in solder at $260\pm5^{\circ}\mathrm{C}$ for 10 ± 0.5 seconds.		
Thermal Shock (Temperature Cycle test)	No mechanical damage Inductance change to be within ±20% to the initial.	Repeat 100 cycles under the following conditions40±3 $^{\circ}$ for 30 min \rightarrow 85±3 $^{\circ}$ for 30 min		
High Temp. Humidity Resistance Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, for 500±12 hours. Measure the test items after leaving at normal temperature and humidity for 24 hours.		
Low Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PCB. Exposure at -55±2°C for 500±12 hours. Measure the test items after leaving at normal temperature and humidity for 24hours.		
High Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PCB. Exposure at 125±2 °C for 500±12 hours. Measure the test items after leaving at normal temperature and humidity for 24hours.		
High Temp. Humidity Resistance Loading Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, Rated Current for 500±12 hours. Measure the test items after leaving at normal temperature and humidity for 24 hours.		
High Temperature Loading Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, Rated Current for 500±12 hours. Measure the test items after leaving at normal temperature and humidity for 24 hours.		
Reflow Test	No mechanical damage Inductance change to be within ±20% to the initial	Peak 260±5℃, 3 times		
Vibration Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PCB. Vibrate as apply 10~55Hz, 1.5mm amplitude for 2 hours in each of three(X,Y,Z) axis (total 6 hours).		
	No mechanical damage	Bending Limit; 2mm Test Speed; 1.0mm/sec. Keep the test board at the limit point in 5 sec. PCB thickness: 1.6mm		
Bending Test	45	20 Unit :mm R340		
	No indication of peeling shall occur on the terminal electrode.	W(kgf) TIME(sec)		
Terminal Adhesion Test		0.5 10±1		
Drop Test	No mechanical damage Inductance change to be within ±20% to the initial.	Random Free Fall test on concrete plate. 1 meter, 10 drops		
lpeak (AC+DC Load Life)	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, Load(Ipeak) for 120 hours. (Frequncy:1MHz, Load(Ipeak):1.5hr on / 0.5hr off) Measure the test items after leaving at normal temperature and humidity for 24 hours. * Load(Ipeak) = Irms(max)×1.4		



Metal Composite Power Inductor (Thin Film) Data Sheet



1. Model: CIGT201210UH1R5MNE

2. Description

Port no	art no	Thickness	Inductance	Inductance tolerance		ince [mΩ]	Rated DC Cu	rrent (Isat) [A]	Rated DC C	,
raitilo.		[uH] (%)	Max.	Тур.	Max.	Тур.	Max.	Тур.		
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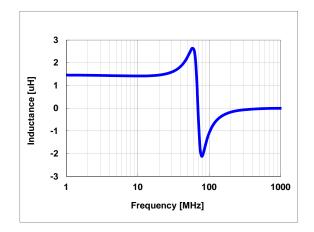
(Irms) : Allowable current of temperature rise : The temperature rise allowable current value is specified when temperature of the inductor is raised 40℃ by DC current. (Reference: ambient temperature is 25℃±10)

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- * Operating temperature range : -40 to +125°C (Including self-temperature rise)

3. Characteristics data

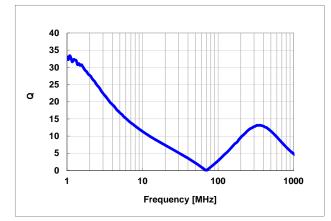
1) Frequency characteristics (Ls)

Agilent E4294A +E4991A , 1MHz to 1,000MHz

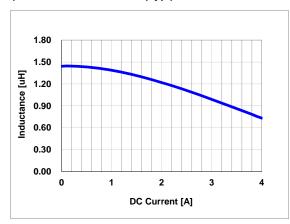


2) Frequency characteristics (Q)

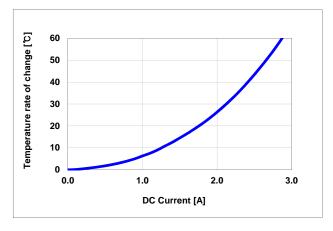
Agilent E4294A +E4991A , 1MHz to 1,000MHz



3) DC Bias characteristics (Typ.)



4)Temperature characteristics (Typ.)





Any data in this sheet are subject to change, modify or discontinue without notice The data sheets include the typical data for design reference only. If there is any question regarding the data sheets, please contact our sales personnel or application engineers

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Samsung Electro-Mechanics: CIGT201210UH1R5MNE