

ACA0861 - A, B, C, D

750/860 MHz CATV Line Amplifier MMIC

Data Sheet

FEATURES

- Flat Gain
- Very Low Distortion
- Excellent Input/Output Match
- Low DC Power Consumption
- Good RF Stability with High VSWR Load Conditions
- Surface Mount Package Compatible with Automatic Assembly
- Low Cost
- · Repeatability of Monolithic Fabrication
- Meets Cenelec Standard
- RoHS-Compliant Package Options



PRODUCT DESCRIPTION

The ACA0861 family of surface mount monolithic GaAs RF Linear Amplifiers has been developed to replace, in new designs, the standard CATV Hybrid amplifiers currently in use. The MMICs consist of two parallel amplifiers, each with 12 dB gain. The Amplifiers are optimized for exceptionally low distortion and noise figure while providing flat gain and excellent input and output return loss. There are four differently specified amplifiers available: two input stages and two output stages. The ACA0861A and the ACA0861C are input stages and are specified at +34 dBmV flat output. The ACA0861B

and ACA0861D are output stages and are specified at +44 dBmV flat output. A Hybrid equivalent is formed when one input stage ACA0861 is cascaded with an ACA0861 output stage between two transmission line baluns. For low gain applications a single ACA0861 can be used between baluns, for higher gain applications more than two ACA0861 can be cascaded between baluns. See ACA0861 application note for more information.

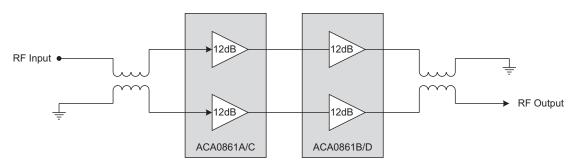


Figure 1: Hybrid Application Diagram

Input Stages

The ACA0861A and the ACA0861C are designed as input stages and are specified at +34 dBmV flat output. These parts can be used alone for low gain, low output level applications or can be cascaded with one of the ACA0861 output stages for higher gain and output signal drive level. The ACA0861A is a low power dissipation part designed to drive the ACA0861B output stage. The ACA0861C is a slightly higher power dissipation part and provides the needed distortion parameters to drive the ACA0861D output stage.

Output Stages

The ACA0861B and ACA0861D are designed as output stages and are specified at +44 dBmV flat output. These parts can be used alone for low gain, high output level applications or can be cascaded with one of the ACA0861 input stages for higher gain. The ACA0861B is a low power dissipation part designed as the output stage with an ACA0861A input stage. The ACA0861D is a higher power dissipation part designed as the output stage with an ACA0861C input stage. Cascaded, an ACA0861A and ACA0861B provide exceptional push-pull hybrid equivalent performance; an ACA0861C and an ACA0861D cascaded provide exceptional power doubling hybrid equivalent performance.

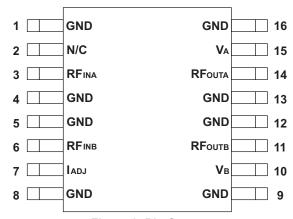


Figure 2: Pin Out

Table 1: Pin Description

PIN	NAME	DESCRIPTION	PIN	NAME	DESCRIPTION
1	GND	Ground	9	GND	Ground
2	N/C	No Connection	10	V _B	Supply for Amplifier B
3	RFINA	Input to Amplifier A	11	RFоитв	Output from Amplifier B
4	GND	Ground	12	GND	Ground
5	GND	Ground	13	GND	Ground
6	RF _{INB}	Input to Amplifier B	14	RFouta	Output from Amplifier A
7	I ADJ	Current Adjust	15	VA	Supply for Amplifier A
8	GND	Ground	16	GND	Ground

ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

PARAMETER	MIN	MAX	UNIT
Amplifier Supplies (pins 10, 11, 14, 15)	0	+15	VDC
RF Input Power (pins 3, 6)	1	+70	dBmV
Storage Temperature	-65	+150	°C
Soldering Temperature	-	+260	°C
Soldering Time	-	5.0	sec

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Notes:

- Pins 3 and 6 should be AC-coupled. No external DC bias should be applied
- 2. Pin 7 should be pulled to ground through a resistor or left open-circuited. No external DC bias should be applied.

Table 3: Operating Ranges

PARAMETER	MIN	TYP	MAX	UNIT
RF Frequency	40	1	860	MHz
Supply: V _D (pins 10, 11, 14, 15)	-	+12	-	VDC
Operating Temperature: T _A	-40	-	+110	°C

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Table 4: Electrical Specifications $(T_A = +25 \text{ °C}, V_D = +12 \text{ VDC})$

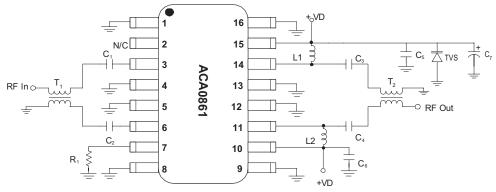
DADAMETED	ACA0861A		ACA0861B		ACA0861C		ACA0861D			UNIT			
PARAMETER	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNII
Gain ⁽¹⁾	11.4	11.9	12.4	11.5	12	12.5	11.5	12	12.5	11.6	12.1	12.6	dB
Gain Flatness ⁽¹⁾	1	1	<u>+</u> 0.3	ı	1	<u>+</u> 0.3	-	1	<u>+</u> 0.3	-	1	<u>+</u> 0.3	dB
Noise Figure ⁽²⁾	1	3	5	1	3	5	-	3	5	-	3	6	dB
CTB ^{(2), (3)} 77 Channels 110 Channels 128 Channels	1 1	-70 -68 -65	- -64 -	1 1	-62 -60 -58	- -57 -	- - -	-77 -75 -71	- -68 -	- - -	-70 -68 -67	- -66 -	dBc
CSO ^{(2),(3)} 77 Channels 110 Channels 128 Channels		-71 -71 -70	- -66 -		-66 -66 -64	- -60 -	- - -	-75 -75 -73	- -68 -	- - -	-72 -72 -70	- -68 -	dBc
XMOD ^{(2),(3)} 77 Channels 110 Channels 128 Channels		-67 -63 -59	- -56 -		-62 -56 -55	- -50 -	- - -	-74 -71 -67	- -62 -	- - -	-71 -68 -66	- -61 -	dBc
Supply Current ⁽⁴⁾	1	180	200	1	310	330	-	260	275	-	450	490	mA
Cable Equivalent Slope ⁽¹⁾	-0.5	-	1.0	-0.5	-	1.0	-0.5	-	1.0	-0.5	-	1.0	dB
Return Loss (Input/Output) ⁽¹⁾	18	22	-	18	22	-	18	22	-	18	22	ı	dB
Thermal Resistance (□,)	-	-	6.0	-	-	6.0	-	-	6.0	-	-	6.0	©,W

Notes:

- (1) Measured performance of MMIC alone. Balun effects de-imbedded from measurement.
- (2) Measured with a balun on input and output of the device. See Figure 3 for test setup.

⁽³⁾ All parts measured with 110 channel flat input. Parts A and C measured at +34 dBmV output (per channel). Parts B and D measured at +44 dBmV output (per channel).

⁽⁴⁾ A fixed resistor is needed for parts A through C; part D does not need an external resistor (see Table 6.) These resistors set the devices' current draw. Bias voltage is +12 VDC.



Note: Apply voltage to both VD lines simultaneously.

Figure 3: Test Circuit

Table 5: Parts List for Test Circuit

REF	DESCRIPTION	QTY	VENDOR	VENDOR PART NO.
C1, C2, C5, C6	0.01uF chip capacitor	4	Murata	GRM39X7R1103K25V
C3, C4	300pF chip capacitor	2	Murata	GRM39X7R301K25V
C7	47uF Electrolytic CAP	1	Digi-Key Corp.	P5275-ND
L1, L2	390nH air-wound chip inductor	2	Coilcraft	1008CS-391
R1	(see Table 6)	1		
T1, T2 ⁽¹⁾	ferrite core	2	Philips	TC3.4/1.8/1.3-3D3
11, 12	wire		MWS Wire industries	B238611
TVS TVS, 12 Volt, 600 Watt		1	Digi-Key Corp.	SMBJ12ACCCT-ND

Notes:

(1) T1, T2 (balun) wind 4 turns thru core, as shown in Figure 4.

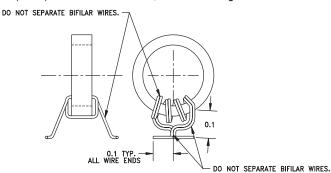


Table 6: R1 Resistor Value

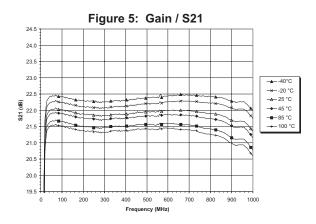
PART NUMBER	R1 VALUE
ACA0861A	21.5 Ohms
ACA0861B	274 Ohms
ACA0861C	121 Ohms
ACA0861D	(open)

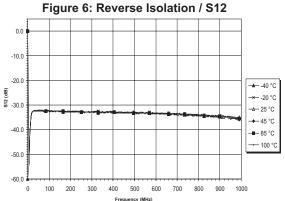
NOTES: 1. MATERIAL:

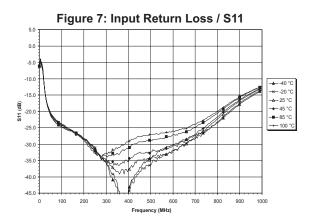
CORE: PHILLIPS (135 CT 050-3D3)
WIRE: MWS WIRE IND.
B2383611(66256-01)
4 TIMES THRU CENTER
AS SHOWN IN FIGURE.

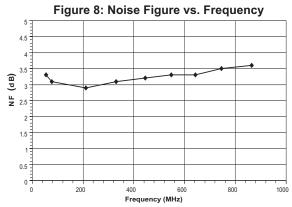
Figure 4: Balun Drawing (4 Turns)

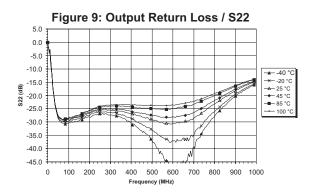
PERFORMANCE DATA

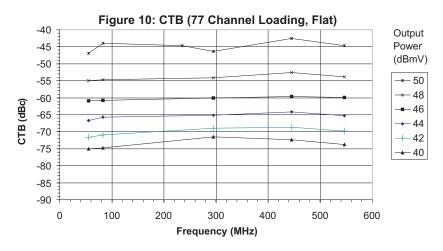


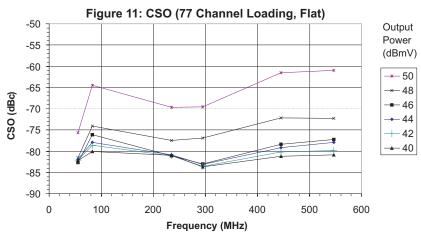


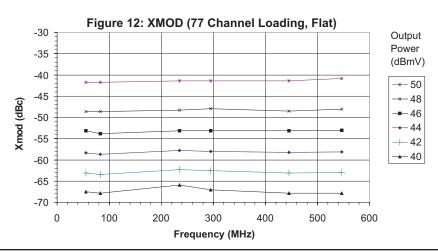


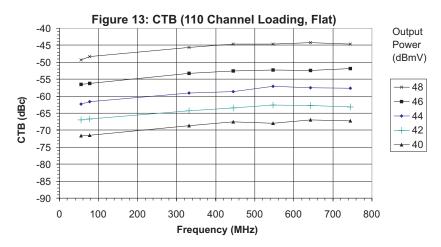


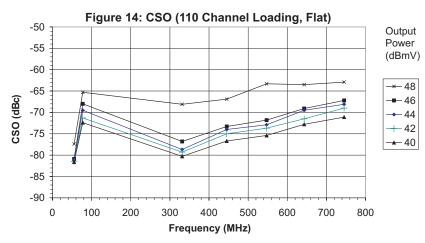


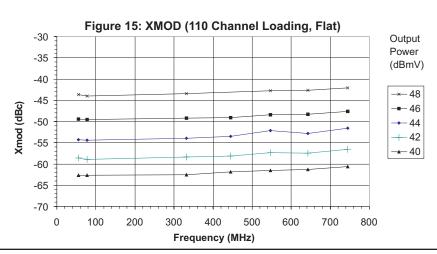


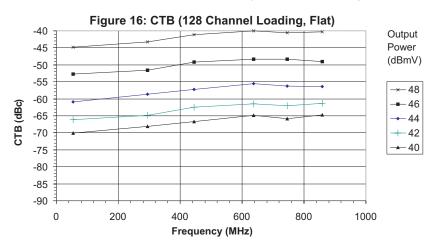


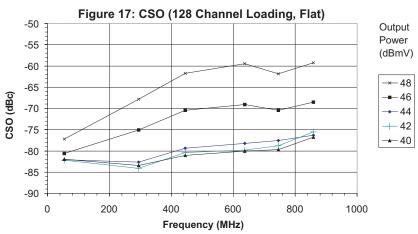


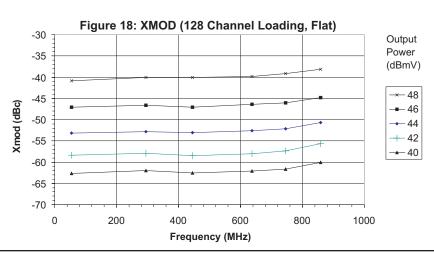


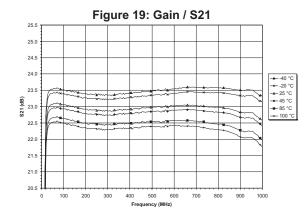


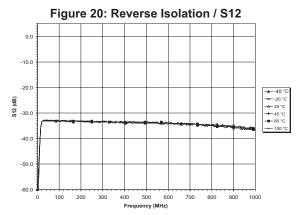


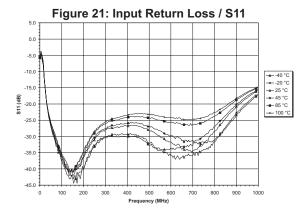


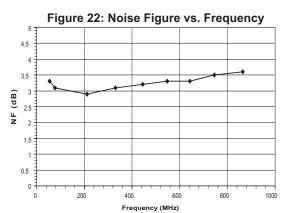












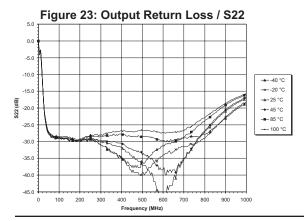
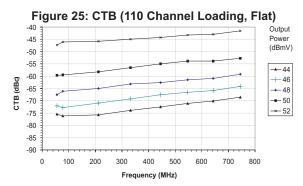
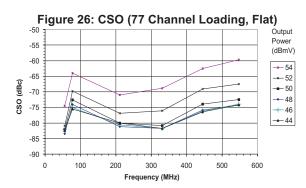
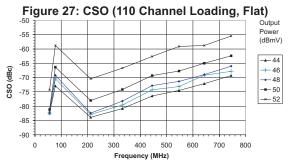
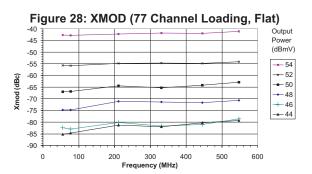


Figure 24: CTB (77 Channel Loading, Flat) Output -45 Power (dBmV) -50 -55 * 54 × 52 -60 **■** 50 -65 → 48 -70 +-46 -75 ---- 44 -80 -85 0 100 200 300 400 500 600 Frequency (MHz)









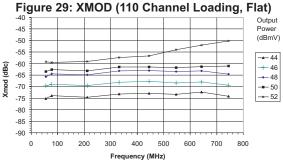
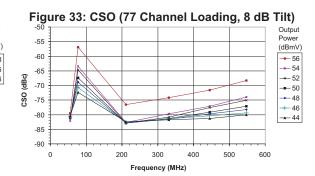


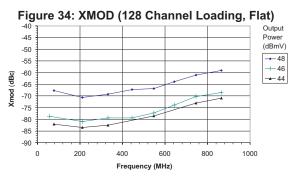
Figure 30: CTB (128 Channel Loading, Flat) -40 Output -45 (dBmV) -50 -55 **→** 48 + 46 -60 CTB (dBc) **-**44 -65 -70 -75 -80 -85 -90 0 200 400 600 800 1000

Frequency (MHz)

Figure 31: CTB (77 Channel Loading, 8 dB Tilt) Output -45 Power (dBmV) -50 • 56 -55 × 54 -60 CTB (dBc) ----- 52 -65 -70 **→** 48 -75 + 46 **→** 44 -80 -85 -90 0 100 200 300 400 500 600 Frequency (MHz)

Figure 32: CSO (128 Channel Loading, Flat) -50 Output Power (dBmV) -55 -60 -- 48 -65 + 46 CSO (dBc) -44 -70 -75 -80 -85 -90 Ω 200 400 600 800 1000 Frequency (MHz)





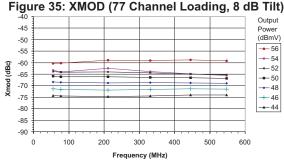


Figure 36: CTB (110 Channel Loading, 10 dB Tilt)

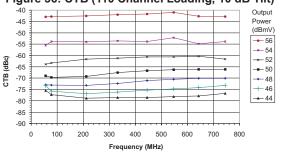


Figure 37: CTB (128 Channel Loading, 12 dB Tilt)

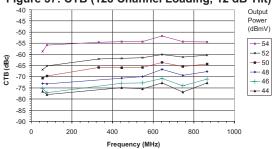


Figure 38: CSO (110 Channel Loading, 10 dB Tilt)

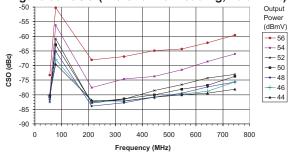


Figure 39: CSO (128 Channel Loading, 12 dB Tilt)

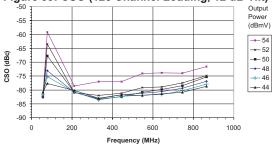


Figure 40: XMOD (110 Channel Loading, 10 dB Tilt)

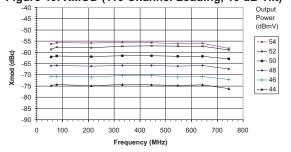
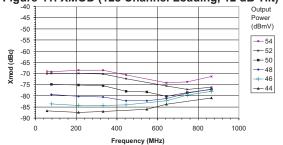
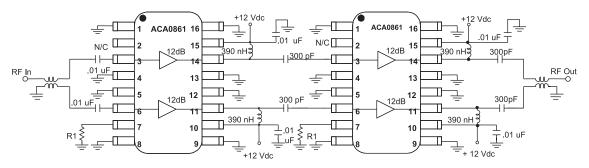


Figure 41: XMOD (128 Channel Loading, 12 dB Tilt)



APPLICATION INFORMATION



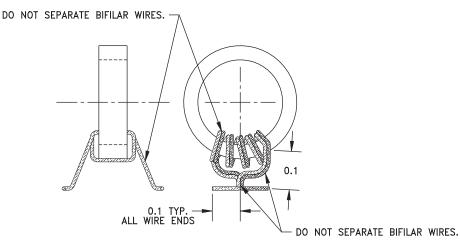
Notes:

- 1. Apply voltage to all +12 Vdc lines simultaneously.
- 2. See Table 6 for R1 values.
- 3. Input and output baluns: wind 5 turns thru core (see Table 7), as shown in Figure 43.

Figure 42: Hybrid Equivalent Test Circuit

Table 7: Parts List for Balun (5 Turns)

PART	VENDOR	VENDOR PART NO.	
ferrite core	Philips	TC3.4/1.8/1.3-3D3	
wire MWS Wire industries		B238611	



NOTES: 1. MATERIAL:

CORE: PHILLIPS (135 CT 050-3D3)

WIRE: MWS WIRE IND.
B2383611(66256-01)
5 TIMES THRU CENTER
AS SHOWN IN FIGURE.

Figure 43: Balun Drawing (5 Turns)

PACKAGE OUTLINE

LE

Q

S

Τ

R

0.052

0°

0.120

0.330

REF. 0.015

8°

0.140

0.350

1.32

0.

3.05

8.38

REF. 0.38

8°

3.56

8.89

5

5

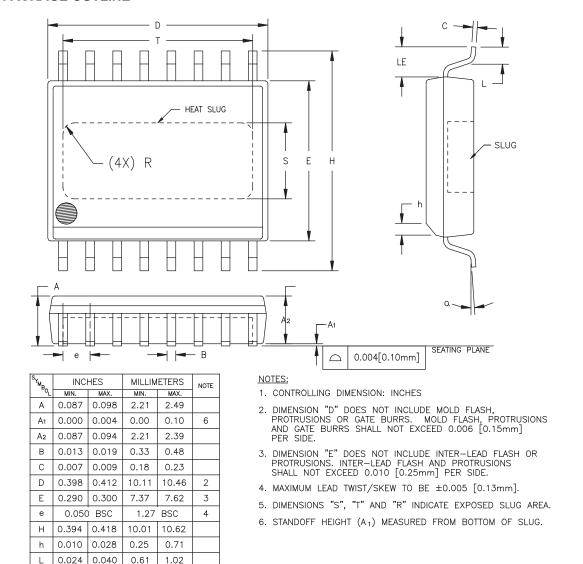


Figure 44: S7 Package Outline - 16 Pin Wide Body SOIC with Heat Slug

ORDERING INFORMATION

ORDER NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
ACA0861AS7CTR	-40 to 110 °C	16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel
ACA0861ARS7P2	-40 to 110 °C	RoHS-Compliant 16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel
ACA0861BS7CTR	-40 to 110 °C	16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel
ACA0861BRS7P2	-40 to 110 °C	RoHS-Compliant 16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel
ACA0861CS7CTR	-40 to 110 °C	16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel
ACA0861CRS7P2	-40 to 110 °C	RoHS-Compliant 16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel
ACA0861DS7CTR	-40 to 110 °C	16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel
ACA0861DRS7P2	-40 to 110 °C	RoHS-Compliant 16 Pin wide Body SOIC with Heat Slug	1,500 piece tape and reel

NOTES

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