

DATA SHEET

SE2565T: 2.4 GHz High Power Wireless LAN Power Amplifier

Applications

- IEEE802.11b DSSS WLAN
- IEEE802.11g,n OFDM WLAN
- High Power Wireless Networking Products

Features

- Dual Mode IEEE802.11b, IEEE802.11g, IEEE802.11n
- 23 dBm, EVM = 3%, 802.11g, OFDM 54 Mbps
- 26 dBm, ACPR < -32 dBc, 802.11b
- Integrated PA, Input Match, 2.8V reference voltage generator
- Integrated Temperature Compensated, Positive Slope Power Detector
- Pb-free, RoHS compliant and Halogen-free
- 3 mm x 3 mm x 0.6 mm QFN, MSL 3

Product Description

The SE2565T is a high power 802.11bgn WLAN power amplifier module providing the functionality of the power amplifier, power detector, reference voltage generator and input match.

The SE2565T is designed for ease of use and maximum flexibility, with an integrated input match, and external output match to adjust the load line for either 3.3V, 23dBm operation.

The SE2565T includes a temperature compensated transmit power detector with over 20 dB of dynamic range and <1.2dB variation under 3:1 mismatch at the antenna.

The SE2565T includes a digital enable control due to an integrated reference voltage generator. The power ramp rise/fall time is 0.5 μ s typical.

Ordering Information

Part No.	Package	Remark
SE2565T	16 pin QFN	Samples
SE2565T-R	16 pin QFN	Tape & Reel
SE2565T-EK1	N/A	Evaluation kit

Functional Block Diagram

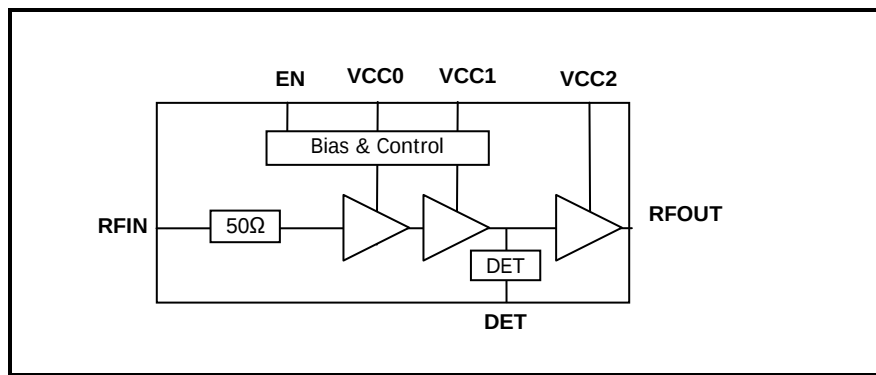


Figure 1: Functional Block Diagram

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Pin Out Diagram

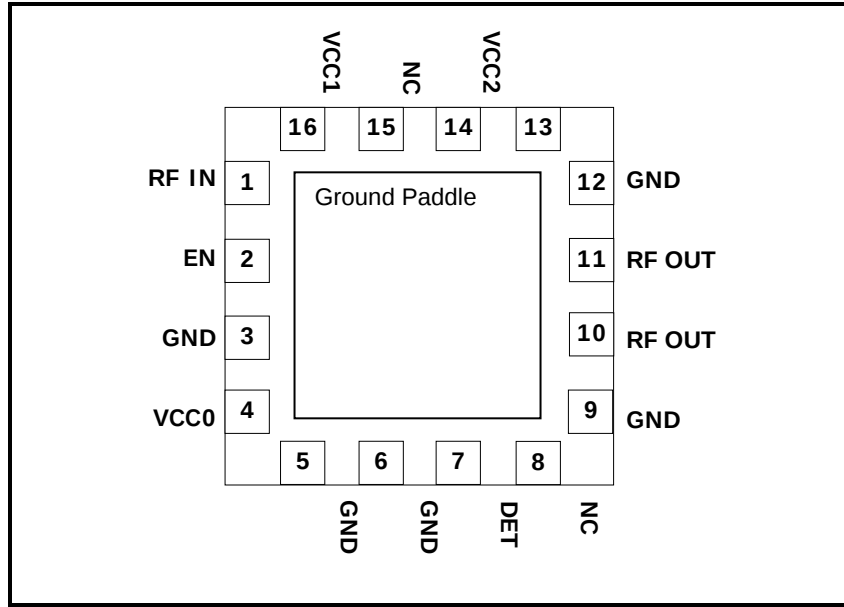


Figure 2: SE2565T Pin Out (Top View Through Package)

Pin Out Description

Pin No.	Name	Description
1	RF IN	RF Input
2	EN	Power Amplifier Enable
3	GND	Ground
4	VCC0	Power Supply for Bias Circuit
5	GND	Ground
6	GND	Ground
7	DET	Power Detector Output
8	NC	No Connect. May be left floating or grounded.
9	GND	Ground
10	RF OUT	RF Output
11	RF OUT	RF Output
12	GND	Ground
13	VCC2	Power Supply for 2 nd Stage
14	NC	No Connect. May be left floating or grounded.
15	VCC1	Power Supply driver stages
16	GND	Ground
Die paddle	GND	Ground

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Absolute Maximum Ratings

These are stress ratings only. Exposure to stresses beyond these maximum ratings may cause permanent damage to, or affect the reliability of the device. Avoid operating the device outside the recommended operating conditions defined below. This device is ESD sensitive. Handling and assembly of this device should be at ESD protected workstations.

Symbol	Definition	Min.	Max.	Unit
VCC	Supply Voltage on VCC	-0.3	4.0	V
V _{IN}	DC input on EN	-0.3	3.6	V
TX	RF Input Power with RF Out terminated in 50Ω	-	12.0	dBm
T _A	Operating Temperature Range	-40	85	°C
T _{STG}	Storage Temperature Range	-40	150	°C
ESD _{HBM}	JEDEC JESD22-A114 all pins	-	1,000	V

Recommended Operating Conditions

Symbol	Parameter	Min.	Typ.	Max.	Unit
T _A	Ambient temperature	-40	25	85	°C
VCC	Supply voltage, relative to GND = 0 V	2.9	3.3	3.6	V

DC Electrical Characteristics

Conditions: VCC = EN = 3.3 V, T_A = 25 °C, as measured on Skyworks Solutions' SE2565T-EV1 evaluation board (de-embedded to device), all unused ports terminated with 50 ohms, unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _{CC-G}	Total Supply Current	P _{OUT} = 23 dBm, 54 Mbps OFDM signal, 64QAM	-	410	-	mA
I _{CC-B}	Total Supply Current	P _{OUT} = 26 dBm, 11 Mbps CCK signal, BT = 0.45	-	450	-	mA
I _{CC-OFF}	Total Supply Current	EN = 0 V, No RF Applied	-	10	100	μA

Logic Characteristics

Conditions: VCC = EN = 3.3 V, T_A = 25 °C, as measured on Skyworks Solutions' SE2565T-EV1 evaluation board (de-embedded to device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V _{ENH}	Logic High Voltage (Module On)	-	1.8	-	V _{CC}	V
V _{ENL}	Logic Low Voltage (Module Off)	-	0	-	0.4	V
I _{ENH}	Input Current Logic High Voltage	-	-	300	-	μA
I _{ENL}	Input Current Logic Low Voltage	V _{ENL} = 0.4V	-	1	50	μA

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Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Z _{EN}	Enable pin input impedance	Passive Pull Down		10		kΩ

AC Electrical Characteristics
802.11g/n Transmit Characteristics

Conditions: VCC = EN = 3.3 V, T_A = 25 °C, as measured on Skyworks Solutions' SE2565T-EV1 evaluation board (de-embedded to device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
F _{IN}	Frequency Range	-	2400	-	2500	MHz
P _{OUT}	Output Power	54 Mbps OFDM signal, 64 QAM, 3% EVM	-	23	-	dBm
		11Mbps CCK signal, BT = 0.045, Mask	-	26	-	
		802.11n, HT20, all data rates, Mask	-	27	-	
		802.11n, HT40, All data rates, Mask	-	24	-	
P _{1dB}	P1dB	-	-	30	-	dBm
S ₂₁	Small Signal Gain	-	30	32	-	dB
ΔS ₂₁	Small Signal Gain Variation	Gain variation over single 40MHz channel	-	0.5	-	dB
		Gain Variation over band	-	1.0	-	
2f	Harmonics	P _{OUT} = 26 dBm, 1 Mbps, 802.11b	-	-50	-45	dBm/MHz
3f			-	-50	-45	dBm/MHz
t _{dr} , t _{df}	Delay and rise/fall Time	50 % of V _{EN} edge and 90/10 % of final output power level	-	0.5	-	μs
S ₁₁	Input Return Loss	-	10	15	-	dB
STAB	Stability	CW, P _{OUT} = 26 dBm 0.1 GHz – 20 GHz Load VSWR = 6:1	All non-harmonically related outputs less than -42 dBm/MHz			
RU	Ruggedness	CW, PIN = +12dBm, Load VSWR = 6:1	No permanent damage.			

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Power Detector Characteristics

Conditions: $V_{CC} = E_N = 3.3\text{ V}$, $T_A = 25\text{ }^{\circ}\text{C}$, as measured on Skyworks Solutions' SE2565T-EV1 evaluation board, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
F_{OUT}	Frequency Range	-	2400	-	2500	MHz
PDR	Power detect range, CW	Measured at RF out	0	-	26	dBm
PDZ_{src}	DC source impedance on PD_OUT	-	-	2.3	-	K Ω
PDZ_{LOAD}	DC load impedance	-	-	26.5	-	K Ω
PDV_{NoRF}	Output Voltage, $P_{OUT} = 5\text{ dBm}$	Measured into 26.5K Ω	-	0.33	-	V
PDV_{p23}	Output Voltage, $P_{OUT} = 23\text{ dBm CW}$	Measured into 26.5K Ω	-	0.70	-	V
PDV_{p27}	Output Voltage, $P_{OUT} = 27\text{ dBm CW}$	Measured into 26.5K Ω	-	0.97	-	V
LPF_{-3dB}	Power detect low pass filter -3dB corner frequency	Measured into 26.5K Ω	-	2.0	-	MHz

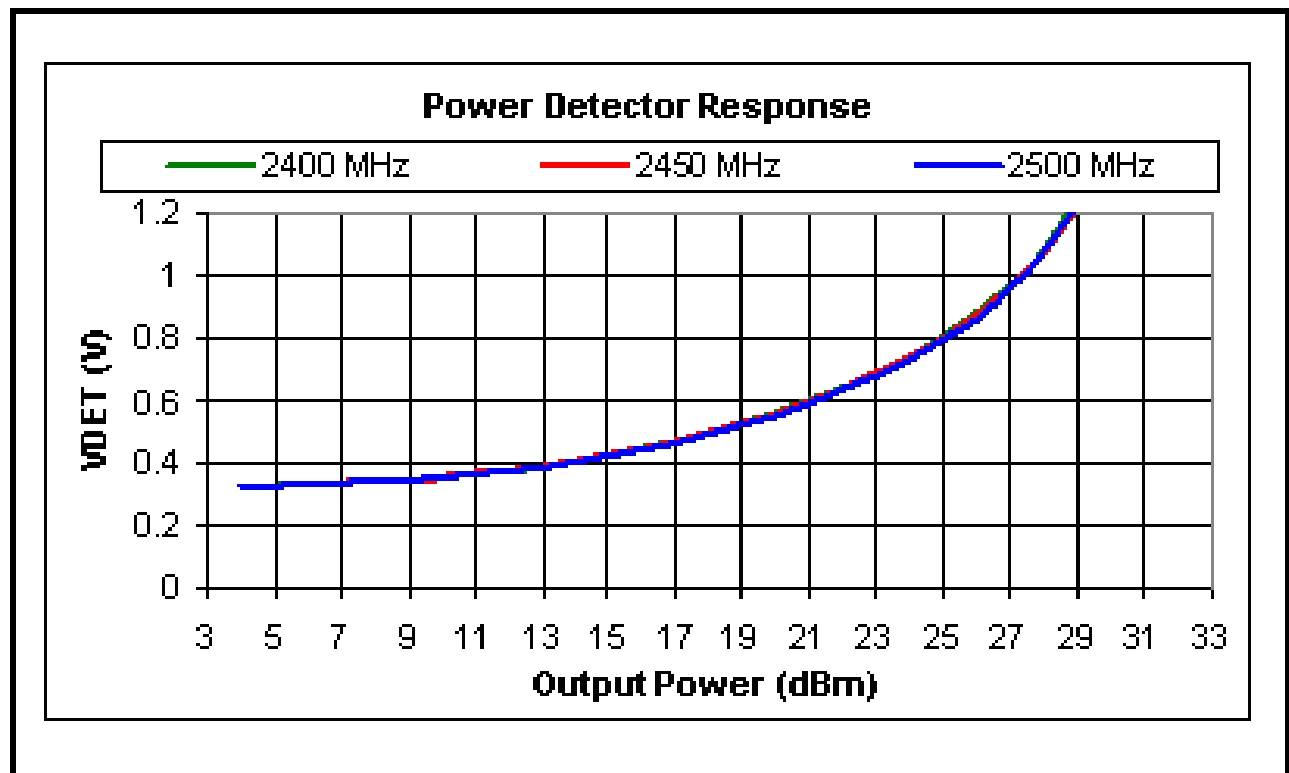


Figure 3: SE2565T Detector Characteristics

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Package Diagram

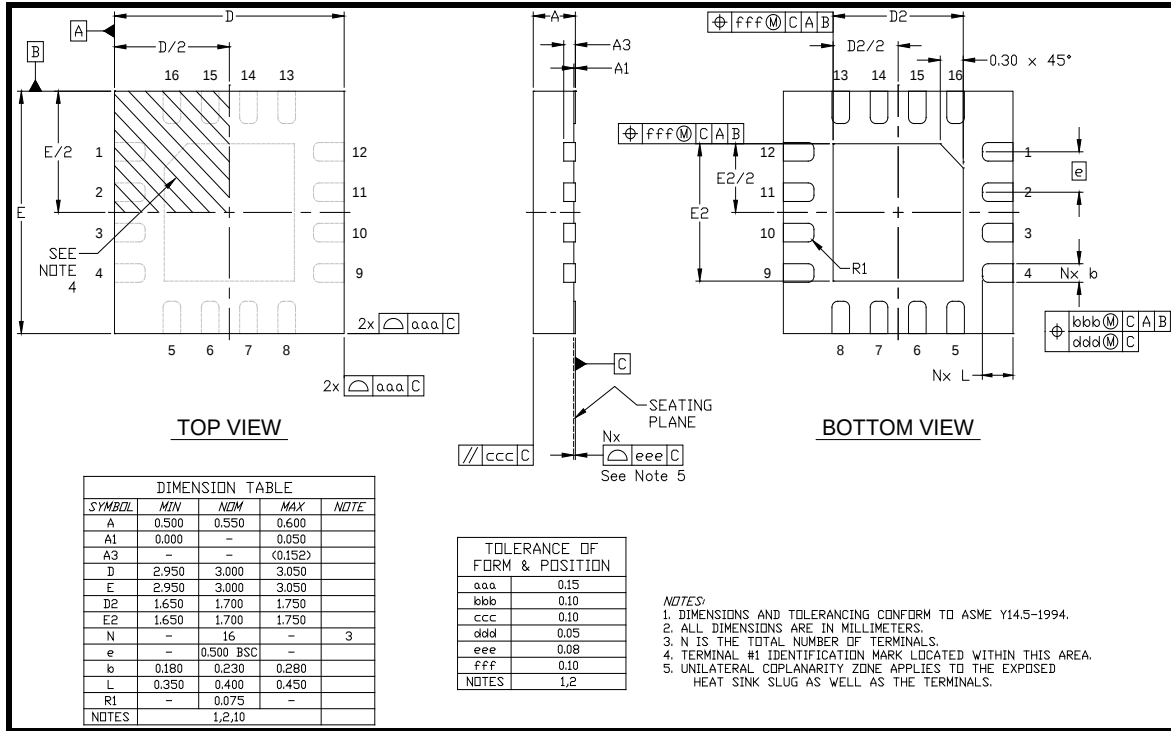
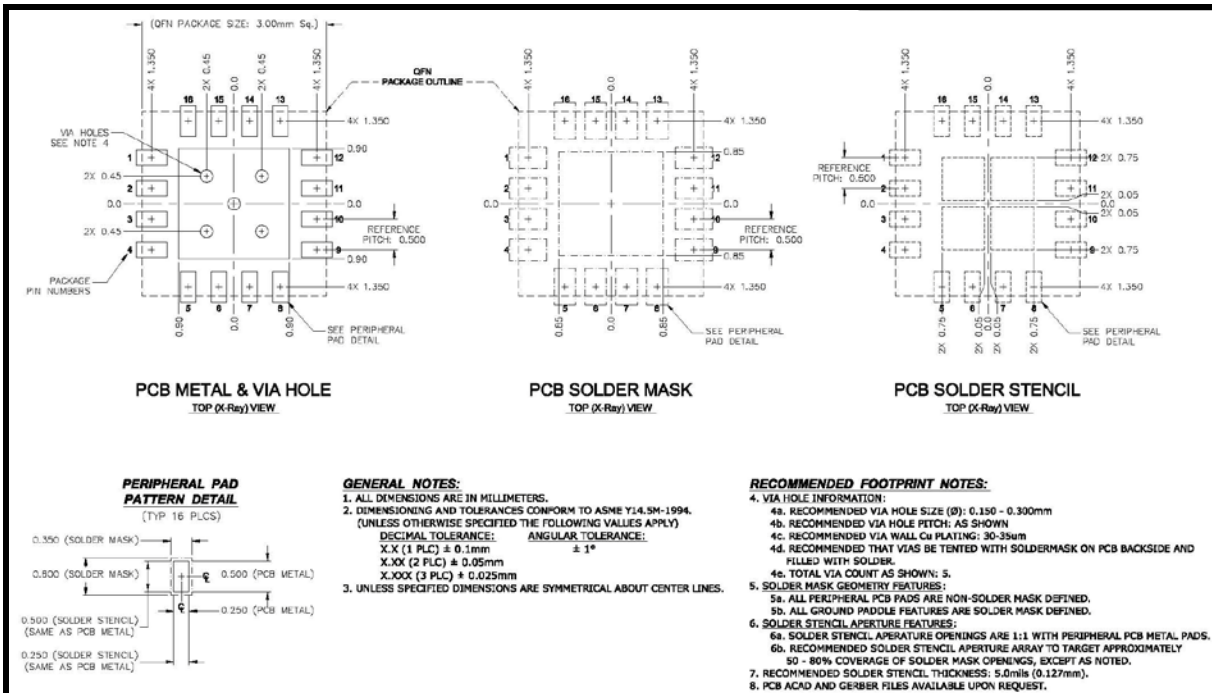


Figure 4: SE2565T Package Diagram

Recommended Land and Solder Patterns



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Figure 5: SE2565T Recommended Land and Solder Pattern

Package Handling Information

Because of its sensitivity to moisture absorption, instructions on the shipping container label must be followed regarding exposure to moisture after the container seal is broken, otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly. The SE2565T is capable of withstanding a Pb free solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is manually attached, precaution should be taken to insure that the device is not subjected to temperatures above its rated peak temperature for an extended period of time. For details on both attachment techniques, precautions, and handling procedures recommended, please refer to:

- “Quad Flat No-Lead Module Solder Reflow & Rework Information”, *Document Number QAD-00045*
- “Handling, Packing, Shipping and Use of Moisture Sensitive QFN”, *Document Number QAD-00044*



Caution! Class 1C ESD sensitive device

Branding Information

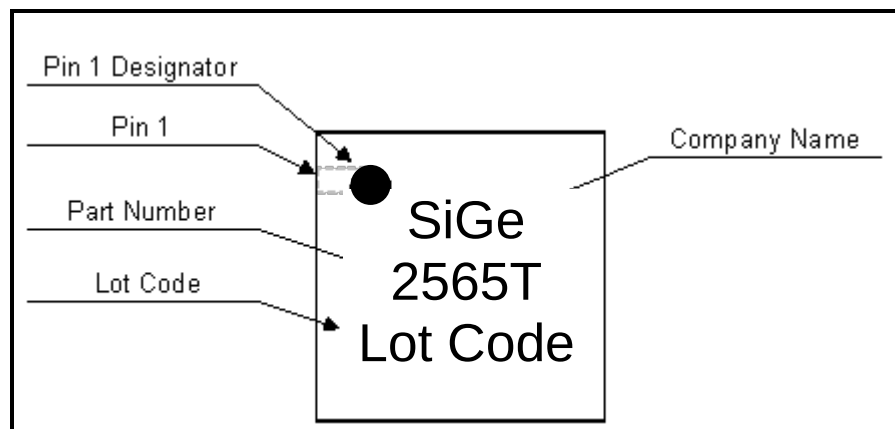


Figure 6: SE2565T Branding

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Tape and Reel Information

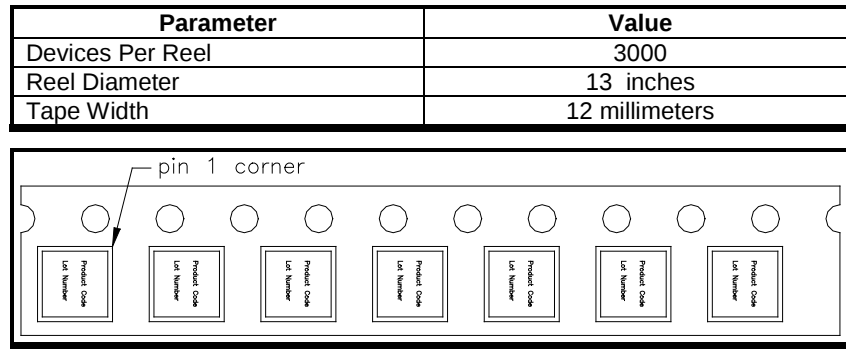


Figure 7: SE2565T-R Tape and Reel Information

Document Change History

Revision	Date	Notes
1.0	Oct 20, 2008	Created
1.1	Dec 19, 2008	Changed pin configuration
1.2	Jan 28, 2009	Updated recommended operating conditions and total current consumption
1.3	May 8, 2009	Updated Package Outline Drawing Updated PA Logic Characteristics
1.4	Jul 8, 2009	Updated to correct pins 8 & 14 definitions.
1.5	Aug 2, 2009	Updated detector characteristics
1.6	Aug 13, 2009	Updated detector characteristics
1.7	Jan 25, 2010	Updated Off State Leakage Updated Detector Characteristics
1.8	Jun 24, 2010	Updated operating temperature range Updated ESD rating to Class 1C
1.9	Dec 18, 2010	Added OFDM Mask Compliance
2.0	Apr 02, 2012	Updated with Skyworks logo and disclaimer statement



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