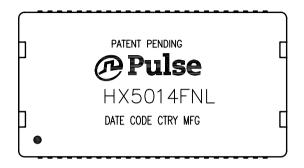


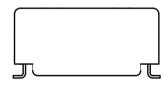


### NOTES:

- ROHS COMPLIANT
- 2. HEADER: DAP (DIALLYL PHTHALATE) WITH FLAMMABILITY RATING UL 94V-0 OR BETTER.
- 3. STORAGE TEMPERATURE: -50°C TO +125°C
- 4. COMPLIANCE TO J-STD:
  - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
  - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
  - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
- 5. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e HX5014FNL BECOMES HX5014FNLT).







FINAL OUTLINE

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,DUAL,1GD,1:1,SM,TU	HX5014FNL-10	PS-2747.001-B	1 OF 3	HX5014FNL	A





#### PORT 1 PORT 2 o 36 CMT5 -o 48 CMT1 TCT5 13 0-TCT1 10-TD1+ 2 0--0 47 MX1+ TD5+ 14 0--0 35 MX5+ $\overline{m}$ TD1- 3 o -0 46 MX1− TD5- 15 o-→ 34 MX5--0 45 CMT2 TCT2 4 0-TCT6 160-- 33 CMT6 TD2+ 5 o--0 44 MX2+ TD6+ 170-- 32 MX6+ -0 43 MX2− -0 31 MX6− TD2- 60 TD6- 180 -o 42 CMT3 -o 30 CMT7 TCT3 7 0-TCT7 19 0-TD3+ 8 0-TD7+ 20 0--○ 41 MX3+ -0 29 MX7+ TD3- 9 o-TD7- 21o--0 28 MX7− -0 40 MX3--0 39 CMT4 -0 27 CMT8 TCT4 100-TCT8 220-TD4+ 110--0 38 MX4+ TD8+ 230--0 26 MX8+ 灬 TD4− 12 0 37 MX4-TD8- 240-- 25 MX8−

**SCHEMATIC** 

## ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS				
OPERATING TEMP	−40°C ~ +85°C				
TURNS RATIO	1.0 ± 2%				
POLARITY	PER SCHEMATIC				
	100 KHz	1-125 MHz			
INSERTION LOSS	-1.2 dB MAX	-0.2-0.002*f <sup>^1.4</sup> dB MAX			
RETURN LOSS	.1-40 MHz	40-100 MHz			
$(Z OUT = 100 OHM \pm 15\%)$	-16 dB MIN	-10+20*LOG <sub>10</sub> (f/80 MHz) dB MIN			
INDUCTANCE (OCL) (MEDIA SIDE, -40°C~+85°C)	350 uH MIN	(MEASURED AT 100 KHz, 100 mVRMS) AND WITH 8 mA DC BIAS			
	1 MHz	10-100 MHz			
CROSSTALK, ADJACENT CHANNELS	-50 dB MIN	-55+22*LOG <sub>10</sub> (f/10) dB MIN			
COMMON MODE	2 MHz	30-200 MHz			
REJECTION RATIO	-50 dB MIN	-15+20*LOG <sub>10</sub> (f/200) dB MIN			
DC RESISTANCE, 1/2 WINDING		.65 OHMS MAX			
DC RESISTANCE IMBALANCE	±.065 OHM	S MAX (CENTER TAP SYMMETRY)			
INPUT — OUTPUT ISOLATION	1500	VRMS MIN @ 60 SECONDS			

NOTE: f IS FREQUENCY IN MHz.

**LEGEND** ⇒ Mx ⇒ M×  $Td \longleftrightarrow Mx$ 

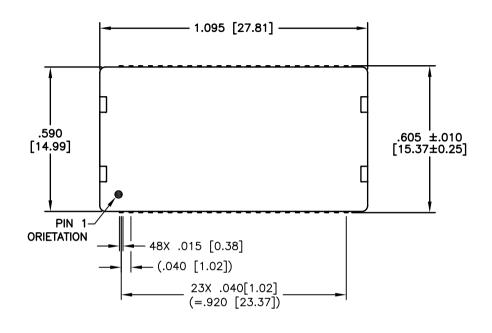
ALL CHANNELS ARE IN PHASE BETWEEN INPUT AND OUTPUT

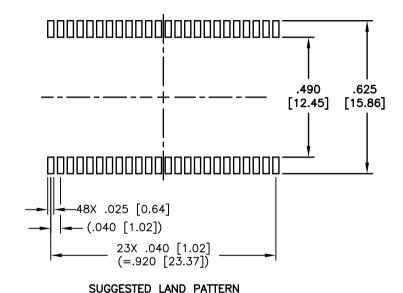
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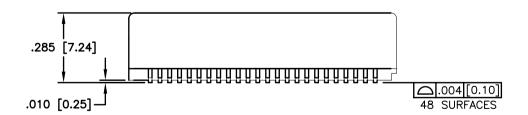
PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,DUAL,1GD,1:1,SM,TU	HX5014FNL-10	PS-2747.001-B	2 OF 3	HX5014FNL	A

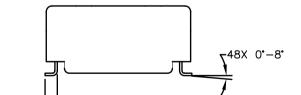












<del>-4</del>8X .036 [0.91]

DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY. .XX=  $\pm$ .01 [ $\pm$ 0.25] .XXX=  $\pm$ .005 [ $\pm$ 0.13]

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,DUAL,1GD,1:1,SM,TU	HX5014FNL-10	PS-2747.001-B	3 OF 3	HX5014FNL	Α

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