Magl<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



## 6V - 36V / 1A / 3.3V, 5V or 12V Output

#### **DESCRIPTION**

The FDSM series of the Magl<sup>3</sup>C Power Module family is a fixed output voltage, fully integrated DC-DC power supply including the controller IC, inductor and capacitors all in one package.

The module requires only an input capacitor and no other external components for operation, reducing design effort and complexity to a minimum.

The FDSM ensures fast time to market and low development costs.

It is pin compatible with the common 78xx linear regulator series. The high efficiency reduces the power dissipation and in many cases a heatsink and assembly parts are unnecessary.

12V to 3.3V conversion achieves up to 87% efficiency. 12V to 5V conversion achieves up to 92% efficiency. 24V to 12V conversion achieves up to 94% efficiency.

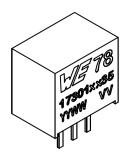
The standard THT (10.4 x 11.6 x 8mm) package allows for easy assembly.

#### **TYPICAL APPLICATIONS**

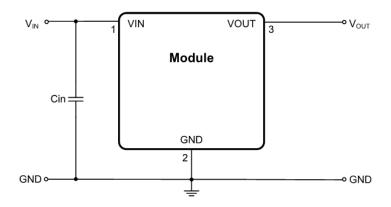
- Point-of-Load DC-DC applications from 9V, 12V, 15V, 18V and 24V industrial rails
- · Replacement for linear regulators
- Interface and microcontroller supplies
- · General purpose

#### **FEATURES**

- · Peak efficiency up to 94%
- · Current capability up to 1A
- Input voltage range: 6V to 36V
- Output voltage: 3.3V (173010335), 5V (173010535) or 12V (173011235)
- Output voltage accuracy:  $\pm$  4% max
- No minimum load required
- · Partially integrated input and output capacitors
- Integrated inductor
- Low output voltage ripple (<20mV<sub>pp</sub>)
- Fixed 520kHz switching frequency
- · Current mode control
- · Pulse skipping for high efficiency at light loads
- · Internal soft-start
- · Thermal shutdown
- · Short circuit protection
- · Cycle by cycle current limit
- Pin compatible with the FDSM power modules series
- Operating ambient temperature range: -40 °C to 85 °C
- RoHS & REACh compliant
- Case and potting material UL 94 Class V0 (flammability testing) certified
- Complies with EN55032 class B conducted and radiated emissions standard



#### **TYPICAL CIRCUIT DIAGRAM**

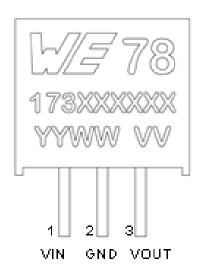


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# **Pinout**



# **MARKING DESCRIPTION**

| MARKING   | DESCRIPTION                                       |  |  |
|-----------|---|--|--|
| WE        | Würth Elektronik eiSos GmbH & Co. KG              |  |  |
| 78        | ndicates compatibility with 78xx linear regulator |  |  |
| 17301xx35 | Order code  |  |  |
| YY        | Year  |  |  |
| WW        | Calendar week                                     |  |  |
| VV        | Output voltage (3.3V, 5V or 12V)                  |  |  |

# **PIN DESCRIPTION**

| SYMBOL | NUMBER | TYPE  | DESCRIPTION   |
|--------|--------|-------|---|
| VIN    | 1      | Power | The supply input pin is a terminal for an unregulated input voltage source. It is recommended to use a $10\mu\text{F/50V}$ input capacitor. |
| GND    | 2      | Power | Ground; reference for V <sub>IN</sub> and V <sub>OUT</sub> .  |
| VOUT   | 3      | Power | Regulated output voltage pin. There is no need for an external output capacitor.  |

MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **ORDERING INFORMATION**

| ORDER CODE | SPECIFICATIONS             | PACKAGE | PACKAGING UNIT      |
|------------|----------------------------|---------|---------------------|
| 173010335  | 36V / 1A / 3.3Vout version |         |                     |
| 173010535  | 36V / 1A / 5Vout version   | SIP-3   | Tube with 42 pieces |
| 173011235  | 36V / 1A / 12Vout version  | ]       |                     |
| 17800FDSM  | Evaluation board           |         |                     |

## PIN COMPATIBLE FAMILY MEMBERS

| ORDER CODE | SPECIFICATIONS        | PACKAGE | PACKAGING UNIT      |  |  |
|------------|-----------------------|---------|---------------------|--|--|
| 173950378  | 28V / 500mA / 3.3Vout |         |                     |  |  |
| 173950578  | 28V / 500mA / 5Vout   |         |                     |  |  |
| 173010378  | 28V / 1A / 3.3Vout    |         |                     |  |  |
| 173010578  | 28V / 1A / 3.3Vout    |         |                     |  |  |
| 173010342  | 42V / 1A / 3.3Vout    | SIP-3   | Tube with 42 pieces |  |  |
| 173010542  | 42V / 1A / 5Vout      |         | Tube with 42 pieces |  |  |
| 173950336  | 36V / 500mA / 3.3Vout |         |                     |  |  |
| 173950536  | 36V / 500mA / 5Vout   |         |                     |  |  |
| 173010335  | 36V / 1A / 3.3Vout    |         |                     |  |  |
| 173010535  | 36V / 1A / 5Vout      |         |                     |  |  |

## **SALES INFORMATION**

# **SALES CONTACT**

Würth Elektronik eiSos GmbH & Co. KG

EMC & Inductive Solutions

Max-Eyth-Str. 1 74638 Waldenburg

Germany

Tel. +49 (0) 7942 945 0

www.we-online.com/powermodules

Technical support: powermodules@we-online.com

MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



## **ABSOLUTE MAXIMUM RATINGS**

#### Caution:

Exceeding the listed absolute maximum ratings may affect the device negatively and may cause permanent damage.

| SVMBOL               | SYMBOL PARAMETER   | LIMIT              |                    | UNIT |
|----------------------|--|--------------------|--------------------|------|
| STMBOL               |  | MIN <sup>(1)</sup> | MAX <sup>(1)</sup> | ONIT |
| VIN                  | Input pin voltage  | -0.3               | 44                 | V    |
| VOUT                 | Output pin voltage   | -0.3               | 44                 | V    |
| T <sub>storage</sub> | Assembled, non-operating storage temperature                 | -40                | 125                | °C   |
| V <sub>esd</sub>     | ESD Voltage (Human Body Model), according to EN61000-R-2 (2) | -4                 | 4                  | kV   |

#### **OPERATING CONDITIONS**

Operating conditions are conditions under which the device is intended to be functional. All values are referenced to GND.

MIN and MAX limits are valid for the recommended ambient temperature range of **-40 °C to 85 °C**. Typical values represent statistically the utmost probable values at the following conditions:  $V_{IN}=6V$  to 36V (173010335),  $V_{IN}=8V$  to 36V (173010535),  $V_{IN}=16V$  to 36V (173011235),  $V_{IN}=16V$ 

| SYMBOL               | PARAMETER                  | MIN <sup>(1)</sup> | TYP <sup>(3)</sup> | MAX <sup>(1)</sup> | UNIT            |
|----------------------|----------------------------|--------------------|--------------------|--------------------|-----------------|
| $V_{IN}$             | Input Voltage (173010335)  | 6                  | -                  | 36                 | V               |
| V <sub>IN</sub>      | Input Voltage (173010535)  |                    | -                  | 36                 | V               |
| V <sub>IN</sub>      | Input Voltage (173011235)  | 16                 | -                  | 36                 | V               |
| Ta                   | Ambient temperature range  | -40                | -                  | 85 <sup>(4)</sup>  | $_{\mathbb{C}}$ |
| I <sub>OUT</sub>     | Nominal output current     | -                  | -                  | 1                  | Α               |
| C <sub>OUT MAX</sub> | Maximal output capacitance | -                  | -                  | 680                | $\mu$ F         |

MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **ELECTRICAL SPECIFICATIONS**

MIN and MAX limits are valid for the recommended ambient temperature range of -40 °C to 85 °C. Typical values represent statistically the utmost probable values at the following conditions:  $V_{IN}$ = 24V (173010335, 173010535 and 173011235),  $I_{OUT}$ = 1A<sup>(5)</sup>,  $I_A$  = 25 °C, unless otherwise noted.

| SYMBOL           | PARAMETER TEST CONDIT             | TEST CONDITIONS   |                    | LIMIT                     |                    | UNIT      |  |
|------------------|-----------------------------------|---|--------------------|---------------------------|--------------------|-----------|--|
| SYMBOL           | STIMBOL PARAMETER 1231 CC         |   | MIN <sup>(1)</sup> | <b>TYP</b> <sup>(3)</sup> | MAX <sup>(1)</sup> | UNIT      |  |
|                  | (                                 | Output Current  |                    |                           |                    |           |  |
| I <sub>CL</sub>  | Current limit threshold           | V <sub>IN</sub> = 24V   | 2.5                | 3.2                       | 3.8                | Α         |  |
|                  | Output Voltage                    |   |                    |                           |                    |           |  |
|                  | Regulated output voltage          | 173010335   | -                  | 3.3                       | -                  | V         |  |
|                  | Regulated output voltage          | 173010535   | -                  | 5                         | -                  | V         |  |
|                  | Regulated output voltage          | 173011235   | -                  | 12                        | -                  | V         |  |
|                  | Line regulation                   | I <sub>OUT</sub> = 1A   | -0.4               | ±0.2                      | 0.4                | %         |  |
| V <sub>OUT</sub> | Load Regulation                   | 10% to 100% load  | -0.6               | ±0.4                      | 0.6                | %         |  |
|                  | Total output voltage regulation   | Full load, input voltage range                                | -4                 | ±2                        | 4                  | %         |  |
|                  | Exterr                            | hal $C_{OUT} = 22\mu F X5R, 20M$                              | Hz BWL             |                           |                    |           |  |
|                  |                                   | $V_{OUT} = 3.3V, I_{OUT} = 1A$                                | -                  | 13.5                      | -                  | $mV_{pp}$ |  |
|                  | Output voltage ripple             | $V_{OUT} = 5V$ , $I_{OUT} = 1A$                               | -                  | 17.5                      | -                  | $mV_{pp}$ |  |
|                  |                                   | $V_{OUT} = 12V$ , $I_{OUT} = 1A$                              | -                  | 25                        | -                  | $mV_pp$   |  |
|                  | Swi                               | tching Frequency  |                    |                           |                    |           |  |
| f <sub>SW</sub>  | Switching frequency               | V <sub>IN</sub> = 12V, Continuous<br>conduction mode<br>(CCM) | -                  | 520                       | -                  | kHz       |  |
|                  |                                   | Input Current   |                    |                           |                    |           |  |
| I <sub>IN</sub>  | No load input current             | Operating, switching  | -                  | 0.1                       | 1                  | mA        |  |
|                  |                                   | Efficiency  |                    |                           |                    |           |  |
|                  |                                   | $V_{IN} = 6V, V_{OUT} = 3.3V$                                 | -                  | 92                        | -                  | %         |  |
|                  |                                   | $V_{IN} = 36V, V_{OUT} = 3.3V$                                | -                  | 81                        | -                  | %         |  |
| $\eta$           | Efficiency, I <sub>OUT</sub> = 1A | $V_{IN} = 8V, V_{OUT} = 5V$                                   | -                  | 94                        | _                  | %         |  |
| "                |                                   | $V_{IN} = 36V, V_{OUT} = 5V$                                  | -                  | 86                        | -                  | %         |  |
|                  |                                   | $V_{IN} = 16V, V_{OUT} = 12V$                                 | -                  | 95                        | -                  | %         |  |
|                  |                                   | $V_{IN} = 36V, V_{OUT} = 12V$                                 | -                  | 92                        | -                  | %         |  |

Magl<sup>3</sup>C Power Module

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# RoHS, REACh

RoHS directive

REACh directive



Directive 2011/65/EU of the European Parliament and the Council of June 8th, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Directive 1907/2006/EU of the European Parliament and the Council of June 1st, 2007 regarding the Registration, Evaluation, Authorization and Restriction of Chemicals (REACh).

#### PACKAGE SPECIFICATIONS

| ITEM             | PARAMETER   | TYP <sup>(3)</sup>      | UNIT |
|------------------|---|-------------------------|------|
| Case             | Black flame-retardant and heat-resistant plastic (UL94 V-0) | -                       | -    |
| Potting material | Silicone, UL94V-0   | -                       | -    |
| Weight           |   | 1.8                     | g    |
| Vibration        | 5g for 20min  | MIL-STD-202, Method 204 |      |

## **NOTES**

- (1) Min and Max are 100% production tested at 25°C. Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods.
- (2) The human body model is a 100pF capacitor discharged through a 1.5 k $\Omega$  resistor into each pin. Test method is per JESD-22-114.
- (3) Typical numbers are valid at 25° C ambient temperature and represent statistically the utmost probability assuming the Gaussian distribution.
- (4) Depending on load current, see derating diagram.
- (5) Measured without heatsink, no airflow.

Magl<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



#### TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply:  $V_{IN} = 24V$ ;  $V_{OUT} = 3.3V$  (173010335),  $V_{OUT} = 5V$  (173010535) and  $V_{OUT} = 12V$  (173011235);  $I_{OUT} = 1A^{(5)}$ ;  $T_{AMB} = 25$  °C.

# RADIATED AND CONDUCTED EMISSIONS EN55032 (CISPR-32) CLASS B COMPLIANT

The 173010335, 173010535 and 173011235 power modules are tested with a standard EMC configuration (1m wire between the module and the load) to give more realistic information about implementation in the applications. The test setup is based on CISPR16 with the limit values CISPR32.

Measured with module on an Evaluation Board 17800FDSM in a Fully Anechoic Room (FAR) at 3m antenna distance.

## **TEST SETUP**

Input wire length:

• Radiated Emission: 160cm (80cm Horizontal + 80cm Vertical)

· Conducted Emission: 80cm

Output wire length:

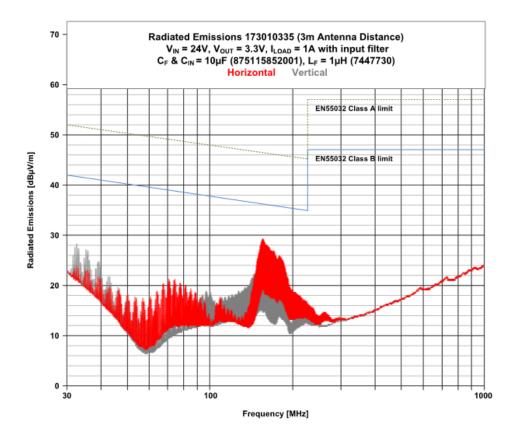
· Long wire (with input filter): 1m

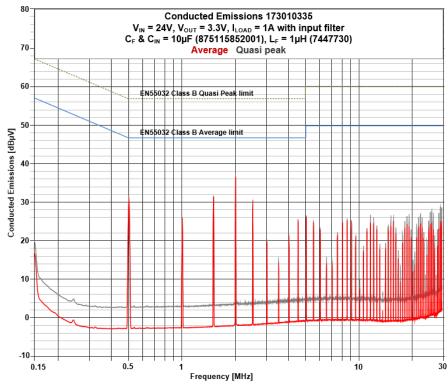
MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **RADIATED AND CONDUCTED EMISSIONS - 173010335**



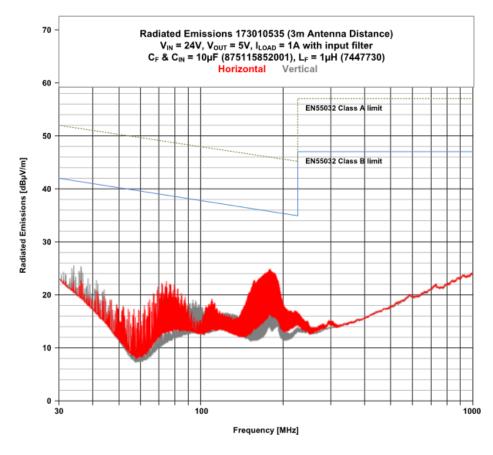


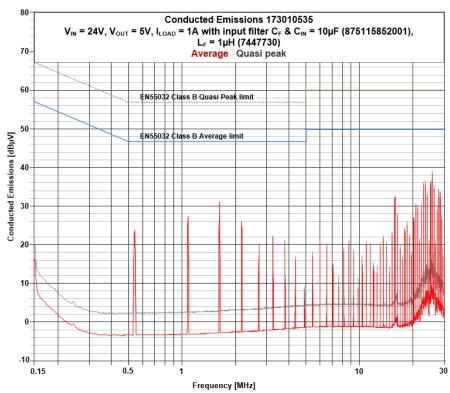
MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **RADIATED AND CONDUCTED EMISSIONS - 173010535**



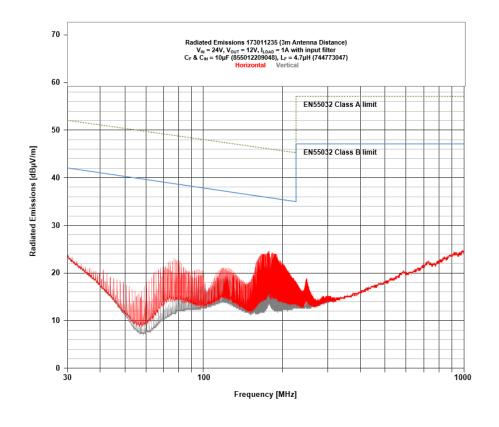


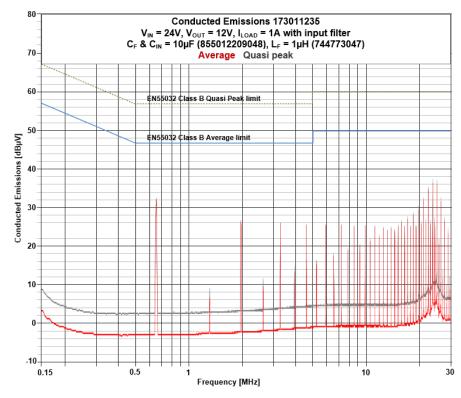
Magl<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **RADIATED AND CONDUCTED EMISSIONS - 173011235**



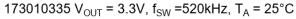


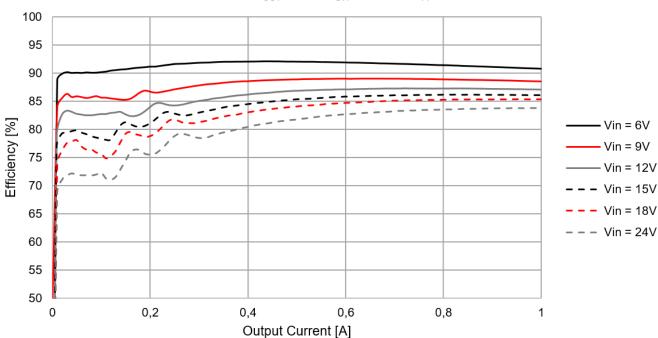
MagI<sup>3</sup>C Power Module

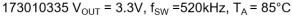
FDSM - Fixed Step Down Regulator Module

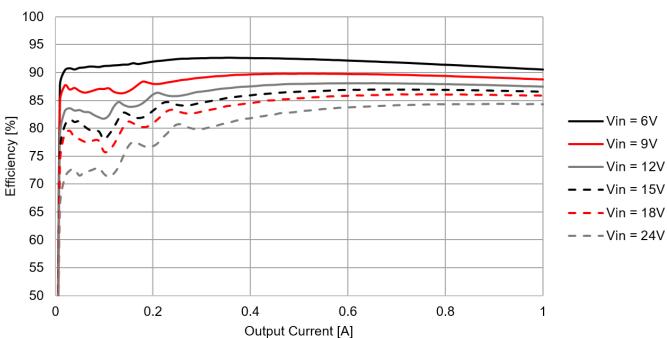


# **EFFICIENCY - 173010335**







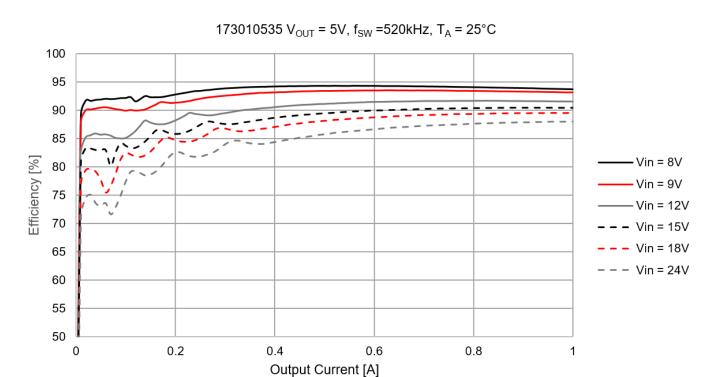


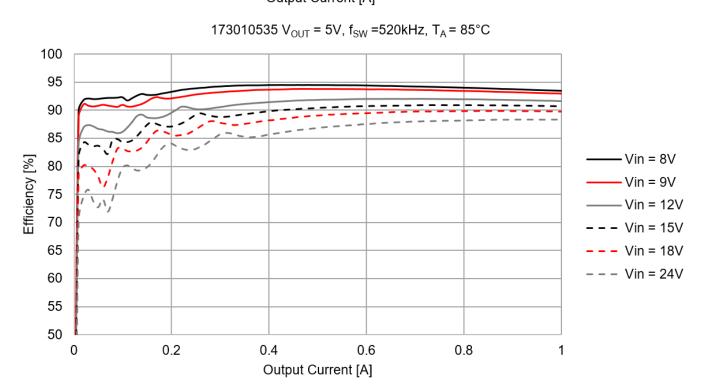
MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **EFFICIENCY - 173010535**





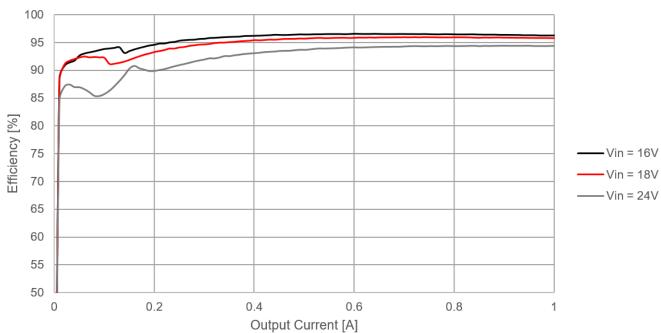
Magl<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module

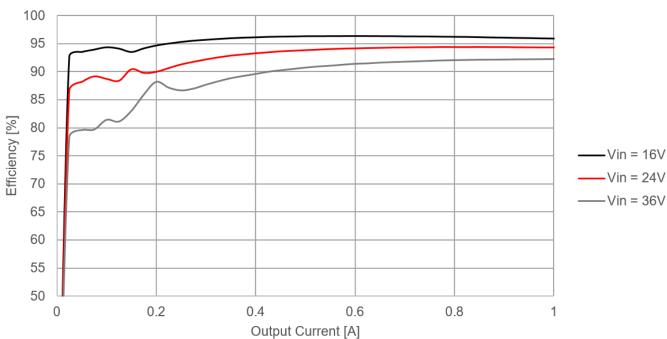


# **EFFICIENCY - 173011235**





173011235  $V_{OUT}$  = 12V,  $f_{SW}$  =520kHz,  $T_A$  = 85°C

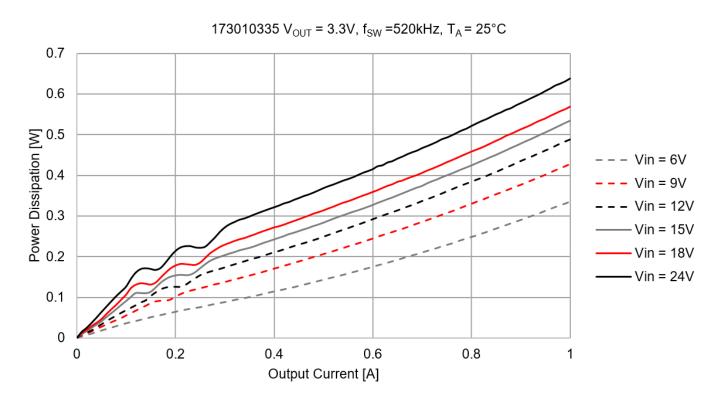


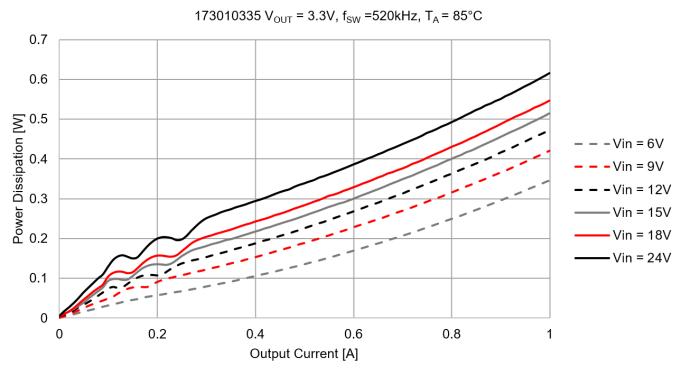
MagI<sup>3</sup>C Power Module

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# **POWER DISSIPATION - 173010335**



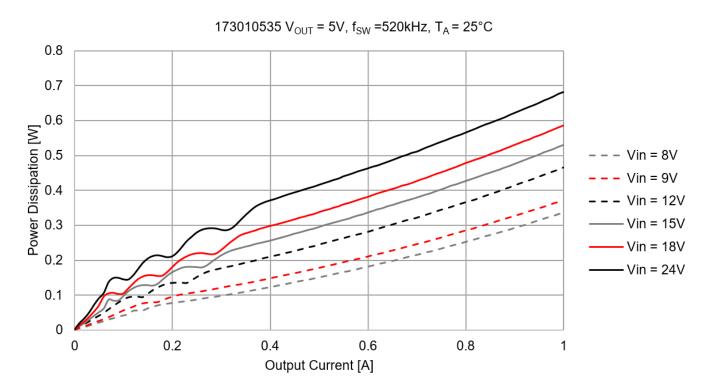


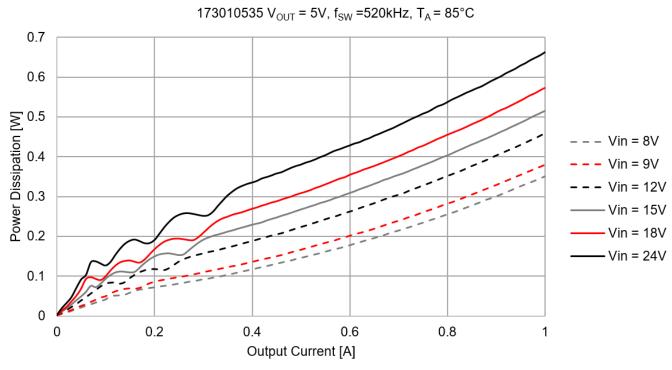
MagI<sup>3</sup>C Power Module

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# **POWER DISSIPATION - 173010535**



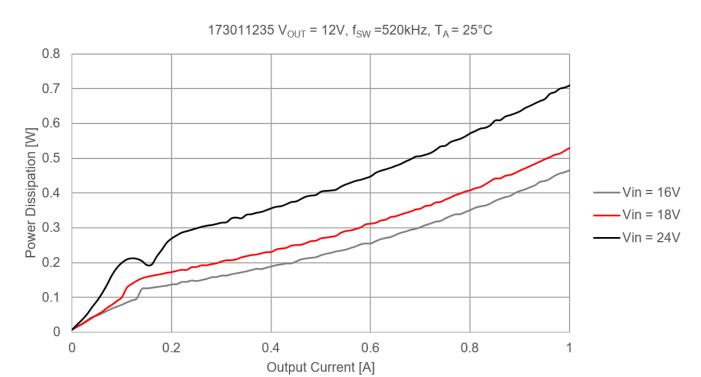


MagI<sup>3</sup>C Power Module

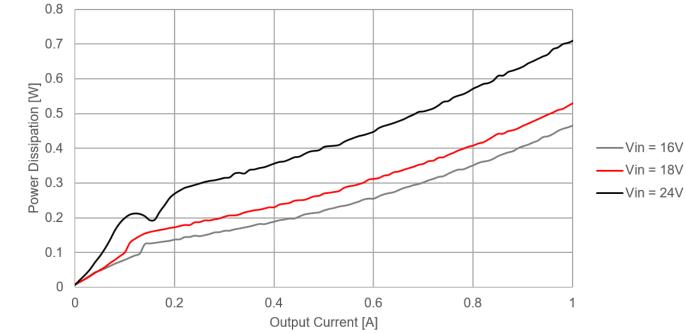
FDSM - Fixed Step Down Regulator Module



# **POWER DISSIPATION - 173011235**





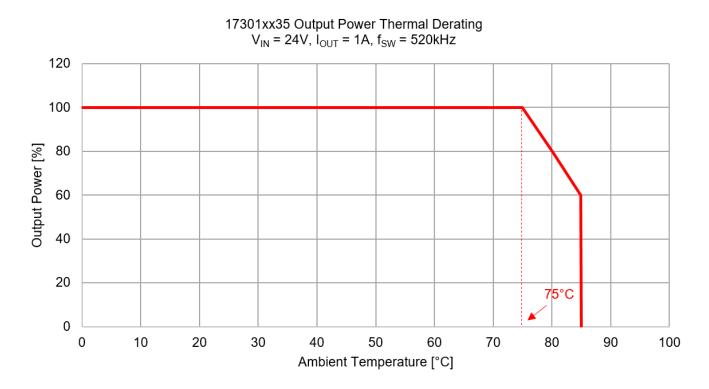


MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **THERMAL DERATING - 17301xx35**

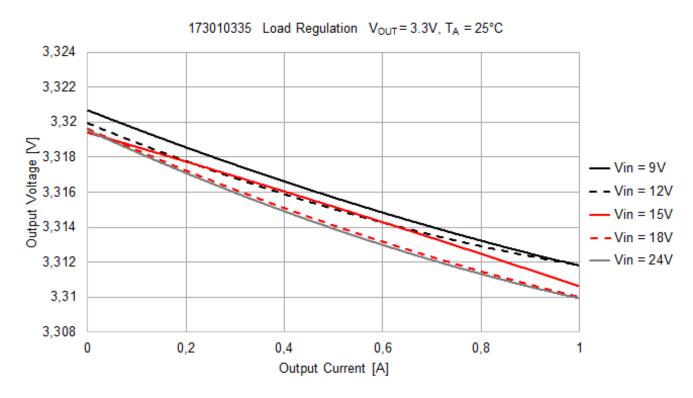


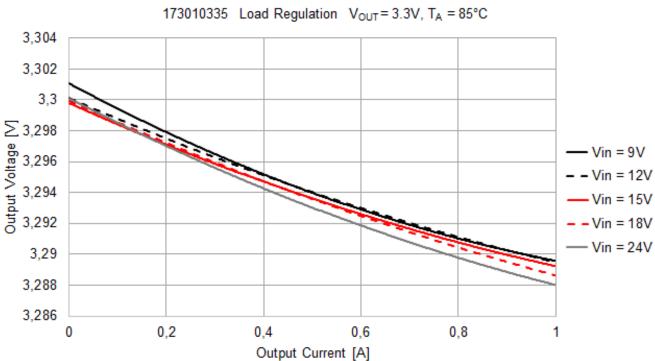
MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **LOAD REGULATION - 173010335**



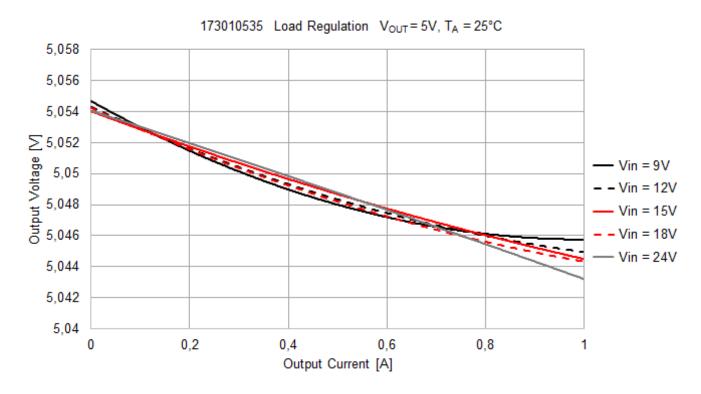


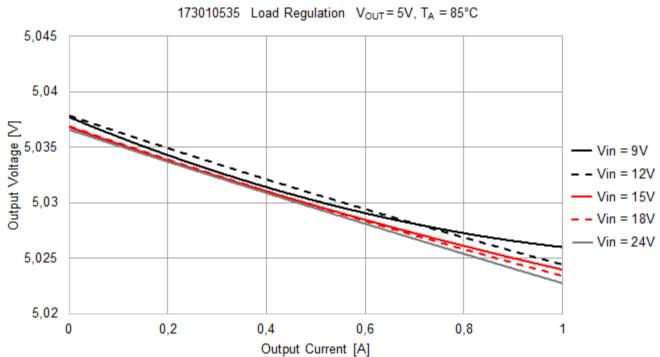
MagI<sup>3</sup>C Power Module

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# **LOAD REGULATION - 173010535**



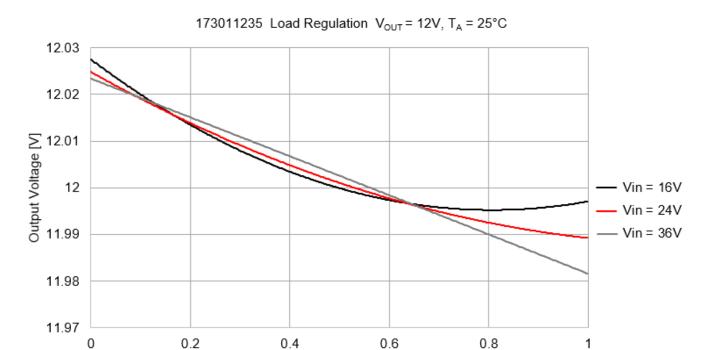


Magl<sup>3</sup>C Power Module

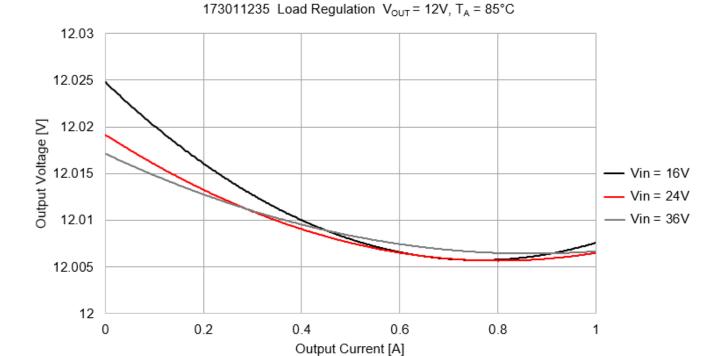
FDSM - Fixed Step Down Regulator Module



# **LOAD REGULATION - 173011235**



Output Current [A]

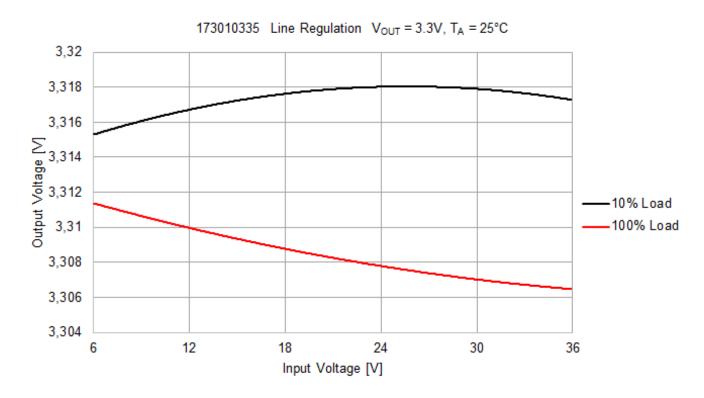


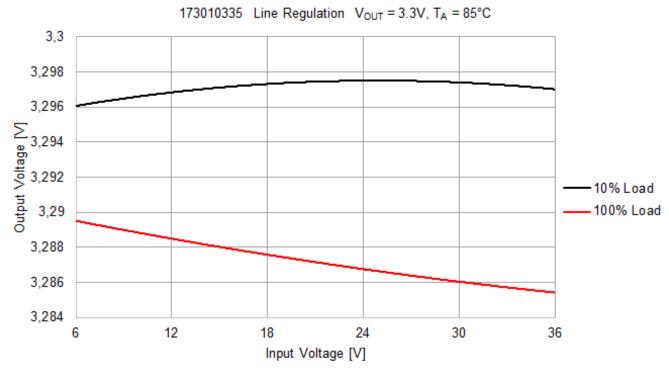
MagI<sup>3</sup>C Power Module

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# **LINE REGULATION - 173010335**



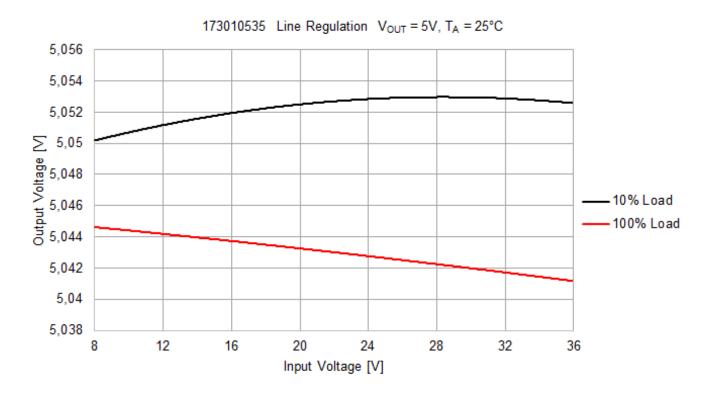


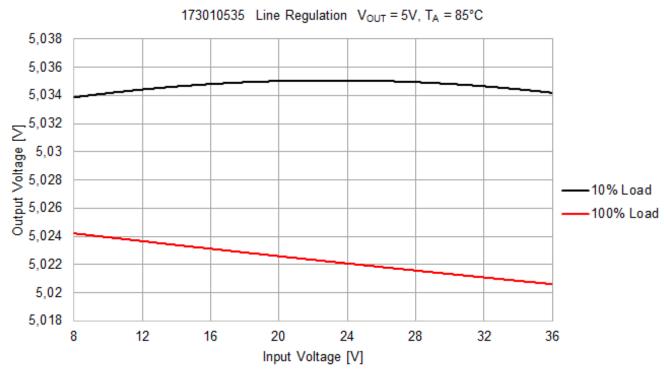
MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



# **LINE REGULATION - 173010535**



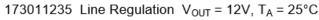


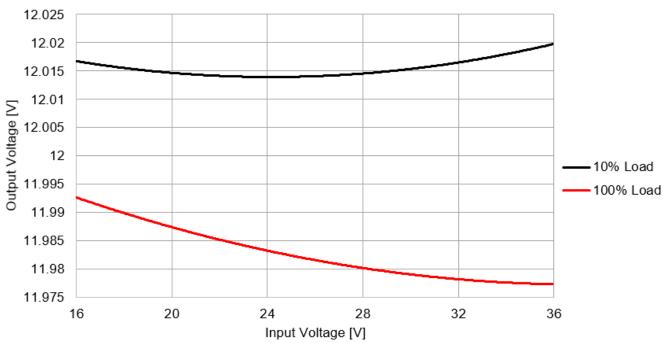
Magl<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module

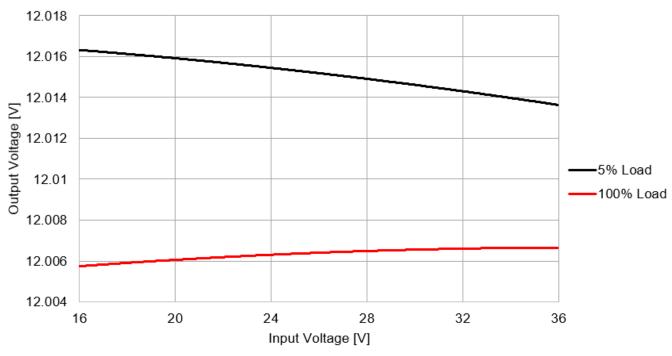


## **LINE REGULATION - 173011235**





173011235 Line Regulation  $V_{OUT} = 12V$ ,  $T_A = 85$ °C

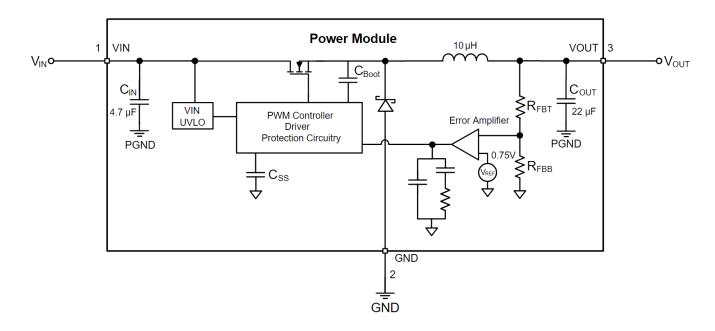


Magl<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module



#### **BLOCK DIAGRAM**



## **CIRCUIT DESCRIPTION**

The Magl<sup>3</sup>C power modules 173010335, 173010535 and 173011235 are all based on a non-synchronous step-down regulator with integrated MOSFET, free-wheeling diode, power inductor, input and output capacitors. The control scheme is based on a current mode (CM) regulation loop.

The  $V_{OUT}$  of the regulator is divided with the internal feedback resistor network and fed into the error amplifier, which compares this signal with the internal 0.75V reference. The error amplifier controls the on-time of a fixed frequency pulse width generator, which drives the MOSFET.

The current mode architecture features a constant frequency during load steps. Only the on-time is modulated. It is internally compensated and stable with low ESR output capacitors. No external compensation network is required. This architecture supports fast transient response and very small output voltage ripple values ( $<20 \text{mV}_{pp}$ ) are achieved.

MagI<sup>3</sup>C Power Module

FDSM - Fixed Step Down Regulator Module

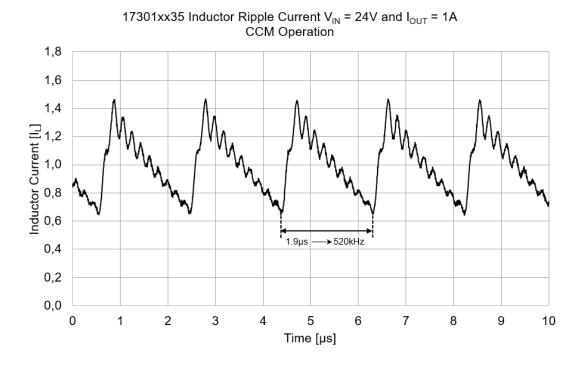


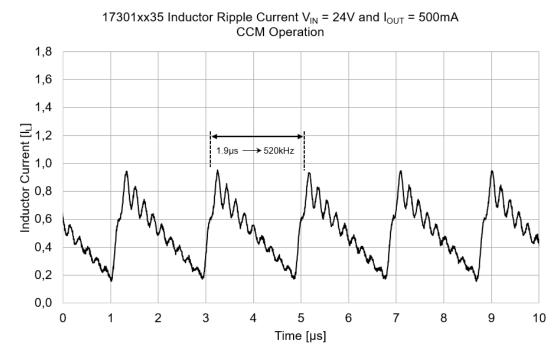
## **LIGHT LOAD OPERATION**

Under light load operation, the device switches from PWM mode to PFM mode. The load current where the transition between modes takes place can be estimated using the following formula:

$$I_{\text{OUT(DCM)}} = \frac{V_{\text{OUT}} \cdot (1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}})}{2 \cdot f_{\text{SW}} \cdot L} \tag{1}$$

The following figures show the device working in PWM mode with continuous conduction operation (CCM).



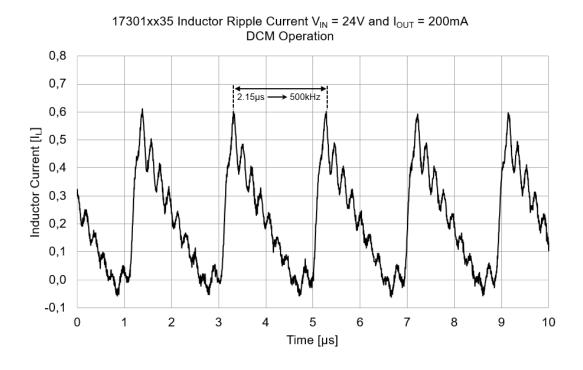


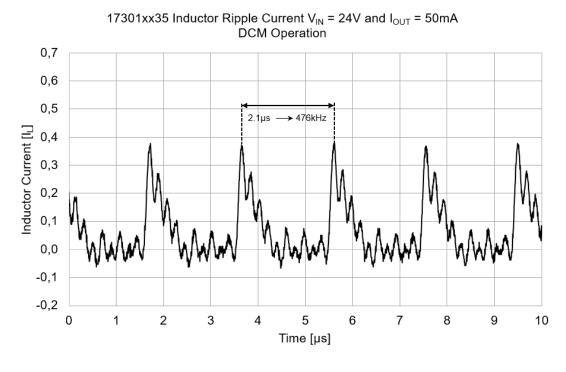
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FDSM - Fixed Step Down Regulator Module



If the load current is further reduced, the device decreases the switching frequency in order to limit the energy transferred to the output (to both the output capacitor and load) maintaining regulation of the output voltage. The frequency reduction is shown in the figure below during PFM mode, with discontinuous operation (DCM).





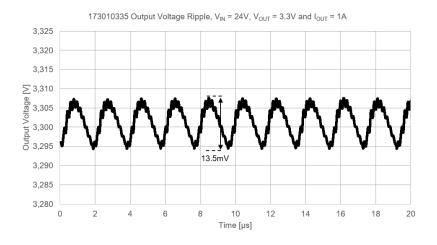
MagI<sup>3</sup>C Power Module

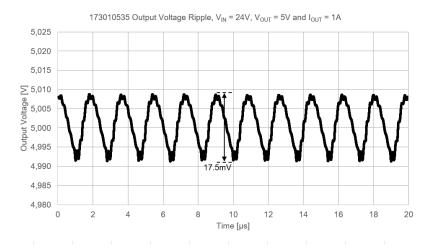
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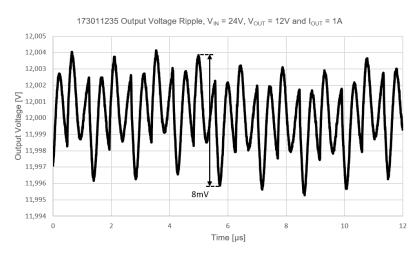


## **OUTPUT VOLTAGE RIPPLE**

The output voltage ripple depends on several parameters. The figure below shows the  $V_{OUT}$  ripple at full load using a  $22\mu F$  MLCC output capacitor. An output voltage ripple of less than  $20mV_{pp}$  is measured under the conditions indicated.







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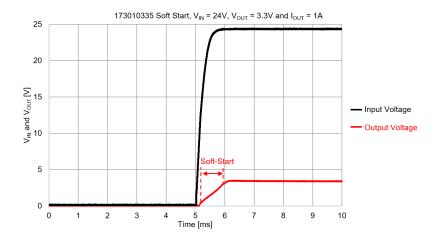
FDSM - Fixed Step Down Regulator Module

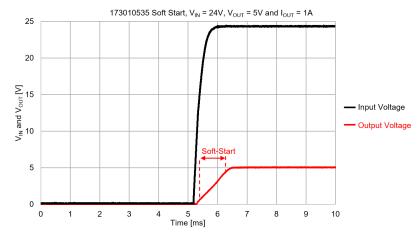


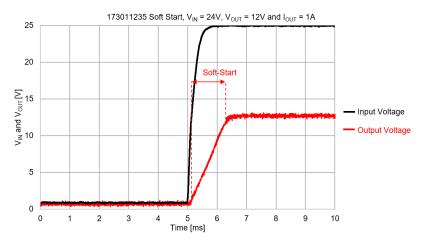
## **PROTECTION FEATURES**

#### Soft-Start

In order to prevent the output voltage from overshooting during start-up, a soft-start is internally set. The figures below show the start-up behavior of the power module with the 3.3V, 5V and 12V output voltage, respectively.







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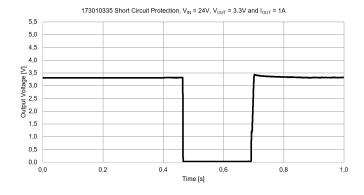


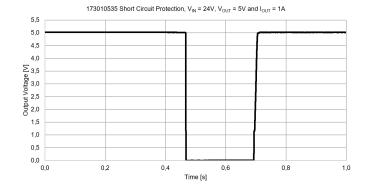
#### **Over Temperature Protection (OTP)**

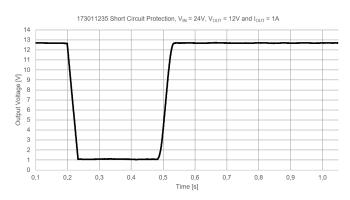
Thermal protection helps to prevent catastrophic failures due to accidental device overheating. The junction temperature of the Magl $^3$ C power module should not be allowed to exceed its maximum ratings. Thermal protection is implemented by an internal thermal shutdown circuit which activates at 170 °C (typ.), causing the device to enter a low power standby state. In this state, the MOSFET remains off, causing  $V_{OUT}$  to fall. When the junction temperature falls back below 158 °C (typ.) (hysteresis is implemented)  $V_{OUT}$  rises smoothly and normal operation resumes.

## **Short Circuit Protection (SCP)**

The short circuit protection is realized via cycle by cycle current monitoring and a frequency foldback scheme where the off time of the high side switch increases relative to a decrease of the feedback voltage. For example, the power module switching frequency is divided by 2, 4 and 8 as the feedback voltage decreases to 75%, 50% and 25%, respectively. Recovery from short circuit protection mode occurs during the switching cycle following the removal of the short circuit condition. When the power module recovers from a short circuit condition, the soft-start will not activate. Therefore, an overshoot at the output voltage can be observed (see figure below). Under short circuit conditions, the input current is limited.







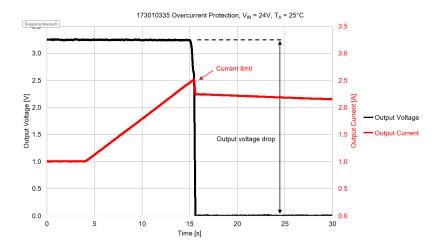
MagI<sup>3</sup>C Power Module

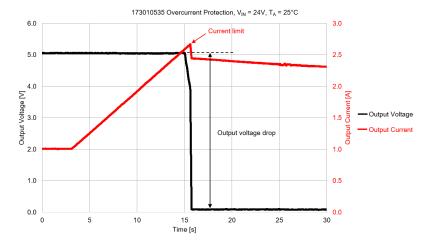
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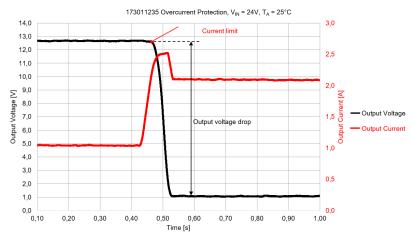


## **Over Current Protection (OCP)**

For protection against load faults, the power module incorporates cycle by cycle current monitoring. During an overcurrent condition the output current is limited and the output voltage drops. When the overcurrent condition is removed, the output voltage returns to the nominal voltage.





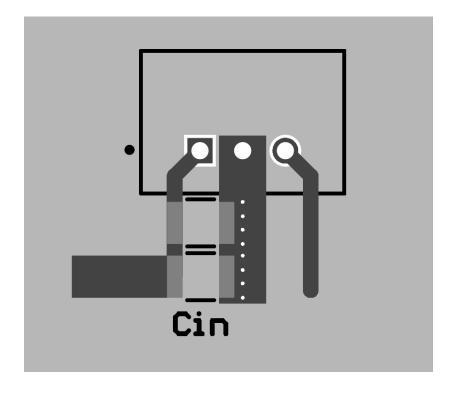


MagI<sup>3</sup>C Power Module

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# LAYOUT RECOMMENDATION



The picture above shows a possible layout for the 17301xx35 family. Nevertheless, the following recommendation should be followed when designing the layout:

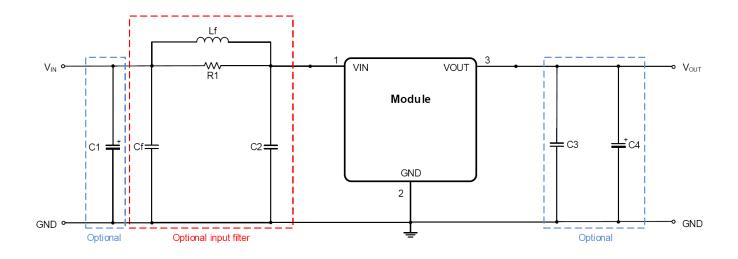
1. The input capacitor should be placed as close as possible to the VIN pin of the device.

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#### **Evaluation Board Schematic**



The 17301xx35 family integrates both the input and output capacitors. It is also recommended to use a  $10\mu\text{F}$  input capacitor, C2, for high impedance input wires or traces and a  $22\mu\text{F}$  output capacitor, C3, for applications where a low output voltage ripple is required.

The additional  $100\mu$ F aluminum electrolytic capacitor, C1, is mounted at the termination of the supply line and provides a slight damping of possible oscillations of the series resonance circuit represented by the inductance of the supply line and the input capacitance.

The additional MLCC,  $C_f$ , is part of the input filter and is not mounted on the board. The inductor,  $L_f$ , is not mounted too (see recommended part number in the table below). A zero ohm resistor,  $R_1$ , is mounted in parallel with  $L_f$ . In case the input filter is placed,  $R_1$  must be removed and an appropriate  $L_f$  mounted.

In case particular application requirements are demanding additional capacitance, the evaluation board gives the possibility to place additional capacitance at the output:  $C_4$  (surface mounted electrolytic). This capacitor allows for the fine tuning of the load transient response of the output voltage.

## **Bill of Materials**

| Symbol         | Description  | Quantity | Order Code   | Manufacturer     |
|----------------|--|----------|--------------|------------------|
| U1             | Magl <sup>3</sup> C Power Module (not mounted)                         | 1        | 17301xx35    | Würth Elektronik |
| C <sub>1</sub> | Aluminum electrolytic capacitor, ATG5 family, $100\mu F/50V$           | 1        | 860010674014 | Würth Elektronik |
| C <sub>2</sub> | Al-Poly capacitor 10 $\mu$ F/63V (not mounted)                         | 1        | 875115852001 | Würth Elektronik |
| C <sub>3</sub> | Ceramic chip capacitor 10 $\mu$ F/16V X5R, 0805 (173010335, 173010535) | 1        | 885012107014 | Würth Elektronik |
| C <sub>4</sub> | Ceramic chip capacitor 10 $\mu$ F/10V X5R, 0805 (173011235)            | 1        | 885012005009 | Würth Elektronik |
| C <sub>f</sub> | Ceramic chip capacitor $4.7\mu\text{F}/16\text{V X5R},$ 0805           | 1        | 885012107018 | Würth Elektronik |
| L <sub>f</sub> | Ceramic chip capacitor $10\mu\text{F}/16\text{V X5R},$ 0805            | 1        | 885012107014 | Würth Elektronik |
| R <sub>1</sub> | SMD bridge 0   | 1        |              |                  |

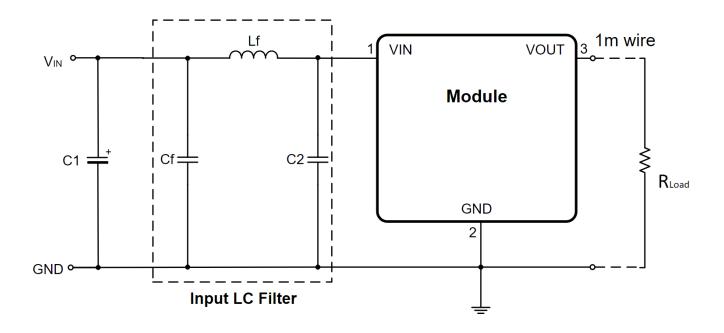
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# Filter Suggestion for Conducted EMI

The input filter shown in the schematic below is recommended to achieve conducted compliance according to EN55032 and CISPR32 Class B norms (see results on page 7). For radiated EMI, the filter is not necessary. It is only used to comply with the setup recommended in the norms.



Bill of Materials of the Input LC Filter ( $V_{IN} = 24V$ ,  $V_{OUT} = 3.3V$ , 5V and 12V,  $I_{OUT} = 1A$ )

| Symbol         | Description   | Order Code   | Manufacturer     |
|----------------|---|--------------|------------------|
| C <sub>2</sub> | Al-Poly capacitor $10\mu\text{F/63V}$   | 875115852001 | Würth Elektronik |
| C <sub>f</sub> | Filter Al-Poly capacitor 10µF/63V   | 875115852001 | Würth Elektronik |
| L <sub>f</sub> | Filter inductor, 1 $\mu$ H, PD2 family, I <sub>SAT</sub> = 5.72A, I <sub>R</sub> = 4A (for 171010335 and 171010535) | 7447730      | Würth Elektronik |
| L <sub>f</sub> | Filter inductor, 1 $\mu$ H, PD2 family, I <sub>SAT</sub> = 5.72A, I <sub>R</sub> = 4A (for 171011235)               | 744773047    | Würth Elektronik |

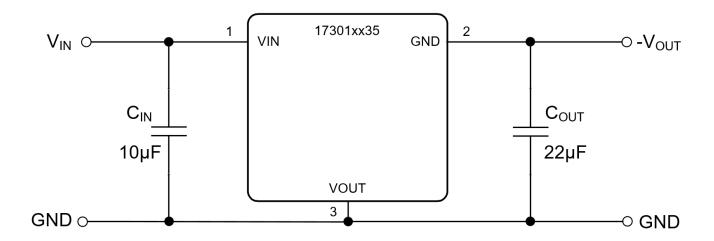
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## **Generating Negative Output Voltage**

Many industrial applications require negative voltages. The 17301xx35 family can easily provide a negative voltage using the circuit shown below. The module VOUT pin is attached to the application ground and the module GND pin is used to provide the output voltage. For low output voltage ripple, it is recommended to use an additional  $22\mu$ F external capacitor at the output of the module.



It is important to be aware that in this configuration the power module must withstand the sum of the input voltage and the absolute value of the output voltage  $(V_{IN} + |V_{OUT}|)$ , instead of just the input voltage. This means that the maximum operating voltage should be limited to  $36V - |V_{OUT}|$  (e.g. if the 173010535 is used in this configuration, the input voltage should not exceed 31V). Moreover, the maximum output current of this configuration is no longer 1A; instead it must be reduced according to the following formula (this is also illustrated in the graph below):

$$I_{\text{OUT(DCM)}} = \frac{V_{\text{OUT}} \cdot (1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}})}{2 \cdot f_{\text{SW}} * L} \tag{2}$$

where D is the duty cycle, in this case defined according to:

$$I_{\text{OUT(DCM)}} = \frac{V_{\text{OUT}} \cdot (1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}})}{2 \cdot f_{\text{SW}} * L}$$
(3)

For additional information, please refer to the Application Note ANS007b.

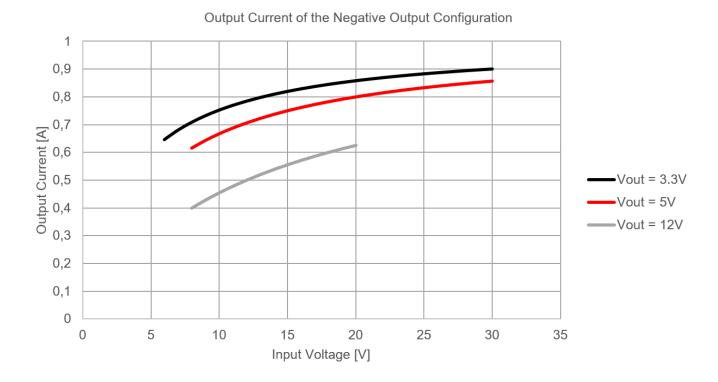
## **Starting Conditions for Generating Negative Output Voltage:**

 $V_{IN\_MIN} = 6V$  (173010335),  $V_{IN\_MIN} = 8V$  (173010535) and  $V_{IN\_MIN} = 8V$  (173011235)

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Compared with a standard positive buck configuration, the negative output buck contains an additional critical loop (between  $V_{IN}$  and  $V_{OUT}$ ), which needs an additional capacitor  $C_{IN}$ , as shown in the circuit above.

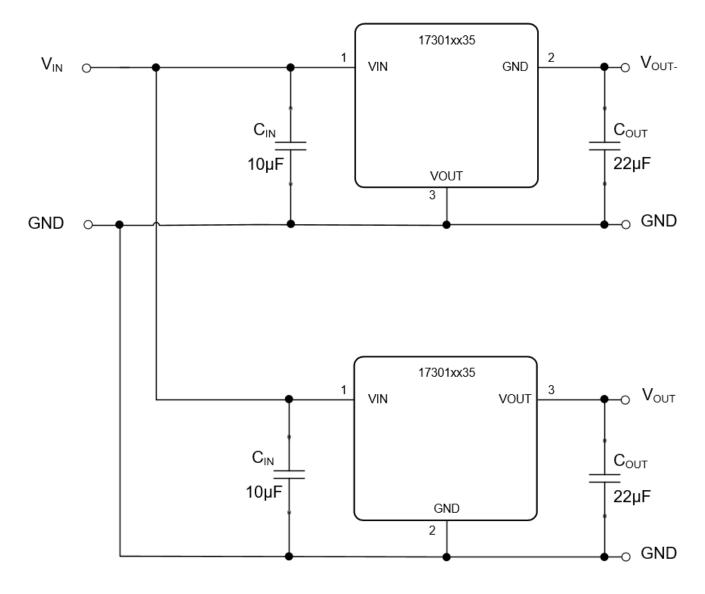
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# **Generating Complementary Output Voltage**

Another common requirement in industrial applications is to provide a complementary voltage (e.g.  $\pm 5$ V). The circuit below shows how this target can be achieved simply combining one of the 17301xx35 family used in a standard configuration (delivering a positive output voltage) with the above mentioned solution for negative voltages. For low output voltage ripple, it is recommended to use an additional  $22\mu$ F external capacitor at the output of the module.



Complementary Output Voltage

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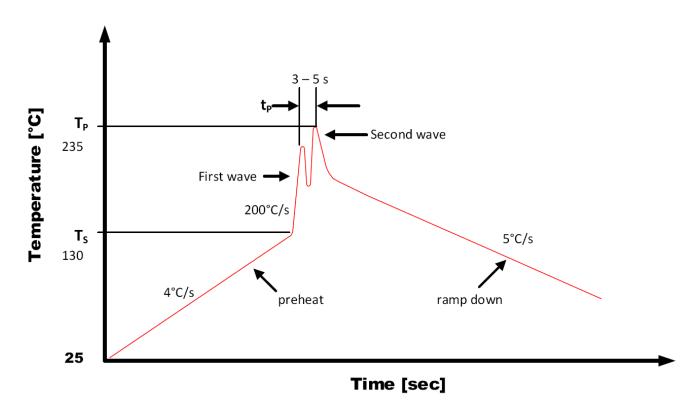
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# **WAVE SOLDER PROFILE**

| Profile Feature                             | Old standard (Pb) | New (Pb-free)    |
|---|-------------------|------------------|
| Time within peak temperature t <sub>p</sub> | 10s               | 10s              |
| Average ramp-up rate                        | 200 <i>°</i> C/s  | 200℃/s           |
| Final preheat temperature Ts                | 130 <i>°</i> C/s  | 130 <i>°</i> C/s |
| Peak temperature T <sub>p</sub>             | +235℃/s           | +260 ℃/s         |
| Ramp-down rate                              | -5℃/s             | -5°C/s           |
| Heating rate during preheat                 | 4℃/s              | 4 ℃/s            |

# Wave Solder Diagram:

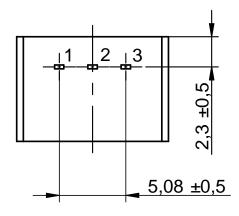


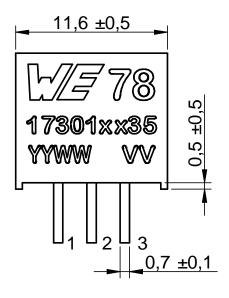
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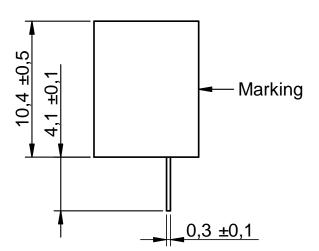


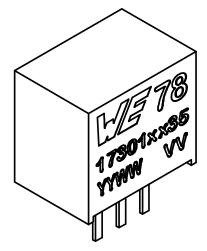
# **PHYSICAL DIMENSIONS**











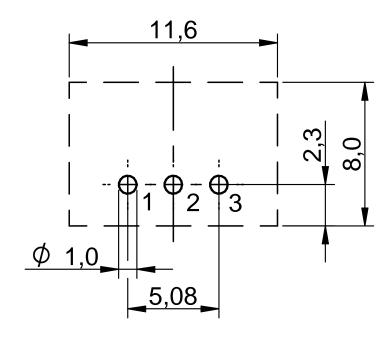
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# RECOMMENDED DRILL HOLES



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# **DOCUMENT HISTORY**

| Revision | Date          | Description                   | Comment |
|----------|---------------|-------------------------------|---------|
| 1.0      | November 2020 | Initial data sheet release    |         |
| 2.0      | April 2021    | Added 173011235 family member |         |

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