VT4060C, VIT4060C

Vishay General Semiconductor

# **Dual Trench MOS Barrier Schottky Rectifier**

Ultra Low V<sub>F</sub> = 0.32 V at I<sub>F</sub> = 5.0 A



- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- AEC-Q101 qualified Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL APPLICATIONS**

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB and TO-262AA Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

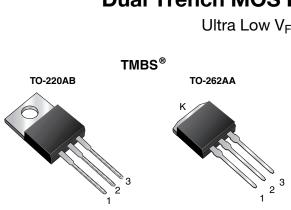
Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VT4060C	VIT4060C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub> 60		0	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	40		A	
	per diode		20			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	240		А	
Voltage rate of change (rated $V_R$ )		dV/dt	10 000		V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150		°C	



VT30L60C	VIT30L60C	

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	240 A			
$V_F$ at $I_F = 20$ A	0.48 V			
T <sub>J</sub> max.	150 °C			
Package	TO-220AB, TO-262AA			
Diode variation	Dual common cathode			

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RoHS COMPLIANT

HALOGEN FREE

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C		0.43	-	V
	I <sub>F</sub> = 10 A			0.48	0.48 -	
	I <sub>F</sub> = 20 A		V <sub>E</sub> (1)	0.53	0.62	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C	VF ()	0.32	-	
	I <sub>F</sub> = 10 A			0.39	-	
	I <sub>F</sub> = 20 A			0.48	0.57	
Reverse current per diode	\/ <sub>−</sub> − 60 \/	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	6.0	
	$V_{\rm R} = 60 \text{ V}$ $T_{\rm A} = 125$	T <sub>A</sub> = 125 °C	IR (=/	34	190	mA

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VT4060C	VIT4060C	UNIT
Typical thermal resistance	per diode	P		.5	°C/W
	per device	$R_{ ext{ heta}JC}$	0.8		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	VT4060C-M3/4W	1.89	4W	50/tube	Tube	
TO-262AA	VIT4060C-M3/4W	1.46	4W	50/tube	Tube	
TO-220AB	VT4060CHM3/4W <sup>(1)</sup>	1.89	4W	50/tube	Tube	
TO-262AA	VIT4060CHM3/4W <sup>(1)</sup>	1.46	4W	50/tube	Tube	

Note

(1) AEC-Q101 qualified



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#### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

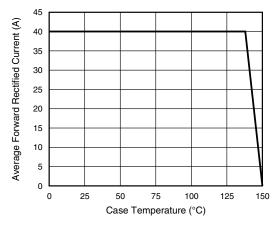


Fig. 1 - Maximum Forward Current Derating Curve

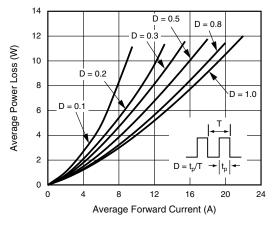


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

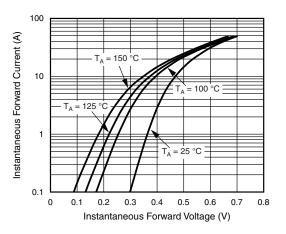


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

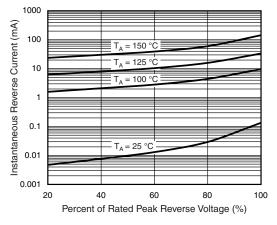


Fig. 4 - Typical Reverse Characteristics Per Diode

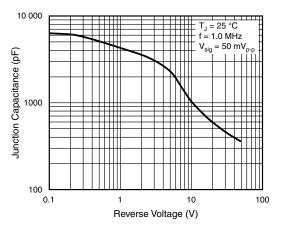


Fig. 5 - Typical Junction Capacitance Per Diode

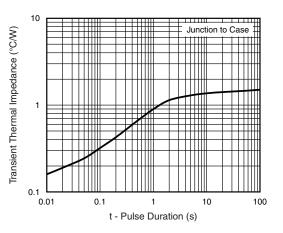


Fig. 6 - Typical Transient Thermal Impedance Per Diode

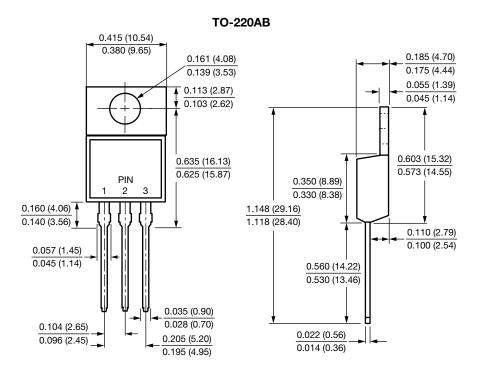
 Revision: 15-Dec-16
 3
 Document Number: 89439

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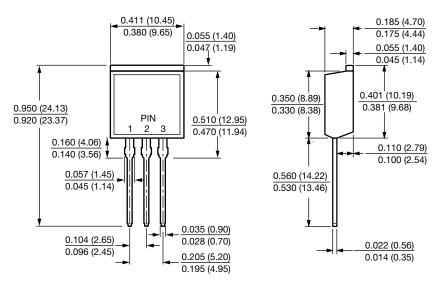




#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



**TO-262AA** 





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