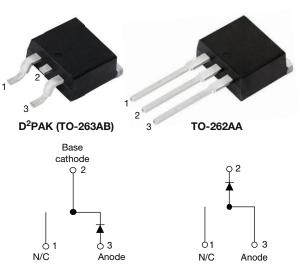
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VS-ETL1506S-M3, VS-ETL1506-1-M3

Vishay Semiconductors

Ultrafast Rectifier, 15 A FRED Pt[®]



VS-ETL1506S-M3

VS-ETL1506-1-M3

PRIMARY CHARACTERISTICS								
I _{F(AV)} 15 A								
V _R	600 V							
V _F at I _F	0.85 V							
t _{rr} (typ.)	60 ns							
T _J max.	175 °C							
Package	D ² PAK (TO-263AB), TO-262AA							
Circuit configuration	Single							

FEATURES

- · State of the art low forward voltage drop
- Ultrafast recovery time
- 175 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC[®]-JESD 47



- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

State of the art, ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 152 °C	15	٨
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	200	A
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATI	LECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-		
Forward valtage	V _F	I _F = 15 A - 0.99		0.99	1.07	V	
Forward voltage		I _F = 15 A, T _J = 150 °C	-	0.85	0.91		
Reverse leakage current	1	$V_{R} = V_{R}$ rated	-	0.01	15		
neverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	6	100	μΑ	
Junction capacitance	CT	V _R = 600 V	-	12	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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DYNAMIC RECOVERY CHAR	ACTERIS	STICS (T _J = 25 °C	unless otherwise spe	ecified)			
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \ dI_F/dt = 100$	0 A/µs, V _R = 30 V	-	60	110	
Poverse receiver time	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 10$	I _F = 15 A, dI _F /dt = 100 A/μs, V _R = 30 V		185	270	ns
Reverse recovery time	t _{rr}	T _J = 25 °C		-	210	-	115
		T _J = 125 °C		-	290	-	
Peak recovery current	I _{RRM}	T _J = 25 °C	I _F = 15 A dI _F /dt = 200 A/μs V _B = 390 V	-	20	-	А
Feak recovery current		T _J = 125 °C		-	26	-	A
	0	T _J = 25 °C		-	2.2	-	
Reverse recovery charge	Q _{rr}	T _J = 125 °C]	-	4.0	-	μC

THERMAL - MECHANICAL S	PECIFIC	ATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	1.3	1.51	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
weight			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style D ² PAK (TO-263AB)		ETL1	506S	
Marking device		Case style TO-262		ETL1	506-1	

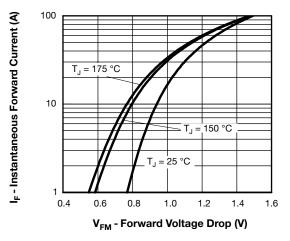


Fig. 1 - Typical Forward Voltage Drop Characteristics

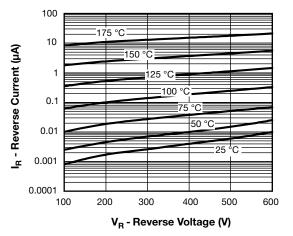


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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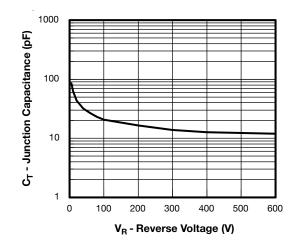


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

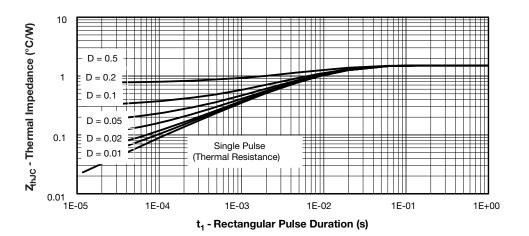
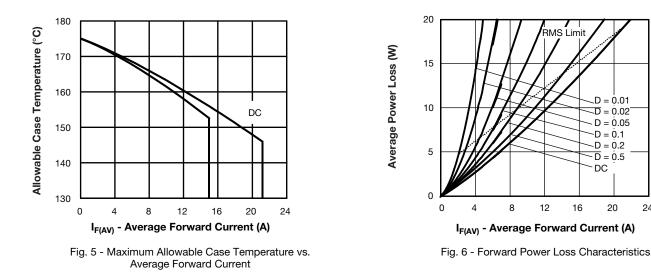


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics



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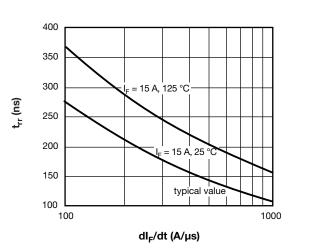
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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

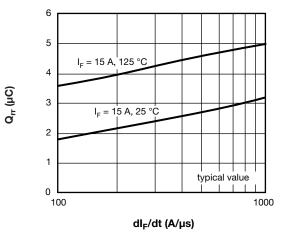


Fig. 8 - Typical Stored Charge vs. dl_F/dt

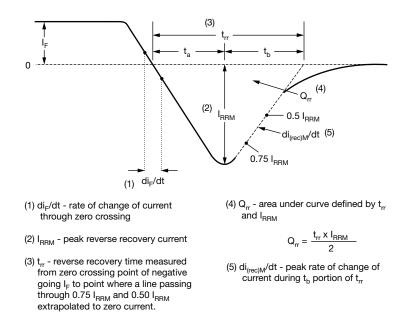
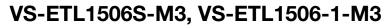


Fig. 9 - Reverse Recovery Waveform and Definitions

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Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	E	т	L	15	06	S	TRL	-M3	
		2	3	4	5	6	7	8	9	
	1 - 2 - 3 - 4 - 5 - 6 -	E = E = T = L = Cur Volt	nay Sem cuit confi single TO-220 ultrafast rent cod	iguratior t recove le (15 = le (06 =	ry time 15 A) 600 V)	oduct				
	7 - - 8 -	• -1	= D ² PAI = TO-20 one = tu	62AA						
			RL = tap RR = tap		•					<i>.</i> .
	9 -	· -M3	s = halog	gen-free	, RoHS-	complia	ant, and	l termin	ations le	ead (Pt

ORDERING INFORMATI	RDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-ETL1506S-M3	50	1000	Antistatic plastic tube				
VS-ETL1506-1-M3	50	1000	Antistatic plastic tube				
VS-ETL1506STRR-M3	800	800	13" diameter reel				
VS-ETL1506STRL-M3	800	800	13" diameter reel				

LINKS TO RELATED DOCUMENTS							
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164					
	TO-262AA	www.vishay.com/doc?96165					
Dat marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information	D ² PAK (TO-263AB)	www.vishay.com/doc?96424					

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Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



ota	ted	90	°C
<u>S</u>	cale	<u>ə:</u> 8	:1

SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL		MILLIM	MILLIMETERS		INCHES		
	OTMEDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
	D1	6.86	8.00	0.270	0.315	3	
	E	9.65	10.67	0.380	0.420	2, 3	
	E1	7.90	8.80	0.311	0.346	3	
	е	2.54	2.54 BSC		0.100 BSC		
	Н	14.61	15.88	0.575	0.625		
	L	1.78	2.79	0.070	0.110		
	L1	-	1.65	-	0.066	3	
	L2	1.27	1.78	0.050	0.070		
	L3	0.25	BSC	0.010	BSC		
	L4	4.78	5.28	0.188	0.208		

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Datum A and B to be determined at datum plane H

(6) Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 13-Jul-17

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Outline Dimensions



Vishay Semiconductors

TO-262AA

DIMENSIONS in millimeters and inches





F D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None





Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

CVMPOI	MILLIN	IETERS	INC	INCHES			
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	BSC	0.100) BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.56	3.71	0.140	0.146			

 ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
 ⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second dimensioner of the second dimensis of the second dimensioner of the second dimensioner of the the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

Revision: 30-Nov-17

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