

COMPLIANT

HALOGEN FREE

Hyperfast Rectifier, 75 A FRED Pt® G5



PRIMARY CHARACTERISTICS				
I _{F(AV)}	75 A			
V_{R}	600 V			
V _F at I _F at 125 °C	1.4 V			
t _{rr} (typ.)	29			
I _{FSM}	565			
T _J max.	175 °C			
Package	TO-247AD 2L			
Circuit configuration	Single			

LINKS TO ADDITIONAL RESOURCES



FEATURES

- · Hyperfast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off



• 175 °C maximum operating junction temperature

Polyimide passivation

- AEC-Q101 qualified meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters. This device is specifically designed to improve efficiency of high speed LLC output rectification stages of EV / HEV on-board battery chargers

MECHANICAL DATA

Case: TO-247AD 2L

Molding compound meets UL 94 V-0 flammability rating **Terminal:** matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 103 °C, D = 0.50	75	
Non-repetitive peak surge current	I _{FSM}	$T_C = 25$ °C, $t_p = 10$ ms, sine wave	565	Α
Repetitive peak forward current	I _{FRM}	$T_C = 103 ^{\circ}\text{C}, D = 0.50, f = 20 \text{kHz}$	150	
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-	
Forward voltage	W	I _F = 75 A	-	1.6	2.2	V
Forward voltage	V _F	I _F = 75 A, T _J = 125 °C	-	1.4	-	
Reverse leakage current I _R	V _R = V _R rated	-	-	25		
	IR	$T_J = 125 ^{\circ}\text{C}$, $V_R = V_R$ rated	-	-	500	μA
Junction capacitance	C _T	V _R = 200 V	-	96	-	pF
Series inductance	L _S	Measured to lead 5 mm from package body	-	8	-	nH



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
			$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		29	-	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	44	-	ns
		T _J = 125 °C		-	69	-	
Dools recovery as week	,	T _J = 25 °C	$I_F = 50 \text{ A}$	-	18	-	^
Peak recovery current I _{RRM}	T _J = 125 °C	dl _F /dt = 1000 A/μs V _R = 400 V	-	42	-	A	
Reverse recovery charge Q _{rr}		T _J = 25 °C	11 22	-	484	-	~C
	Q _{rr}	T _J = 125 °C		-	1731	-	nC
D	T _J = 25 °C		-	48	-		
Reverse recovery time	everse recovery time t _{rr}	T _J = 125 °C		-	75	-	ns
Peak recovery current I _{RRM}	,	T _J = 25 °C	$I_F = 75 \text{ A}$ $dI_F/dt = 1000 \text{ A/}\mu\text{s}$ $V_R = 400 \text{ V}$	-	21	-	Α
	IRRM	T _J = 125 °C		-	46	-	A
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	573	-	~ C
	Q _{rr}	T _J = 125 °C		-	2048	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	R _{thJC}		-	-	0.5	°C/W
Weight			-	5.5	-	g
weignt			-	0.2	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Marking device		Case style: TO-247AD 2L		E5PX7	'506LH	

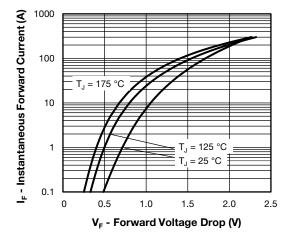


Fig. 1 - Forward Voltage Drop Characteristics

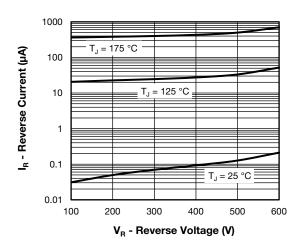


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

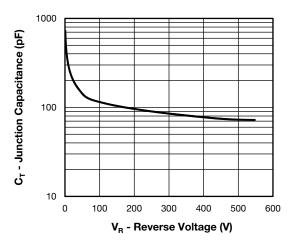


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

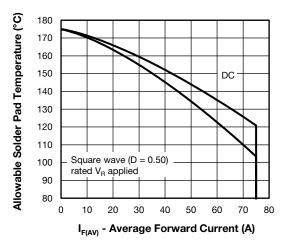


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

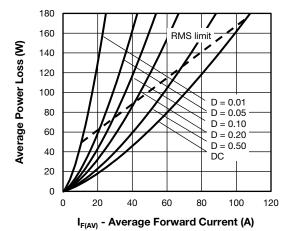


Fig. 5 - Forward Power Loss Characteristics

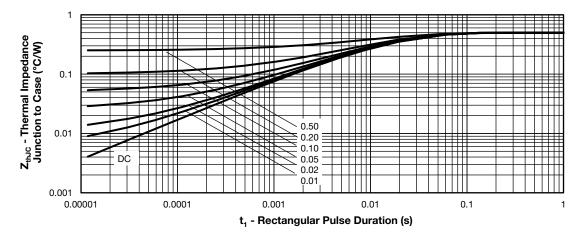


Fig. 6 - Transient Thermal Impedance, Junction to Case

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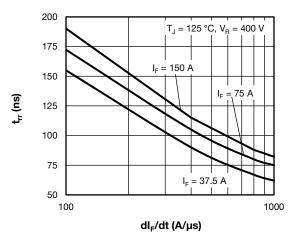


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

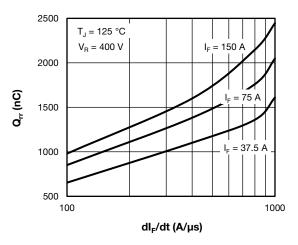


Fig. 8 - Typical Reverse Recovery Charge vs. dl_F/dt

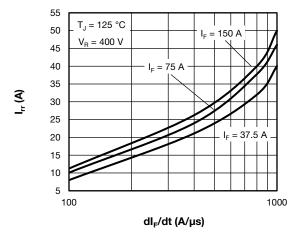


Fig. 9 - Typical Reverse Recovery Current vs. $dI_{\mbox{\scriptsize F}}/dt$

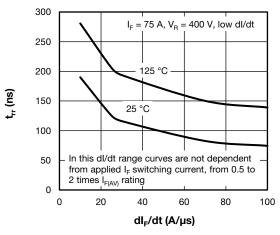


Fig. 10 - Typical Reverse Recovery Time vs. dl_E/dt

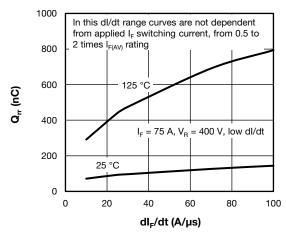


Fig. 11 - Typical Reverse Recovery Charge vs. dl_F/dt

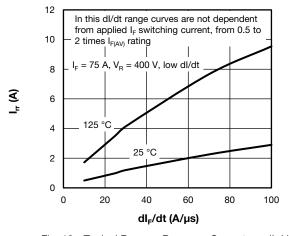


Fig. 12 - Typical Reverse Recovery Current vs. dI_F/dt

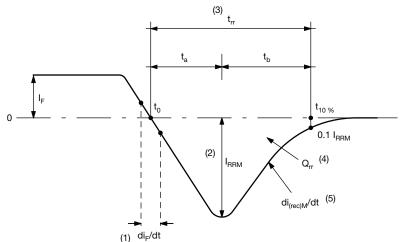


Fig. 13 - Reverse Recovery Waveform and Definitions

Notes

- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from t₀, crossing point of negative going I_F, to point t_{10%}, 0.1 I_{RBM}
- $^{(4)}$ Q_{rr} area under curve defined by t₀ and t_{10 %}

$$Q_{rr} = \int_{t_0}^{t_{10} \%} I(t) dt$$

 $^{(5)}$ di_(rec)M/dt - peak rate of change of current during t_b portion of t_{rr}

ORDERING INFORMATION TABLE

Device code VS-Ρ Ε 5 X 75 06 Н **N3** L 7 (2) 3 4 5 6 8 9 (10)Vishay Semiconductors product Circuit configuration 2 E = single diode FRED Pt® Gen 5 P = TO-247 package Process type: X = hyperfast recovery Current rating (75 = 75 A)Voltage rating (06 = 600 V) 8 Package: L = long lead (TO-247AD) H = AEC-Q101 qualified Environmental digit: N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-E5PX7506LHN3	25	500	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95536
Part marking information	www.vishay.com/doc?95648



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