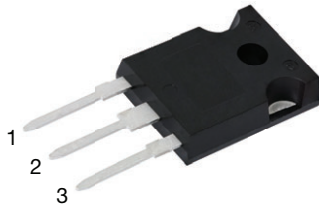
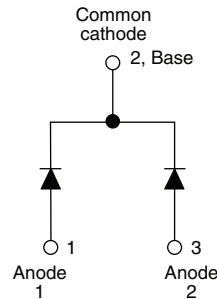


## Ultrafast Rectifier, FRED Pt<sup>®</sup>, 2 x 30 A


**TO-247AC 3L**


### FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
 COMPLIANT  
 HALOGEN  
**FREE**

### DESCRIPTION / APPLICATIONS

VS-60CPU02... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, welding, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 x 30 A
$V_R$	200 V
$V_F$ at $I_F$	0.75 V
$t_{rr}$ typ.	30 ns
$T_J$ max.	175 °C
Package	TO-247AC 3L
Circuit configuration	Common cathode

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	$V_{RRM}$		200	V
Average rectified forward current	$I_{F(AV)}$	per leg	30	A
		per device	Rated $V_R$ , $T_C = 145$ °C	
Non-repetitive peak surge current per leg	$I_{FSM}$	$T_J = 25$ °C, $t_p = 10$ ms	300	
Peak repetitive forward current per leg	$I_{FM}$	Rated $V_R$ , square wave, 20 kHz, $T_C = 137$ °C	60	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ( $T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100$ $\mu$ A	200	-	-	V
Forward voltage	$V_F$	$I_F = 30$ A	-	0.92	1.1	
		$I_F = 30$ A, $T_J = 150$ °C	-	0.75	0.85	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	50	$\mu$ A
		$T_J = 150$ °C, $V_R = V_R$ rated	-	30	300	
Junction capacitance	$C_T$	$V_R = 200$ V	-	100	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	12	-	nH



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1.0\text{ A}$ , $di_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	30	36	ns
		$T_J = 25\text{ }^\circ\text{C}$	-	30	-	
		$T_J = 125\text{ }^\circ\text{C}$	-	47	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^\circ\text{C}$	-	3	-	A
		$T_J = 125\text{ }^\circ\text{C}$	-	6.5	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^\circ\text{C}$	-	42	-	nC
		$T_J = 125\text{ }^\circ\text{C}$	-	160	-	

<b>THERMAL - MECHANICAL SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-65	-	175	$^\circ\text{C}$
Thermal resistance, junction to case per leg	$R_{thJC}$		-	0.6	1.0	$^\circ\text{C}/\text{W}$
Thermal resistance, junction to ambient per leg	$R_{thJA}$	Typical socket mount	-	-	40	
Thermal resistance, case to heatsink	$R_{thCS}$	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	6.0	-	g
			-	0.21	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC 3L	60CPU02			

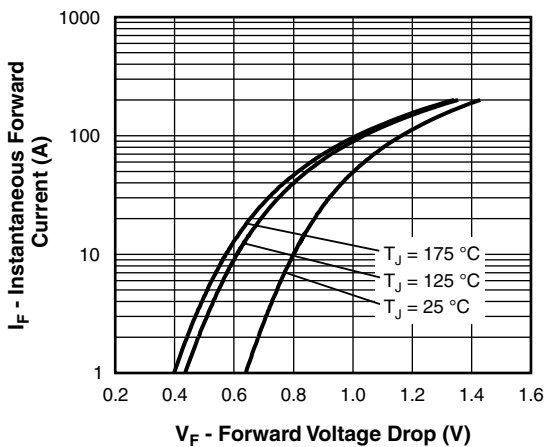


Fig. 1 - Typical Forward Voltage Drop Characteristics

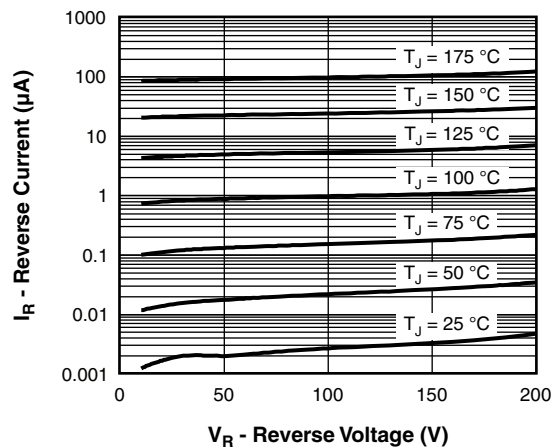


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

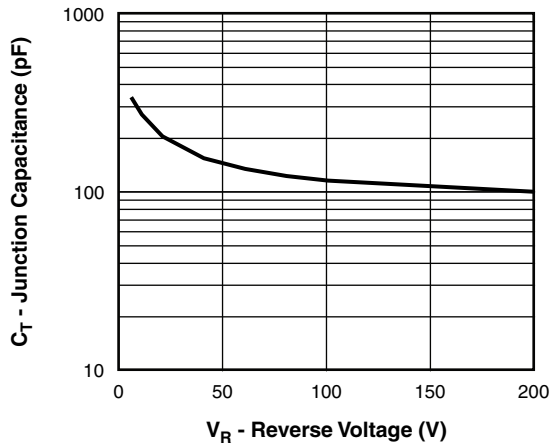


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

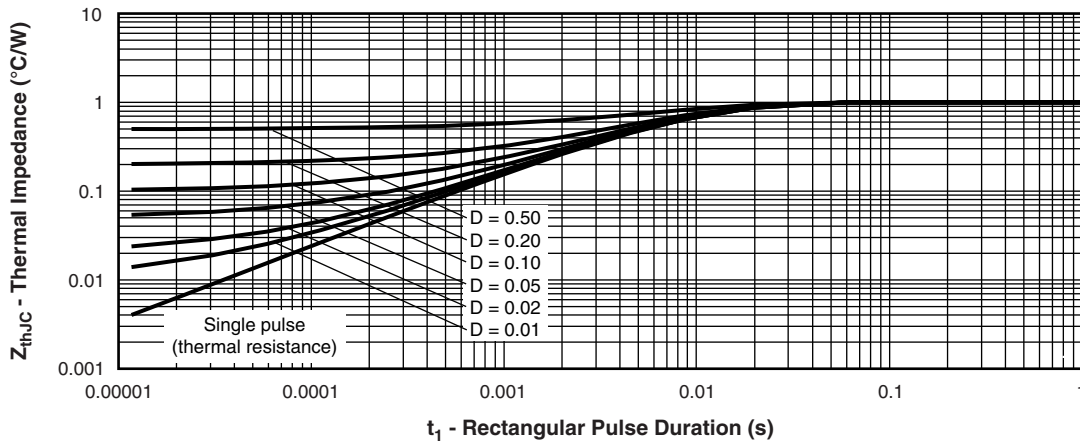


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

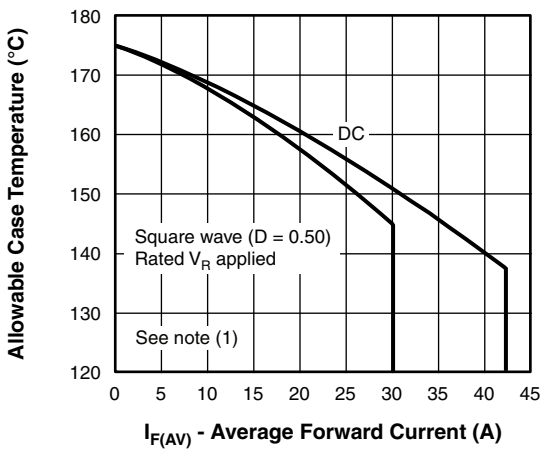


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

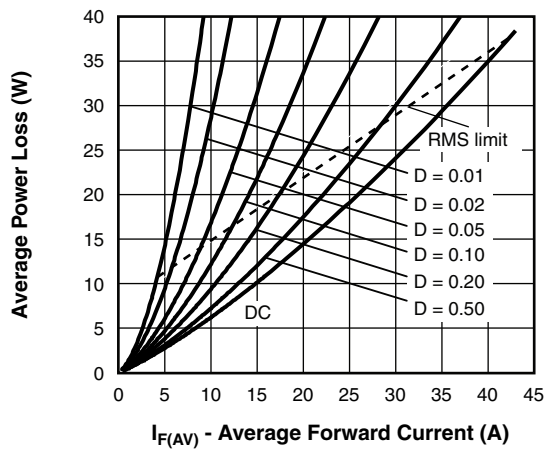


Fig. 6 - Forward Power Loss Characteristics

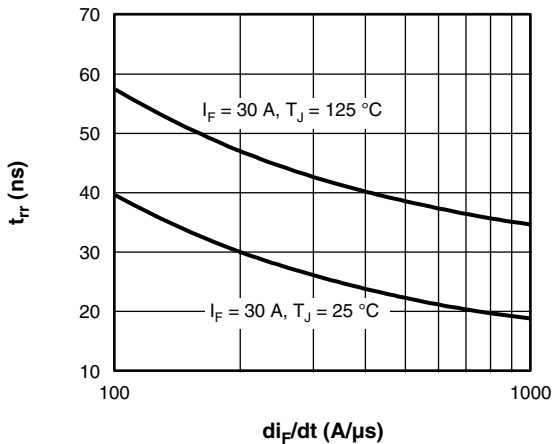


Fig. 7 - Typical Reverse Recovery Time vs.  $di_F/dt$

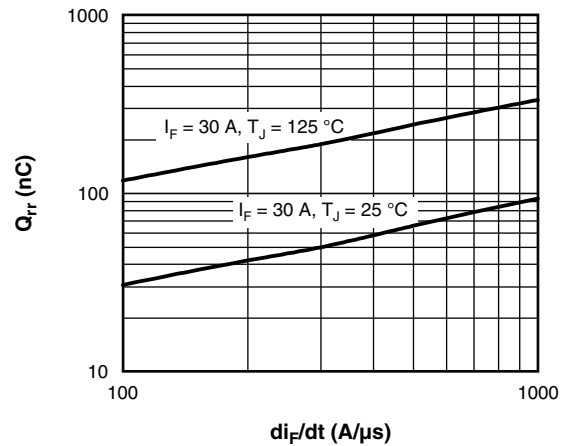


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$

**Note**

- (1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $Pd$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);
- $Pd_{REV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

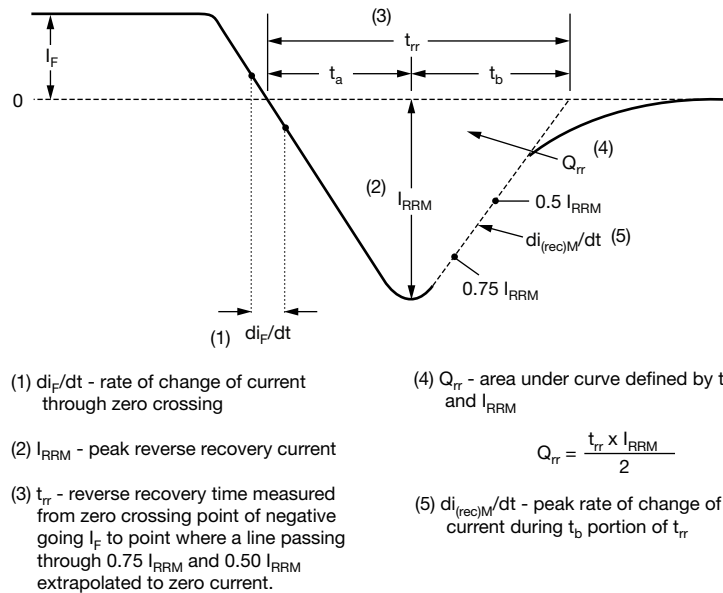
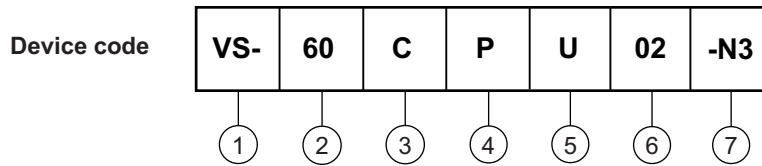


Fig. 9 - Reverse Recovery Waveform and Definitions



**ORDERING INFORMATION TABLE**



- 1** - Vishay Semiconductors product
- 2** - Current rating (60 = 60 A)
- 3** - Circuit configuration:  
C = common cathode
- 4** - Package:  
P = TO-247AC (modified)
- 5** - U = ultrafast rectifier
- 6** - Voltage rating (02 = 200 V)
- 7** - Environmental digit:  
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

<b>ORDERING INFORMATION (Example)</b>			
<b>PREFERRED P/N</b>	<b>QUANTITY PER T/R</b>	<b>MINIMUM ORDER QUANTITY</b>	<b>PACKAGING DESCRIPTION</b>
VS-60CPU02-N3	25	500	Antistatic plastic tube

<b>LINKS TO RELATED DOCUMENTS</b>	
Dimensions	<a href="http://www.vishay.com/doc?96138">www.vishay.com/doc?96138</a>
Part marking information	<a href="http://www.vishay.com/doc?95007">www.vishay.com/doc?95007</a>





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