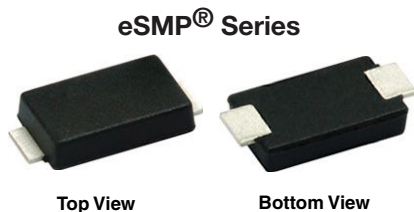


# Hyperfast Rectifier, 3 A FRED Pt®



Top View

Bottom View

**SlimSMA (DO-221AC)**

Cathode  Anode

## FEATURES

- Hyperfast recovery time, reduced  $Q_{rr}$ , and soft recovery
- 175 °C maximum operating junction temperature
- Low forward voltage drop
- Low leakage current
- Specific for output and snubber operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

## DESIGN SUPPORT TOOLS

[click logo to get started](#)
**3D**  
Models  
Available

## PRIMARY CHARACTERISTICS

$I_{F(AV)}$	3 A
$V_R$	200 V
$V_F$ at $I_F$	0.74 V
$t_{rr}$	30 ns
$T_J$ max.	175 °C
Package	SlimSMA (DO-221AC)
Circuit configuration	Single

## DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		200	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 145\text{ °C}^{(1)}$	3	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$	85	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-65 to +175	°C

### Note

<sup>(1)</sup> Device on PCB with 8 mm x 16 mm soldering lands

## ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	200	-	-	V
Forward voltage	$V_F$	$I_F = 3\text{ A}$	-	0.86	0.93	
		$I_F = 3\text{ A}, T_J = 125\text{ °C}$	-	0.74	0.78	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	2	$\mu\text{A}$
		$T_J = 125\text{ °C}, V_R = V_R$ rated	-	1	8	
Junction capacitance	$C_T$	$V_R = 200\text{ V}$	-	13	-	pF

**DYNAMIC RECOVERY CHARACTERISTICS** ( $T_J = 25\text{ }^{\circ}\text{C}$  unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1.0\text{ A}$ , $dI_F/dt = 50\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	26	-	ns
		$I_F = 0.5\text{ A}$ , $I_R = 1\text{ A}$ , $I_{rr} = 0.25\text{ A}$	-	-	30	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	18	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	26	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	2.5	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	4	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	23	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	50	-	

**THERMAL - MECHANICAL SPECIFICATIONS** ( $T_J = 25\text{ }^{\circ}\text{C}$  unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to lead	$R_{thJL}$	Device mounted on PCB with 8 mm x 16 mm soldering lands	-	8	10	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction to ambient	$R_{thJA}$	Device mounted on PCB with 2 mm x 3.5 mm soldering lands	-	91	110	
Approximate Weight			0.032			g
			0.0011			oz.
Marking device		Case style SlimSMA (DO-221AC)	3H2			

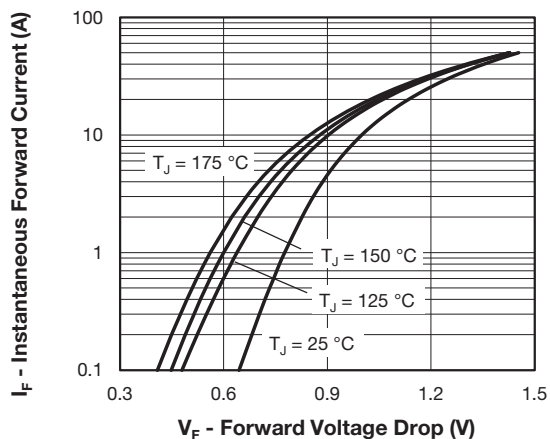


Fig. 1 - Typical Forward Voltage Drop Characteristics

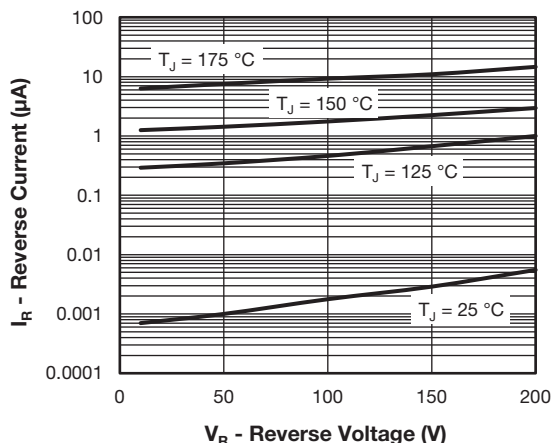


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

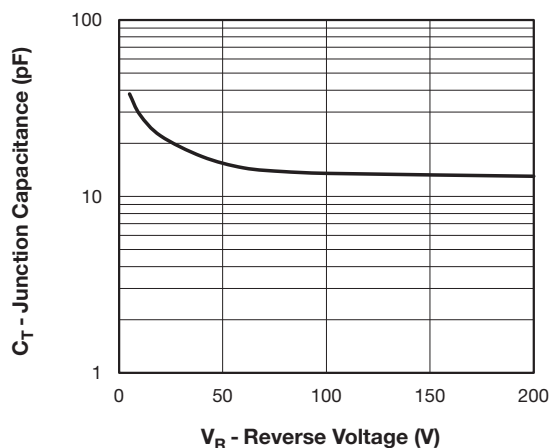


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

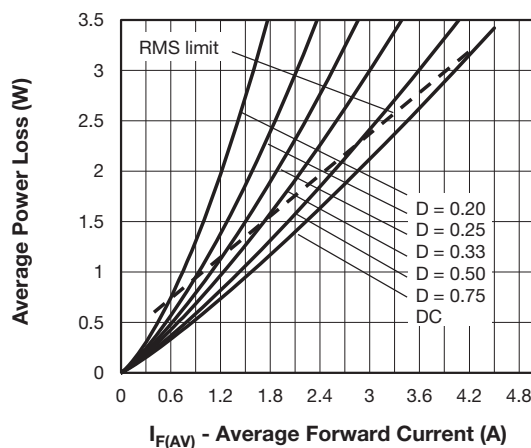


Fig. 5 - Forward Power Loss Characteristics

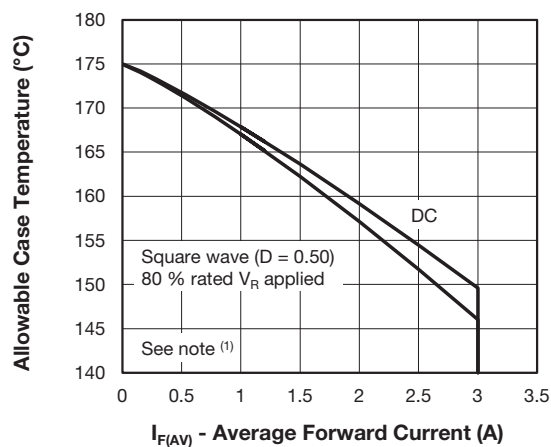
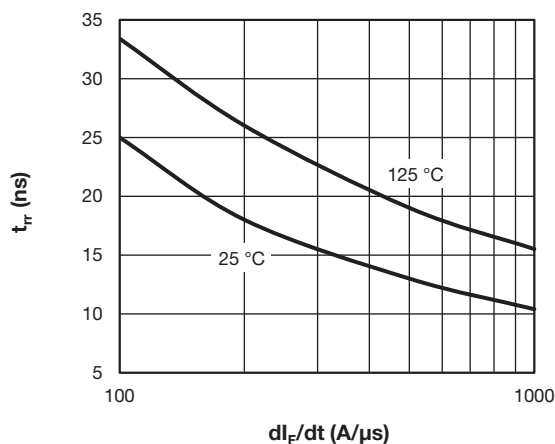
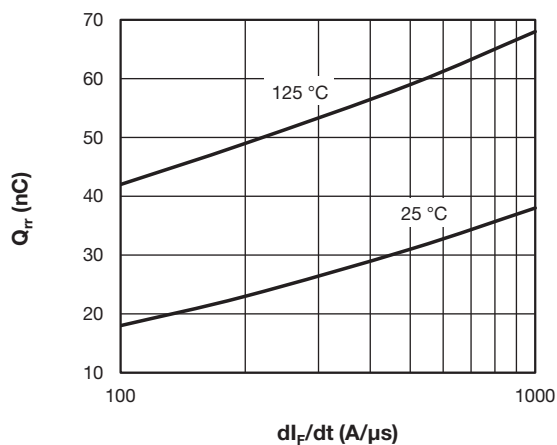


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current


Fig. 6 - Typical Reverse Recovery vs.  $dI_F/dt$ 

Fig. 7 - Typical Stored Charge vs.  $dI_F/dt$ 
**Note**

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see Fig. 6);  
 $P_{dREV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

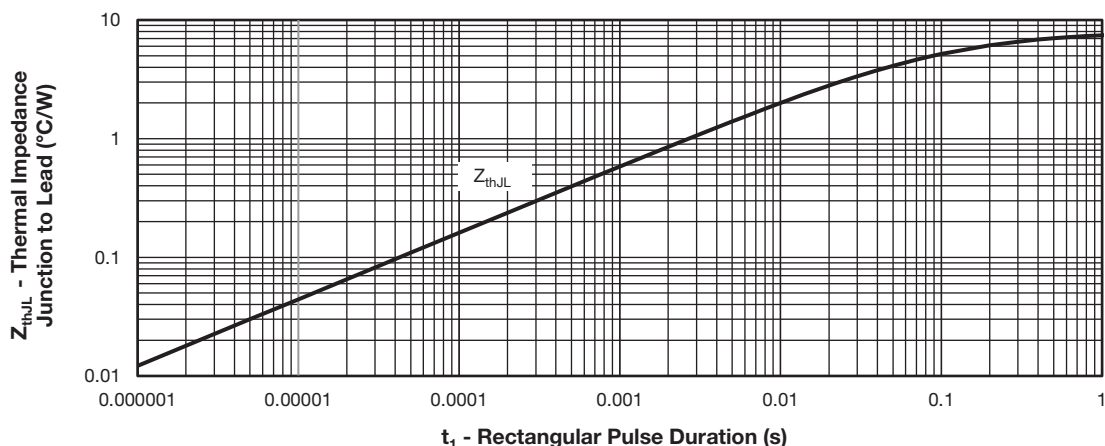
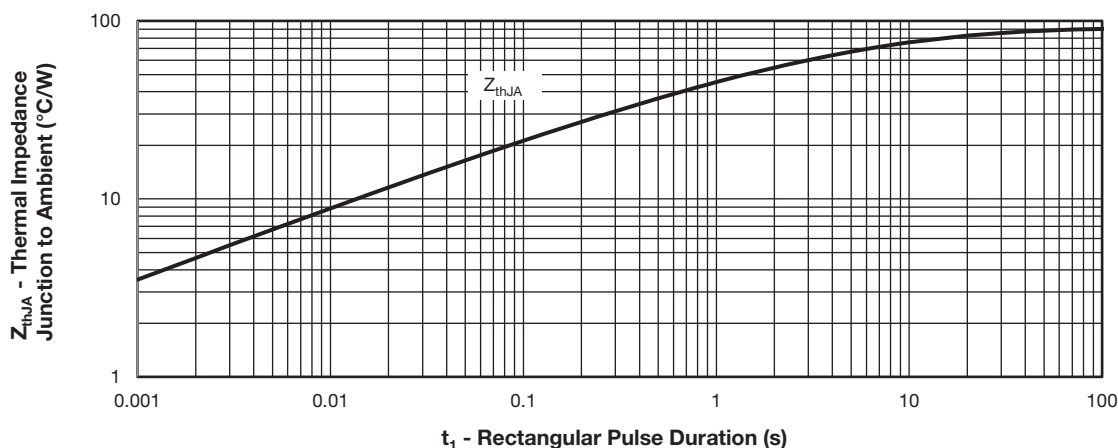
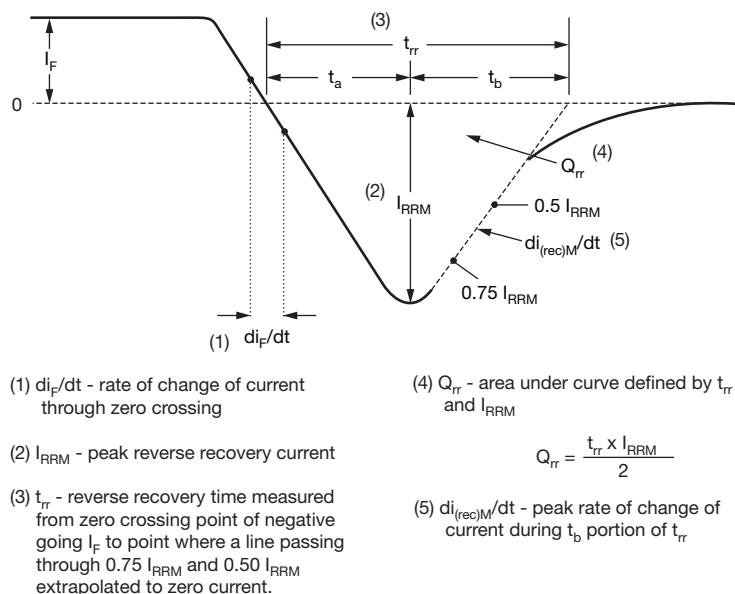

Fig. 8 - Typical Thermal Impedance  $Z_{thJL}$  Junction-to-Lead

Fig. 9 - Typical Thermal Impedance  $Z_{thJA}$  Junction-to-Ambient


Fig. 10 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	<b>VS-</b>	<b>3</b>	<b>E</b>	<b>J</b>	<b>H</b>	<b>02</b>	<b>-M3</b>
	1	2	3	4	5	6	7

- |          |   |   |
|----------|---|---|
| <b>1</b> | - | Vishay Semiconductors product                                       |
| <b>2</b> | - | Current rating (3 = 3 A)  |
| <b>3</b> | - | Circuit configuration:<br>E = single diode                          |
| <b>4</b> | - | J = SlimSMA package   |
| <b>5</b> | - | Process type,<br>H = hyperfast recovery                             |
| <b>6</b> | - | Voltage code (02 = 200 V)   |
| <b>7</b> | - | -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free |

**ORDERING INFORMATION** (Example)

PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-3EJH02-M3/6A	3500	3500	7" diameter plastic tape and reel
VS-3EJH02-M3/6B	14 000	14 000	13" diameter plastic tape and reel

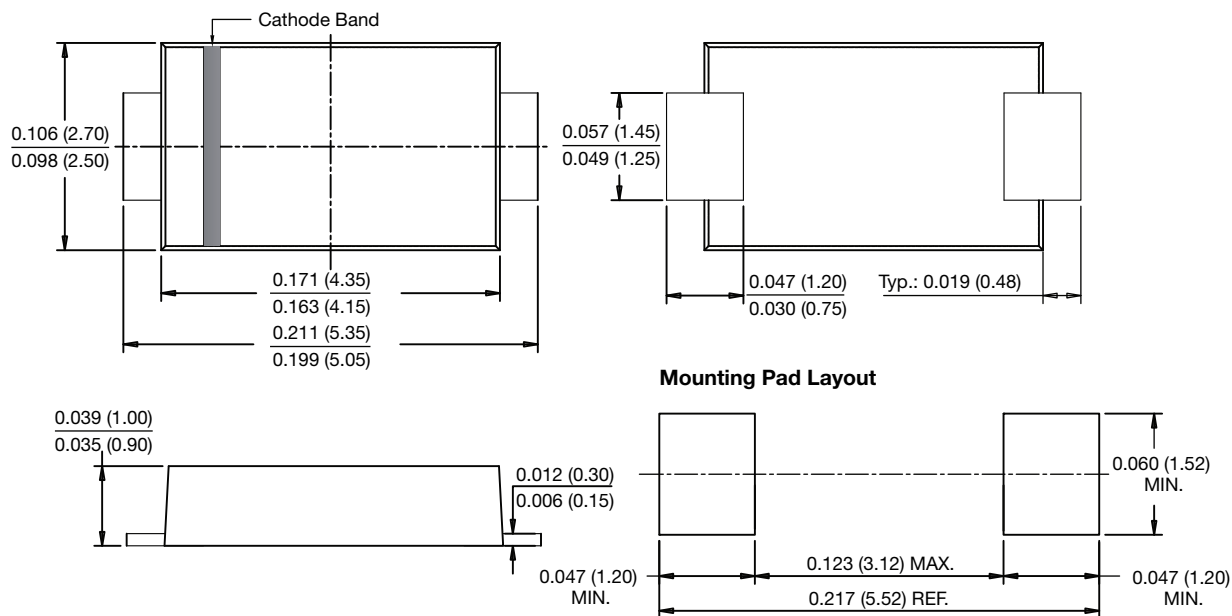
**LINKS TO RELATED DOCUMENTS**

Dimensions	<a href="http://www.vishay.com/doc?95571">www.vishay.com/doc?95571</a>
Part marking information	<a href="http://www.vishay.com/doc?95562">www.vishay.com/doc?95562</a>
Packaging information	<a href="http://www.vishay.com/doc?88869">www.vishay.com/doc?88869</a>



## DO-221AC (SlimSMA)

**DIMENSIONS** in inches (millimeters)





## Disclaimer

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