

## **High Performance Schottky Rectifier, 120 A**



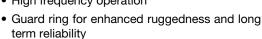


HALF-PAK (D-67)

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	120 A			
$V_{R}$	45 V			
Package	HALF-PAK (D-67)			
Circuit configuration	Single diode			

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation



- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

The VS-121NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS VALUES UN			
I <sub>F(AV)</sub>	Rectangular waveform	120	Α	
V <sub>RRM</sub>		45	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	16 000	Α	
V <sub>F</sub>	120 A <sub>pk</sub> , T <sub>J</sub> = 125 °C 0.6		V	
T <sub>J</sub>	Range	-55 to +175	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-121NQ045PbF	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	45	V
Maximum working peak reverse voltage	$V_{RWM}$	45	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 137 °C, rectangular waveform		120	Α
Maximum peak one cycle non-repetitive surge current	l=a	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	16 000	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	2000	A
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 13 A, L = 1 mH		81	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \text{ x } V_R$ typical		13	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	120 A	T 05 °C	0.65	V
		240 A	T <sub>J</sub> = 25 °C	0.82	
		120 A	T <sub>J</sub> = 125 °C	0.6	
		240 A		0.76	
Maximum reverse leakage current See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	10	mA
		T <sub>J</sub> = 125 °C		90	
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5200	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

<sup>(1)</sup> Pulse width =  $500 \mu s$ 

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storag	e temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C	
Maximum thermal resistance,	junction to case	R <sub>thJC</sub>	DC operation See fig. 4	0.38 °C/W		
Typical thermal resistance, ca	se to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
				1.06	oz.	
	minimum			3 (26.5)		
Mounting torque	maximum		Nigor live visuate of the variety	4 (35.4)	N⋅m	
Terminal torque -	minimum		Non-lubricated threads	3.4 (30)	(lbf · in)	
	maximum			5 (44.2)		
Case style				HALF-PAK module		

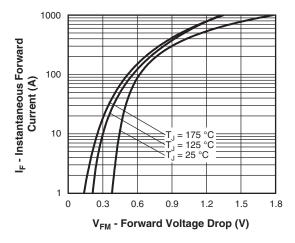


Fig. 1 - Maximum Forward Voltage Drop Characteristics

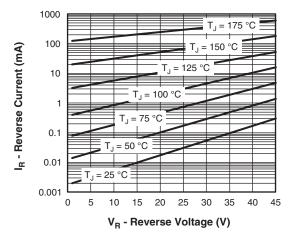


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



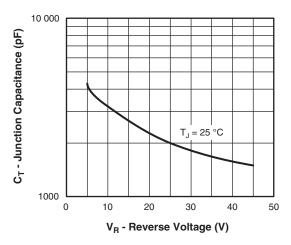


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

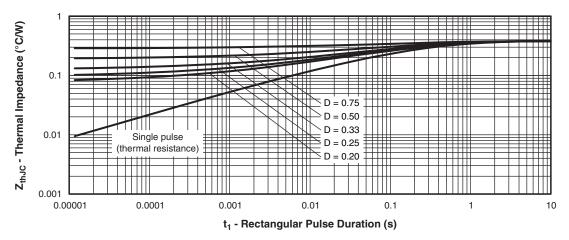


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

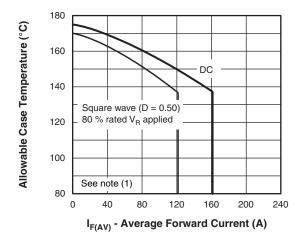


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

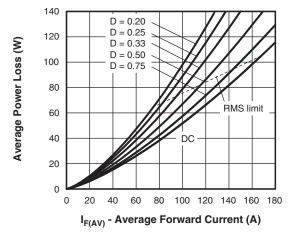


Fig. 6 - Forward Power Loss Characteristics

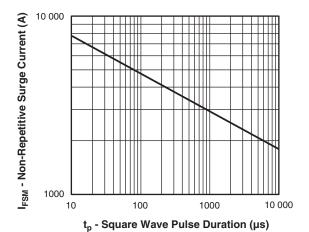


Fig. 7 - Maximum Non-Repetitive Surge Current

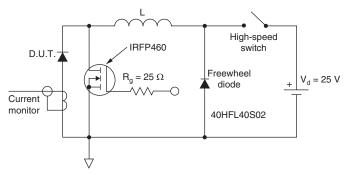


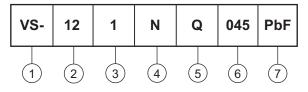
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$ 

#### **ORDERING INFORMATION TABLE**

#### Device code



- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- Product silicon identification
- 4 N = not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (045 = 45 V)
- 7 Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95020		



### **D-67 HALF-PAK**

### **DIMENSIONS** in millimeters (inches)









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