




## Insulated Ultrafast Rectifier Module, 120 A



SOT-227

### FEATURES

- Two fully independent diodes
- Ceramic fully insulated package ( $V_{ISOL} = 2500 V_{AC}$ )
- Ultrafast reverse recovery
- Ultrasoft reverse recovery current shape
- Low forward voltage
- Optimized for power conversion: welding and industrial SMPS applications
- Industry standard outline
- Plug-in compatible with other SOT-227 packages
- Easy to assemble
- Direct mounting to heatsink
- UL approved file E78996 
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

RoHS  
COMPLIANT

### PRODUCT SUMMARY

$V_R$	600 V
$I_{F(AV)}$ per module at $T_C = 80^\circ C$	120 A
$t_{rr}$	96 ns
Type	Modules - Diode, FRED Pt®

### DESCRIPTION

The UFL120FA60P insulated modules integrate two state of the art Vishay Semiconductors ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness, and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be a predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, and DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	$V_R$		600	V
Continuous forward current per diode	$I_F$	$T_C = 85^\circ C$	69	A
Single pulse forward current per diode	$I_{FSM}$	$T_C = 25^\circ C$	750	
Maximum power dissipation per module	$P_D$	$T_C = 85^\circ C$	180	W
RMS isolation voltage	$V_{ISOL}$	Any terminal to case, $t = 1 \text{ min}$	2500	V
Operating junction and storage temperatures	$T_J, T_{Stg}$		- 55 to 175	$^\circ C$

**UFL120FA60P**

Vishay Semiconductors

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<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	$V_{BR}$	$I_R = 100\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	$V_{FM}$	$I_F = 60\text{ A}$	-	1.29	1.60	
		$I_F = 120\text{ A}$	-	1.49	1.88	
		$I_F = 60\text{ A}$	-	1.13	1.35	
		$I_F = 120\text{ A}$	-	1.37	1.68	
Reverse leakage current	$I_{RM}$	$V_R = V_R\text{ rated}$	-	0.1	100	$\mu\text{A}$
		$T_J = 175\text{ }^{\circ}\text{C}$ , $V_R = V_R\text{ rated}$	-	0.2	1.0	mA
Junction capacitance	$C_T$	$V_R = 600\text{ V}$	-	80	-	pF

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	96	141	ns
		$T_J = 125\text{ }^{\circ}\text{C}$	-	190	246	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	7	13	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	17	25	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	340	917	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	1581	3075	

<b>THERMAL - MECHANICAL SPECIFICATIONS</b>						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	$R_{thJC}$		-	-	1.0	$^{\circ}\text{C/W}$
Junction to case, both leg conducting			-	-	0.5	
Case to heatsink	$R_{thCS}$	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	N · m
Case style			SOT-227			

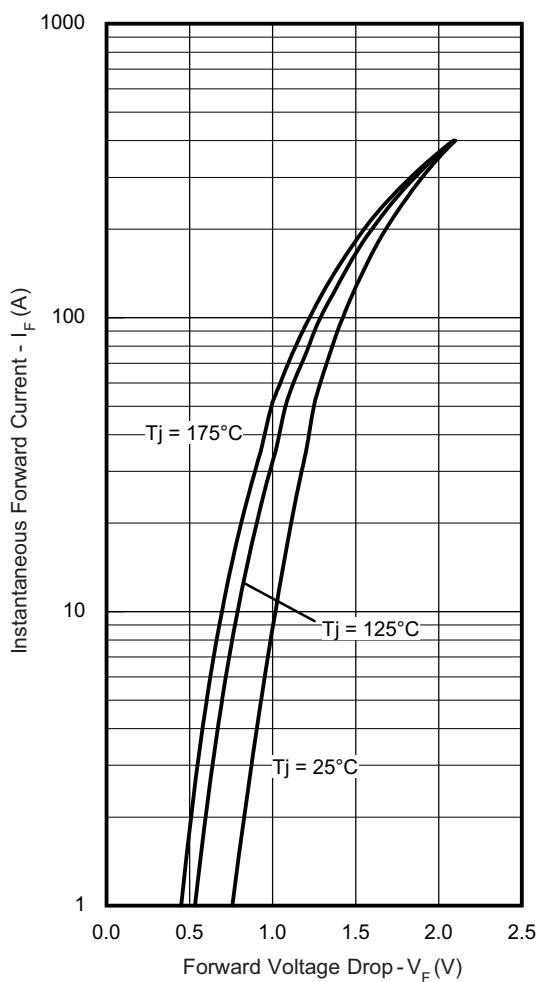


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Diode)

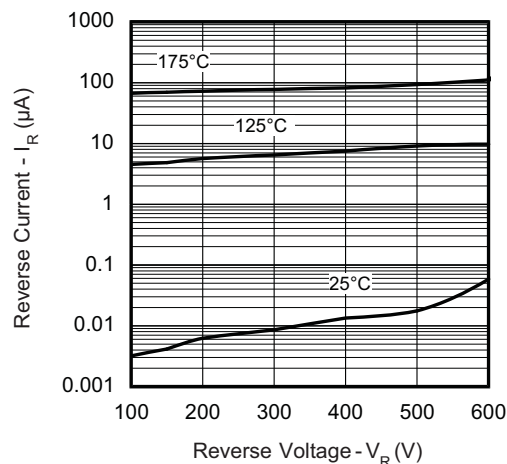


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

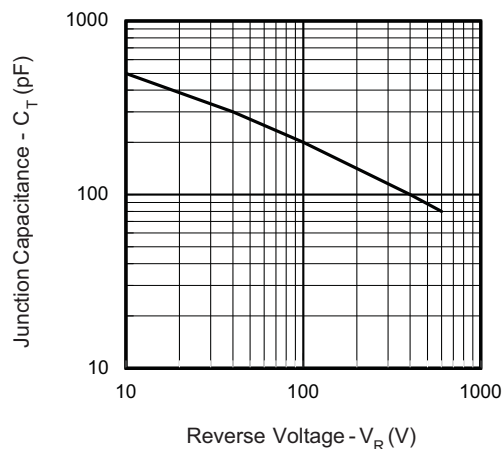


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

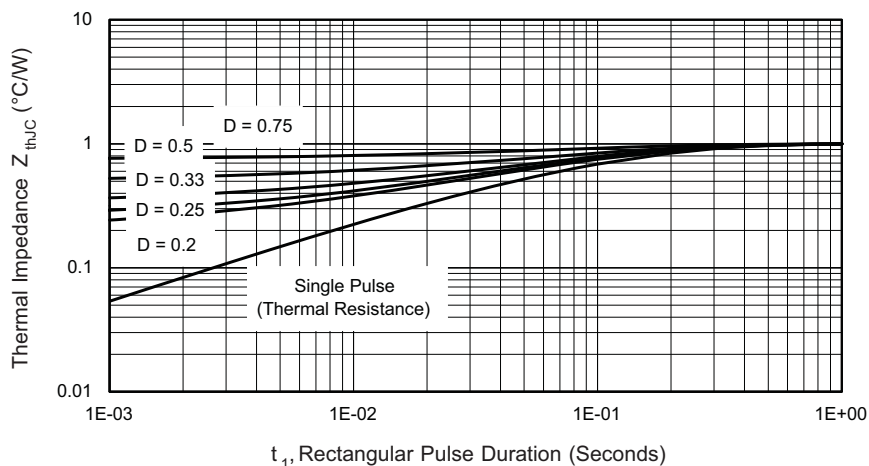


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics (Per Diode)

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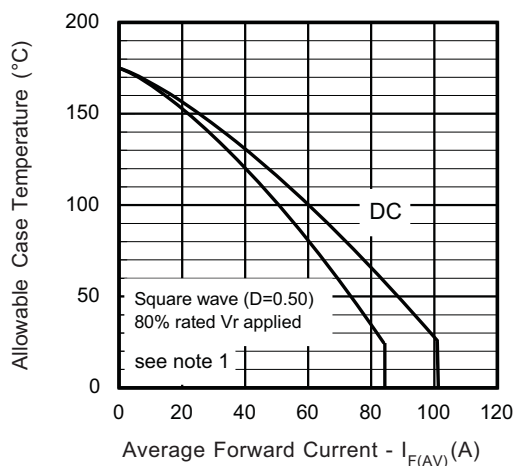


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

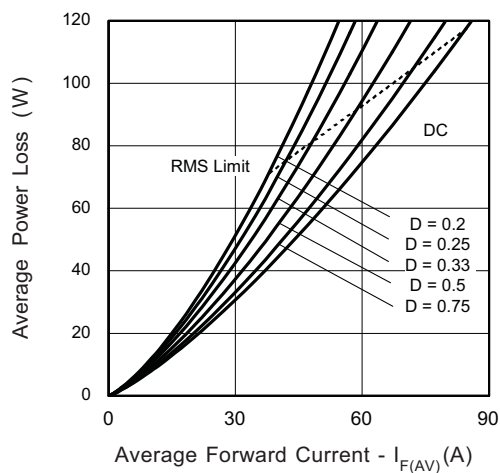


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

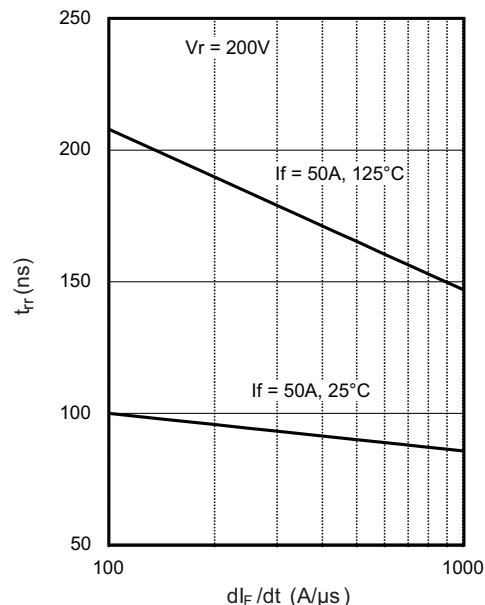


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$

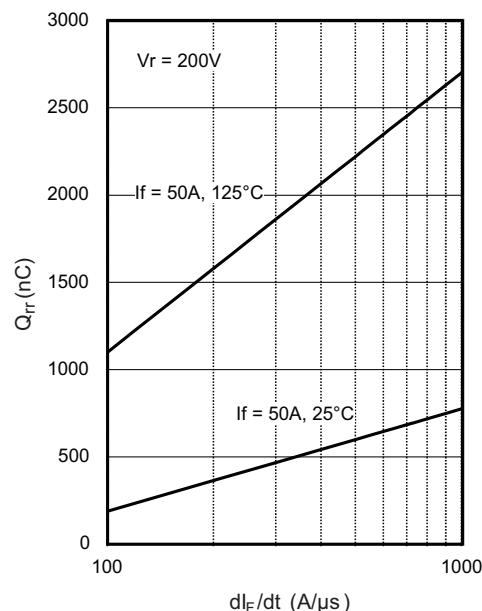


Fig. 8 - Typical Stored Charge vs.  $dI_F/dt$

## Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{dREV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80\%$  rated  $V_R$



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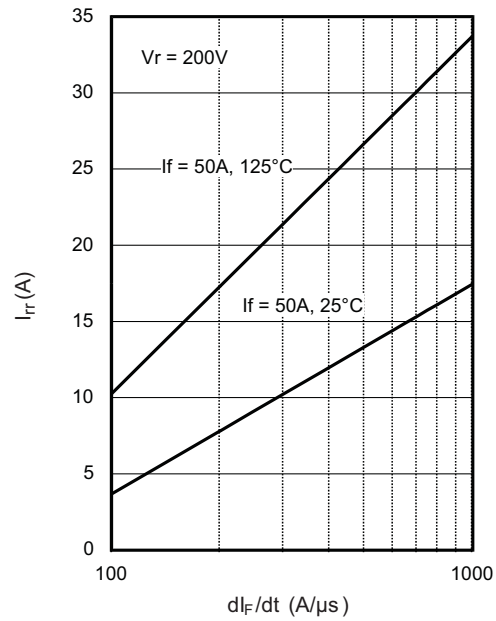


Fig. 9 - Typical Stored Current vs.  $di_F/dt$

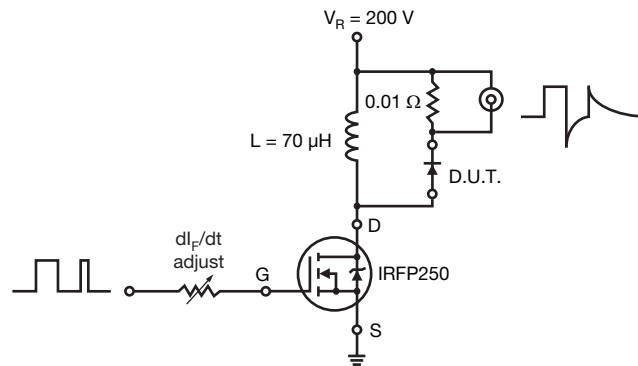


Fig. 10 - Reverse Recovery Parameter Test Circuit





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