HALOGEN FREE



# Vishay General Semiconductor

# High Current Density Surface Mount Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.29 \text{ V}$  at  $I_F = 5 \text{ A}$ 



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	20 A			
V <sub>RRM</sub>	50 V			
I <sub>FSM</sub>	240 A			
V <sub>F</sub> at I <sub>F</sub> = 20 A (T <sub>A</sub> = 125 °C)	0.46 V			
T <sub>J</sub> max.	150 °C			
Package	TO-277A (SMPC)			
Diode variation	Single die			

### **FEATURES**

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

### **MECHANICAL DATA**

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Tarminala, matta ti

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V20PL50	UNIT	
Device marking code		20L5		
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	V	
Maximum average few yeard restified assured /fig. 1	I <sub>F</sub> <sup>(1)</sup>	20	A	
Maximum average forward rectified current (fig. 1)	I <sub>F</sub> <sup>(2)</sup>	5.5		
Maximum DC reverse voltage	$V_{DC}$	40	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	240	Α	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

### **Notes**

- (1) Mounted on 30 mm x 30 mm pad areas aluminum PCB
- (2) Free air, mounted on recommended copper pad area



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.40	-	V
	I <sub>F</sub> = 10 A			0.45	-	
	I <sub>F</sub> = 20 A			0.51	0.59	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.29	-	
	I <sub>F</sub> = 10 A			0.36	-	
	I <sub>F</sub> = 20 A			0.46	0.54	
Reverse current	I V <sub>R</sub> = 40 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.02	-	mA
		T <sub>A</sub> = 125 °C		15	-	
	V <sub>R</sub> = 50 V	T <sub>A</sub> = 25 °C		-	3	mΛ
	v <sub>R</sub> = 50 v	T <sub>A</sub> = 125 °C		20	60	- mA

### **Notes**

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	R SYMBOL V2		UNIT	
Typical they mal registered	R <sub>0JA</sub> (1)(2)	68	°C/W	
Typical thermal resistance	R <sub>0JM</sub> (3)	4		

#### **Notes**

- (1) Free air, mounted on recommended copper pad area; thermal resistance R<sub>BJA</sub> junction to ambient
- $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta,JA}$
- (3) Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R<sub>0JM</sub> junction to mount measured at cathode side

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V20PL50-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V20PL50-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

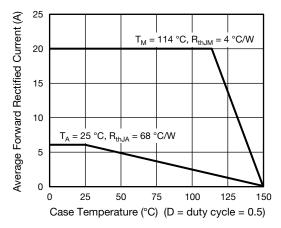


Fig. 1 - Maximum Forward Current Derating Curve

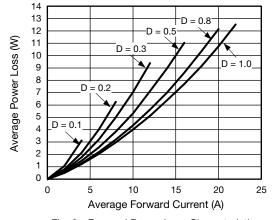


Fig. 2 - Forward Power Loss Characteristics



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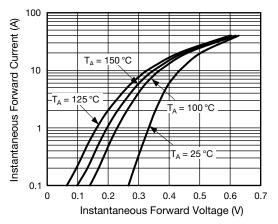


Fig. 3 - Typical Instantaneous Forward Characteristics

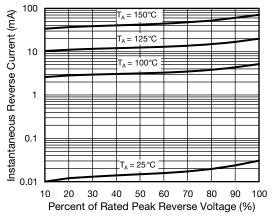


Fig. 4 - Typical Reverse Leakage Characteristics

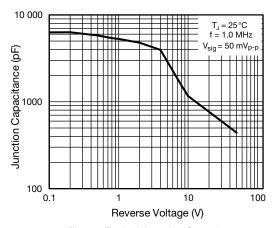


Fig. 5 - Typical Junction Capacitance

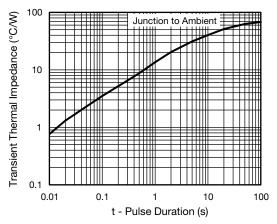
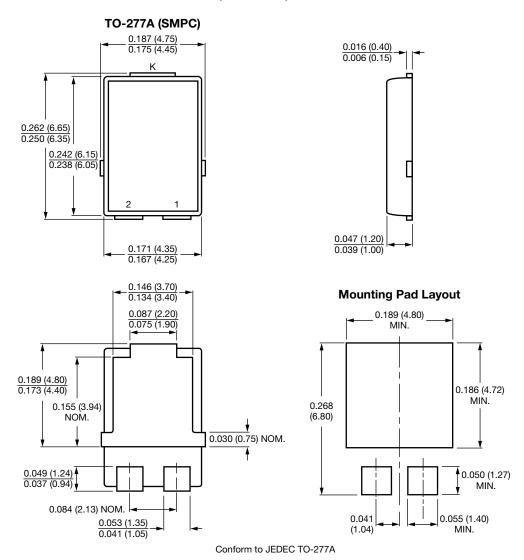


Fig. 6 - Typical Transient Thermal Impedance



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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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