## FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL J-STD-020. level 1, per LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Vishay General Semiconductor

### TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test Polarity: as marked

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V20D45C	UNIT	
Device marking code			V20D45C		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	45	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	20	А	
	per diode		10	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	120	A	
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +150	- °C	
Storage temperature range		T <sub>STG</sub>	-55 to +150		

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0,JA</sub>

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## Dual Low-Voltage TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low V<sub>F</sub> = 0.34 V at I<sub>F</sub> = 5.0 A

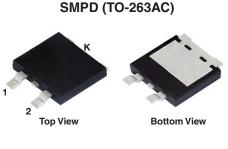
Availabl

RoHS

COMPLIANT

HALOGEN FREE





eSMP<sup>®</sup> Series

Anode 1 O-

Anode 2 O Cathode

### DESIGN SUPPORT TOOLS AVAILABLE



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 10 A			
V <sub>RRM</sub>	45 V			
I <sub>FSM</sub>	120 A			
V <sub>F</sub> at I <sub>F</sub> = 10 A (T <sub>A</sub> = 125 °C)	0.40 V			
T <sub>J</sub> max.	150 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.44	-	V	
	I <sub>F</sub> = 10 A			0.49	0.57		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.34	-		
	I <sub>F</sub> = 10 A			0.40	0.50		
Reverse current at rated $V_R$ per diode	V <sub>R</sub> = 45 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	1	- mA	
		T <sub>A</sub> = 125 °C		10	20		
Typical junction capacitance	4.0 V, 1 MHz		CJ	1900	-	pF	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}\,$  Pulse test: Pulse width  $\leq 5\mbox{ ms}$ 

<b>THERMAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V20D45C	UNIT	
Typical thermal resistance per device	R <sub>0JC</sub> <sup>(1)</sup>	1.8	°C/W	
	R <sub>0JA</sub> <sup>(2)(3)</sup>	48		

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub> $\theta$ JA</sub>

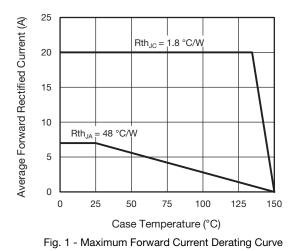
(3) Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V20D45C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V20D45CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

Note

<sup>(1)</sup> AEC-Q101 qualified

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)



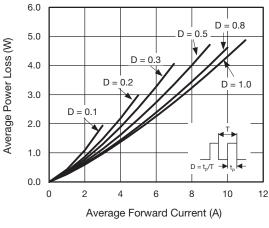
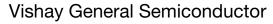


Fig. 2 - Average Power Loss Characteristics

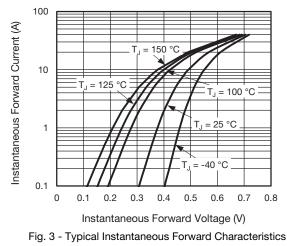
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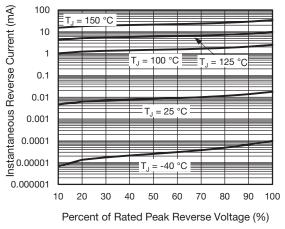
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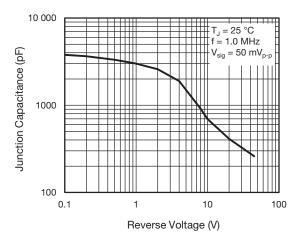


Fig. 5 - Typical Junction Capacitance

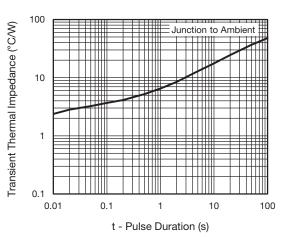


Fig. 6 - Typical Transient Thermal Impedance

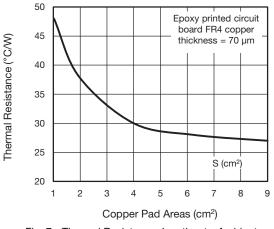


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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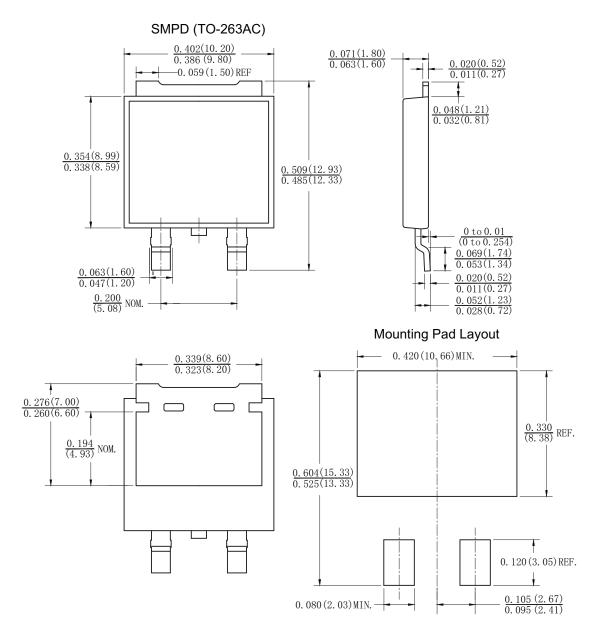
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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