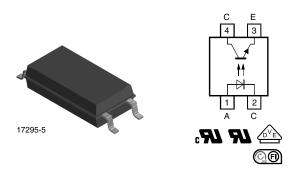
TCLT101. Series

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Vishay Semiconductors

Optocoupler, Phototransistor Output, LSOP-4, 110 °C Rated, Long Mini-Flat Package



DESCRIPTION

The TCLT101. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4-lead LSOP package.

APPLICATIONS

- Switchmode power supplies
- Computer peripheral interface
- Microprocessor system interface

FEATURES

- SMD low profile 4 lead package
- High isolation 5000 V_{RMS}
- CTR flexibility available see order information
- Extra low coupling capacitance
- Connected base
- DC input with transistor output
- Temperature range -55 °C to +110 °C
- Creepage distance > 8 mm
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

AGENCY APPROVALS

- UL1577, file no. E76222
- cUL file no. E76222, equivalent to CSA bulletin 5A
- DIN EN 60747-5-5
- FIMKO: EN 60950

ORDERING INFO	RMATION							
ТС	L	Т	1	0	1	¥	LSOP-	-4
	PART NUMBER					im 🛌		
AGENCY	AGENCY CTR (%)							
CERTIFIED/PACKAGE	5 mA	10	mA	5 mA				
UL, cUL, VDE, FIMKO	50 to 600	63 to 125	100 to 200	50 to 150	100 to 300	80 to 160	130 to 260	200 to 400
LSOP-4	TCLT1010	TCLT1012	TCLT1013	TCLT1015	TCLT1016	TCLT1017	TCLT1018	TCLT1019

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V _R	6	V			
Forward current		I _F	60	mA			
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	А			
Power dissipation		P _{diss}	100	mW			
Junction temperature		Τ _j	125	°C			
OUTPUT							
Collector emitter voltage		V _{CEO}	70	V			
Emitter collector voltage		V _{ECO}	7	V			
Collector current		I _C	50	mA			
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA			
Power dissipation		P _{diss}	150	mW			
Junction temperature		Тj	125	°C			

1 For technical questions, contact: <u>optocoupleranswers@vishay.com</u> Document Number: 81256



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(5-2008)

TCLT101. Series



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER TEST CONDITION SYMBOL VALUE UNIT								
COUPLER								
Total power dissipation		P _{tot}	250	mW				
Operating ambient temperature range		T _{amb}	-55 to +110	°C				
Storage temperature range		T _{stg}	-55 to +125	°C				
Soldering temperature ⁽¹⁾		T _{sld}	260	°C				

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (<u>www.vishay.com/doc?80054</u>).

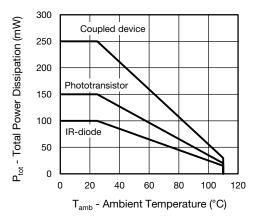


Fig. 1 - Total Power Dissipation vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION SYMBOL MIN. TYP. MAX. U							
INPUT								
Forward voltage	I _F = 50 mA	V _F	-	1.25	1.6	V		
Junction capacitance	V _R = 0 V, f = 1 MHz	Cj	-	50	-	pF		
OUTPUT								
Collector emitter voltage	$I_{\rm C} = 1 \rm{mA}$	V _{CEO}	70	-	-	V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7	-	-	V		
Collector emitter leakage current	$V_{CE} = 20 \text{ V}, \text{ I}_{F} = 0 \text{ A}$	I _{CEO}	-	10	100	nA		
COUPLER								
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}	-	-	0.3	V		
Cut-off frequency	V_{CE} = 5 V, I_F = 10 mA, R_L = 100 Ω	f _c	-	110	-	kHz		
Coupling capacitance	f = 1 MHz	C _k	-	0.3	-	pF		

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = 5 \text{ mA}$	TCLT1010	CTR	50	-	600	%	
	V _{CE} = 5 V, I _E = 10 mA	TCLT1012	CTR	63	-	125	%	
	$v_{CE} = 5 v, i_F = 10 mA$	TCLT1013	CTR	100	-	200	%	
	V _{CE} = 5 V, I _F = 1 mA	TCLT1012	CTR	22	45	-	%	
I _C /I _F		TCLT1013	CTR	34	70	-	%	
IC/IF	V _{CE} = 5 V, I _F = 5 mA	TCLT1015	CTR	50	-	150	%	
		TCLT1016	CTR	100	-	300	%	
		TCLT1017	CTR	80	-	160	%	
		TCLT1018	CTR	130	-	260	%	
		TCLT1019	CTR	200	-	400	%	

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	1.6	kV
Partial discharge test voltage -	t _{Tr} = 60 s, t _{test} = 10 s,	V _{IOTM}	8	kV
lot test (sample test)	(see figure 2)	V _{pd}	1.3	kV
Isolation test voltage (RMS)		V _{ISO}	5000	V _{RMS}
	V _{IO} = 500 V	R _{IO}	10 ¹²	Ω
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	10 ¹¹	Ω
	V _{IO} = 500 V, T _{amb} = 150 °C (construction test only)	R _{IO}	10 ⁹	Ω
Forward current		I _{si}	130	mA
Power dissipation		P _{so}	265	mW
Rated impulse voltage		V _{IOTM}	8	kV
Safety temperature		T _{si}	150	°C
Clearance distance			8.0	mm
Creepage distance			8.0	mm
Insulation distance (internal)			0.40	mm

Note

 According to DIN EN 60747-5-2 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

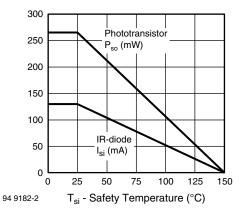


Fig. 2 - Derating Diagram

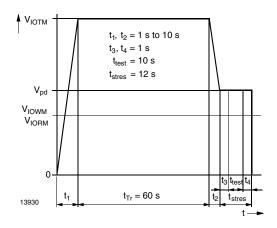


Fig. 3 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-2 (VDE 0884); IEC 60747-5-5

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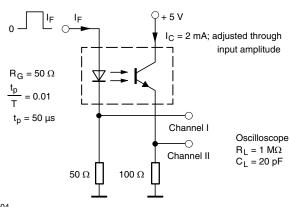
TCLT101. Series



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SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Delay time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	t _d	-	3	-	μs			
Rise time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 2 \text{ mA}, \text{ R}_{L} = 100 \Omega,$ (see figure 3)	t _r	-	3	-	μs			
Fall time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 2 \text{ mA}, \text{ R}_{L} = 100 \Omega,$ (see figure 3)	t _f	-	4.7	-	μs			
Storage time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	ts	-	0.3	-	μs			
Turn-on time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 2 \text{ mA}, \text{ R}_{L} = 100 \Omega,$ (see figure 3)	t _{on}	-	6	-	μs			
Turn-off time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 2 \text{ mA}, \text{ R}_{L} = 100 \Omega,$ (see figure 3)	t _{off}	-	5	-	μs			
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega,$ (see figure 4)	t _{on}	-	9	-	μs			
Turn-off time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega,$ (see figure 4)	t _{off}	-	10	-	μs			



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Fig. 4 - Test Circuit, Non-Saturated Operation

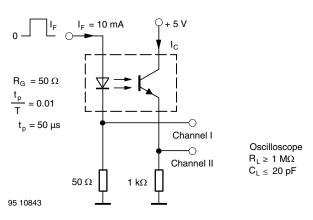


Fig. 5 - Test Circuit, Saturated Operation

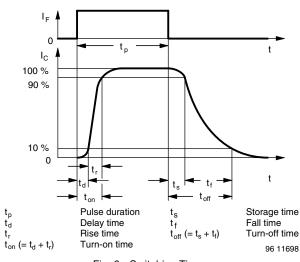


Fig. 6 - Switching Times

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TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)

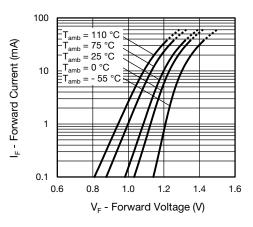


Fig. 7 - Forward Voltage vs. Forward Current

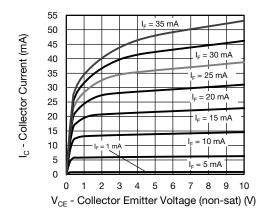


Fig. 8 - Collector Current vs. Collector Emitter Voltage (NS)

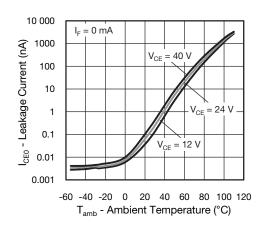


Fig. 9 - Leakage Current vs. Ambient Temperature

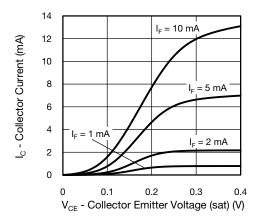


Fig. 10 - Collector Current vs. Collector Emitter Voltage (saturated)

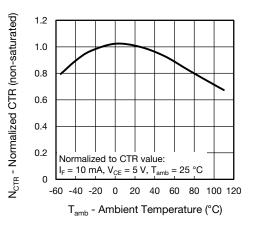


Fig. 11 - Normalized Current Transfer Ratio (non-saturated) vs. Ambient Temperature

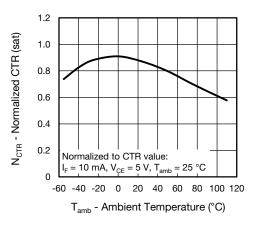


Fig. 12 - Normalized Current Transfer Ratio (saturated) vs. Ambient Temperature

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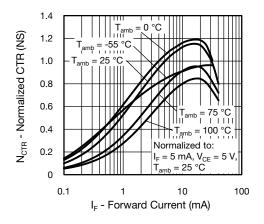


Fig. 13 - Normalized CTR (non-saturated) vs. Forward Current

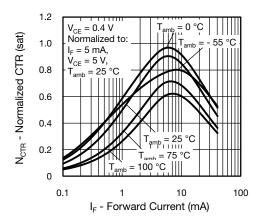


Fig. 14 - Normalized CTR (saturated) vs. Forward Current

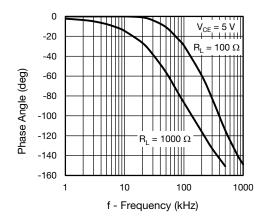


Fig. 15 - Phase Angle vs. Frequency

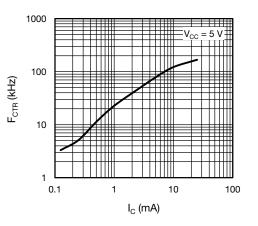


Fig. 16 - CTR Frequency vs. Collector Current

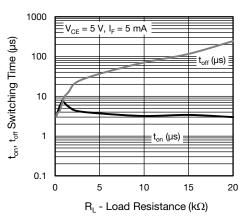


Fig. 17 - Switching Time vs. Load Resistance

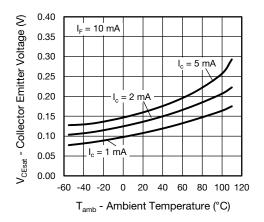
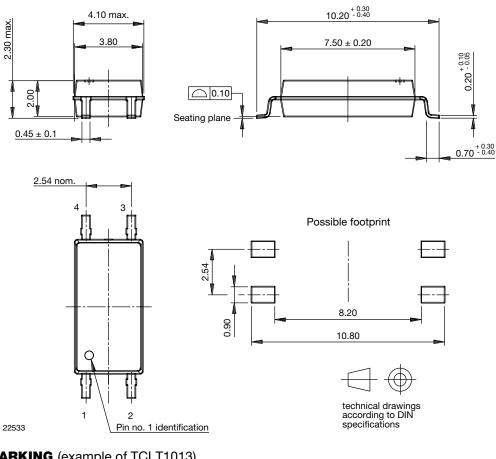


Fig. 18 - Collector Emitter Voltage vs. Ambient Temperature (saturated)

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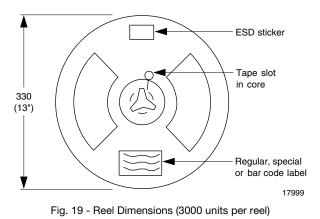
PACKAGE DIMENSIONS (in millimeters)



PACKAGE MARKING (example of TCLT1013)



TAPE AND REEL DIMENSIONS (in millimeters)



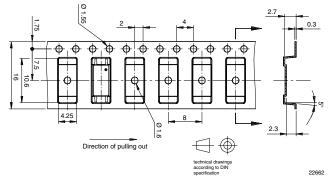


Fig. 20 - Tape Dimensions

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SOLDER PROFILE

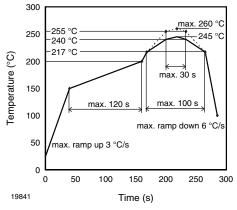


Fig. 21 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020 **Vishay Semiconductors**

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions: $T_{amb} < 30$ °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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