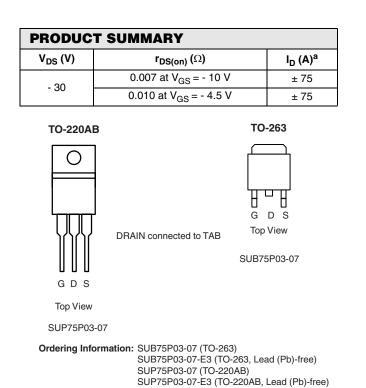
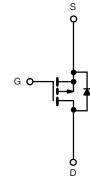


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COMPLIANT

P-Channel 30-V (D-S) 175 °C MOSFET





P-Channel MOSFET

Parameter	Symbol	Limit ± 20	Unit V		
Gate-Source Voltage	V _{GS}				
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	1-	- 75 ^a	А	
	T _C = 125 °C	I _D	- 65		
Pulsed Drain Current	I _{DM}	- 240			
Avalanche Current		I _{AR}	- 60	1	
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	180	mJ	
Power Dissipation	T_{C} = 25 °C (TO-220AB and TO-263)	Р	187 ^d	w	
	T _A = 25 °C (TO-263) ^c	P _D	3.75		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Limit	Unit		
Junction-to-Ambient	PCB Mount (TO-263) ^c	R _{thJA}	40	°C/W		
	Free Air (TO-220AB)	¹ 'thJA	62.5			
Junction-to-Case		R _{thJC}	0.8			

Notes:

a. Package limited.

/ISHA

b. Duty cycle \leq 1 %.

c. When Mounted on 1" square PCB (FR-4 material).

d. See SOA curve for voltage derating.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

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Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static		·				
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 30			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 3	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μΑ
		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			- 250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 120			А
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 30 A		0.0055	0.007	Ω
		V_{GS} = - 10 V, I _D = - 30 A, T _J = 125 °C			0.010	
		V_{GS} = - 10 V, I _D = - 30 A, T _J = 175 °C			0.013	
		V _{GS} = - 4.5 V, I _D = - 20 A		0.008	0.010	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 75 A	20			S
Dynamic ^b	*	•		•	•	
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = - 25 V, f = 1 MHz		9000		pF
Output Capacitance	C _{oss}			1565		
Reversen Transfer Capacitance	C _{rss}			715		
Total Gate Charge ^c	Qg			160	240	nC
Gate-Source Charge ^c	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 10 V, I_{D} = - 75 A		32		
Gate-Drain Charge ^c	Q _{gd}			30		
Turn-On Delay Time ^c	t _{d(on)}			25	40	- ns
Rise Time ^c	t _r	V_{DD} = - 15 V, R _L = 0.2 Ω I _D \cong - 75 A, V _{GEN} = - 10 V, R _G = 2.5 Ω		225	360	
Turn-Off Delay Time ^c	t _{d(off)}			150	240	
Fall Time ^c	t _f			210	340	
Source-Drain Diode Ratings and Cha	racteristics (T _C = 25 °C) ^b				
Continuous Current	ا _S				- 75	^
Pulsed Current	I _{SM}				- 240	A
Forward Voltage ^a	V _{SD}	I _F = - 75 A, V _{GS} = 0 V		- 1.2	- 1.5	V
Reverse Recovery Time	t _{rr}			55	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 75 A, di/dt = 100 A/μs		2.5	5	Α
Reverse Recovery Charge	Q _{rr}	1		0.07	0.25	μC

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

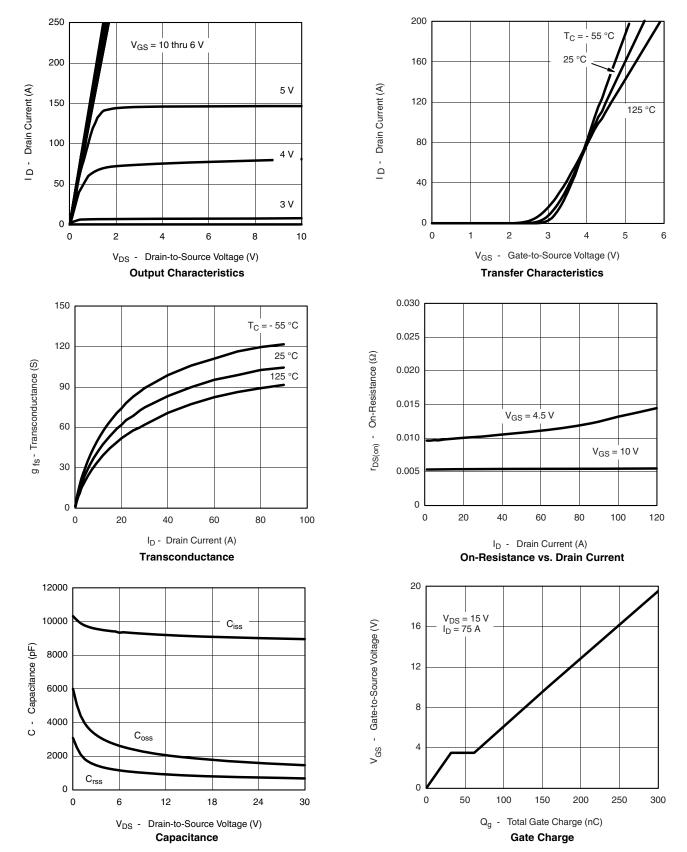
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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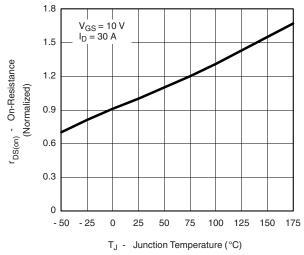
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



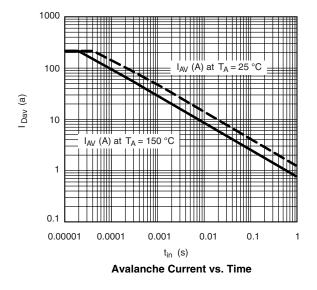
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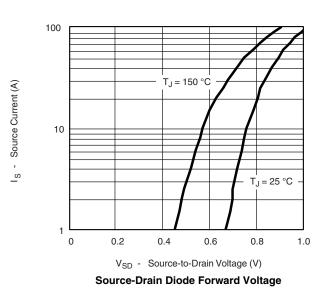
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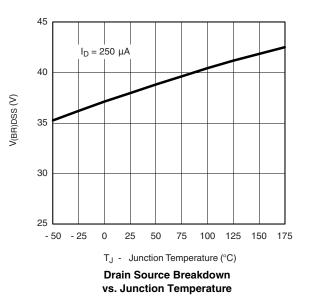
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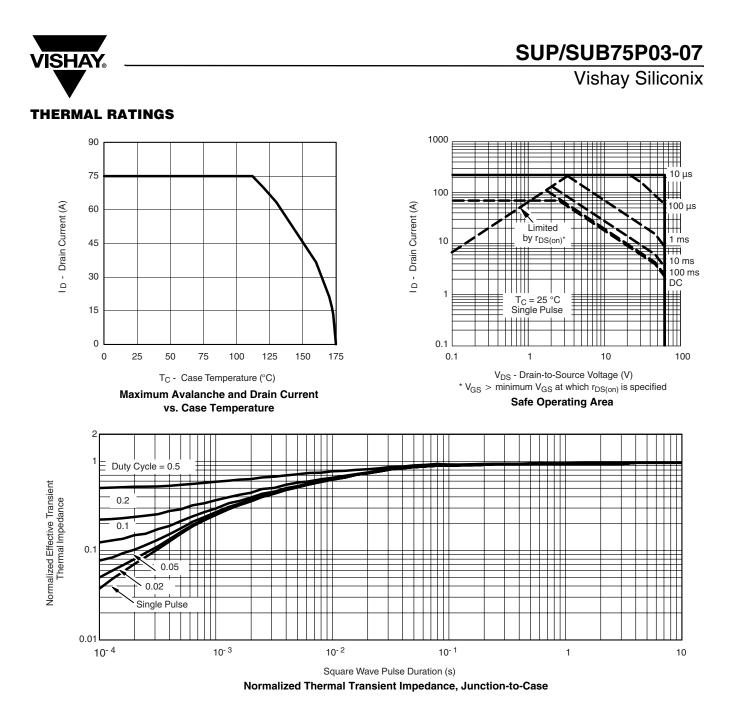


On-Resistance vs. Junction Temperature









Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71109.



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