

## Sample Kit

### Precision Wide Terminal Thin Film Chip Resistors

### MCW 0406-25 0.1 % AT



#### FEATURES

- Rated dissipation  $P_{70}$  up to 0.250 W
- Resistance range down to 1R
- Superior temperature cycling robustness
- Operating temperature up to 155 °C
- AEC-Q200 qualified (pending)
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT

Files contain lengths of cardboard tape, each holding 20 resistors. The kit contains every fourth value of the E 96-series. Parts are sorted in the ascending order of its resistance value and appropriately marked.

SPECIFICATIONS	
Laboratory kit	LCW AT 96/4
Resistor type	MCW 0406-25 0.1 % AT
Temperature coefficient	$\pm 25$ ppm/K
Tolerance	$\pm 0.1$ %
Resistance range/E-series	47 $\Omega$ to 100 k $\Omega$ /every 4 <sup>th</sup> value of E 96 <sup>(1)</sup>
Number of resistance values	80
Resistors per value/total	20/1600
Number of pages/files	8/1

#### Notes

- The products are specified by the underlying standard product's datasheet "Precision Wide Terminal Thin Film Chip Resistor" in its most current edition at [www.vishay.com/doc?28847](http://www.vishay.com/doc?28847)
- <sup>(1)</sup> Supplemented by 47R

PART NUMBER AND PRODUCT DESCRIPTION																	
PART NUMBER: LCW964MCW0406MDB00																	
L	C	W	9	6	4	M	C	W	0	4	0	6	M	D	B	0	0
KIT		E-SERIES		FRACTION OF E-SERIES		TYPE/SIZE		VERSION		TCR		TOLERANCE					
LCW		96		4 = 1/4		MCW 0406		M = AT		D = ± 25 ppm/K		B = ± 0.1 %					
PRODUCT DESCRIPTION: LCW 96 4 MCW 0406-25 0.1 % AT																	
LCW		96		4		MCW 0406		-25		0.1 %		AT					
KIT		E-SERIES		FRACTION OF E-SERIES		TYPE/SIZE		TCR		TOLERANCE		VERSION					
LCW		96		4 = 1/4		MCW 0406		± 25 ppm/K		± 0.1 %		AT					

#### Note

- The product can be ordered using either the PART NUMBER or the PRODUCT DESCRIPTION

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Vishay:

[LCW964MCW0406MDB00](#)