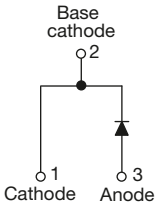
High Performance Schottky Rectifier, 15 A

2L TO-220AC



FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

DESCRIPTION

The VS-15TQ060... Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	15 A
V_R	60 V
V_F at I_F	0.56 V
I_{RM} typ.	45 mA at 125 °C
T_J max.	150 °C
E_{AS}	6 mJ
Package	2L TO-220AC
Circuit configuration	Single

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	15	A
V_{RRM}		60	V
I_{FSM}	t_p = 5 µs sine	1000	A
V_F	15 A_{pk}, T_J = 125 °C	0.56	V
T_J	Range	-55 to +150	°C

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-15TQ060-M3	UNITS
Maximum DC reverse voltage	V_R	60	V
Maximum working peak reverse voltage	V_{RWM}		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at T_C = 104 °C, rectangular waveform		15	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I_{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V_{RRM} applied	1000	
		10 ms sine or 6 ms rect. pulse		260	
Non-repetitive avalanche energy	E_{AS}	T_J = 25 °C, I_{AS} = 1.50 A, L = 11.5 mH		6	mJ
Repetitive avalanche current	I_{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1.50	A

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V_{FM} (1)	15 A	T_J = 25 °C	0.62	V
		30 A		0.82	
		15 A	T_J = 125 °C	0.56	
		30 A		0.71	
Maximum reverse leakage current	I_{RM} (1)	T_J = 25 °C	V_R = Rated V_R	0.80	mA
		T_J = 125 °C		160	
Typical reverse leakage current	I_{RM} (1)	T_J = 125 °C	V_R = Rated V_R	45	mA
Maximum junction capacitance	C_T	V_R = 5 V_{DC}, (test signal range 100 kHz to 1 MHz) 25 °C		720	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body		8	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/µs

Note

(1) Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_J, T_{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case		R_{thJC}	DC operation See fig. 4	3.25	°C/W
Typical thermal resistance, case to heatsink		R_{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm (lbf · in)
	maximum			12 (10)	
Marking device			Case style 2L TO-220AC (JEDEC®)	15TQ060	

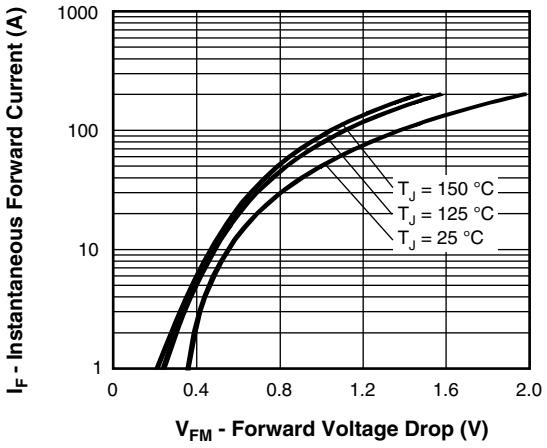


Fig. 1 - Maximum Forward Voltage Drop Characteristics

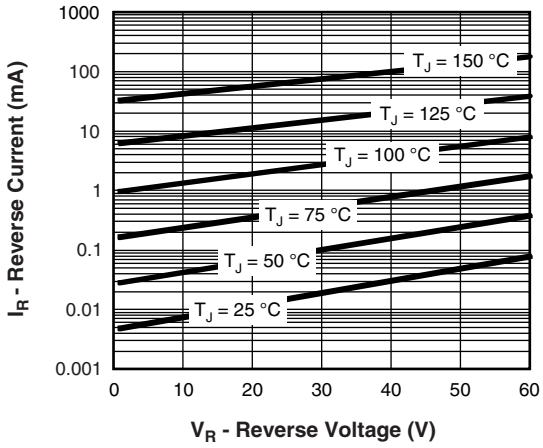


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

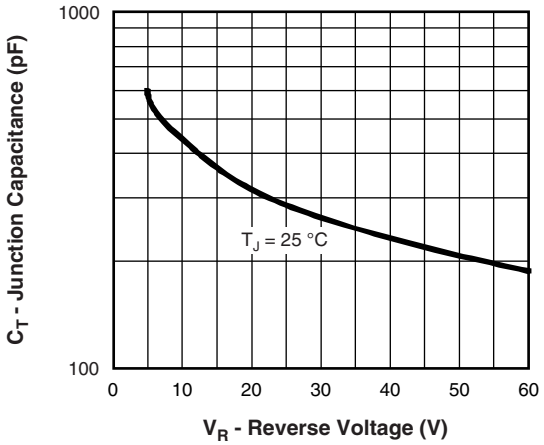


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

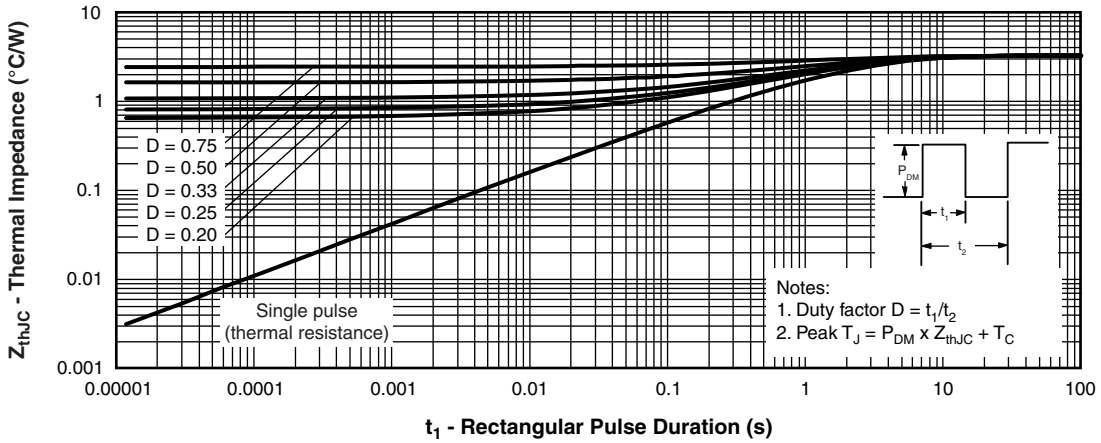


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

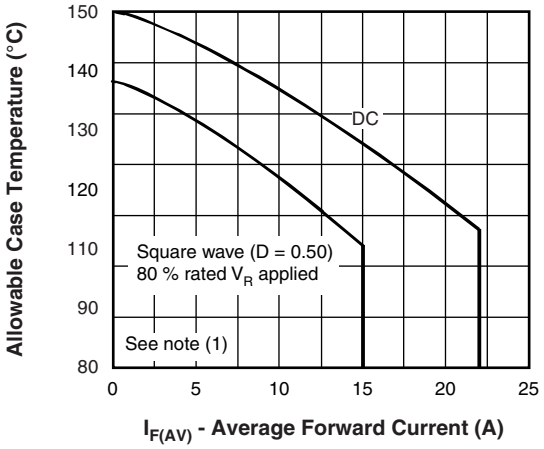


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

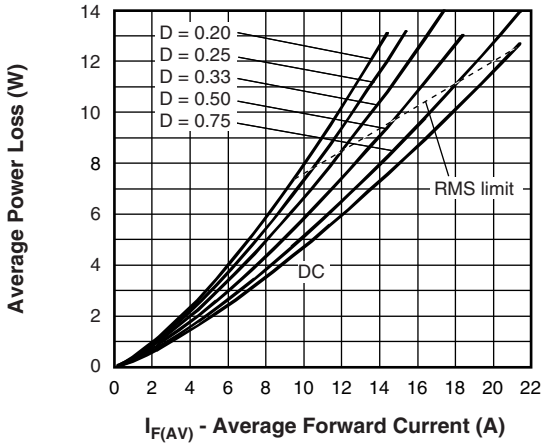


Fig. 6 - Forward Power Loss Characteristics

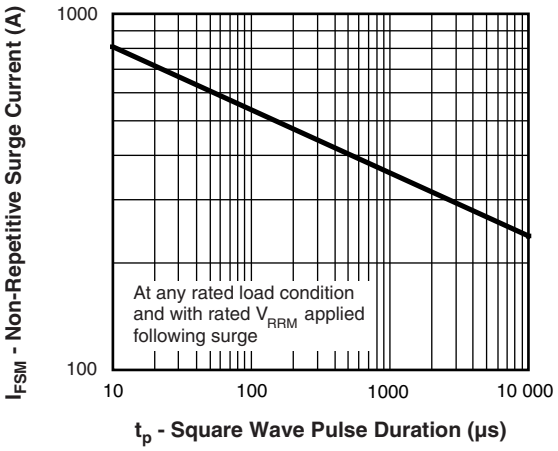


Fig. 7 - Maximum Non-Repetitive Surge Current

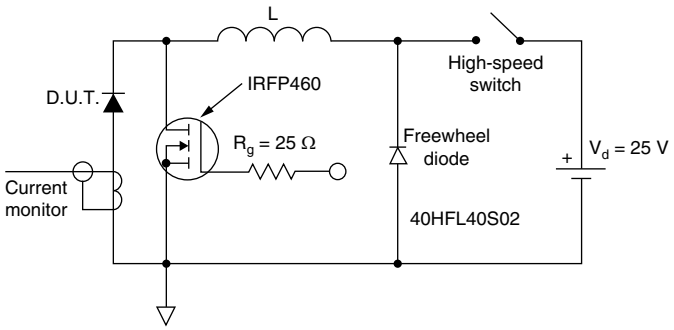


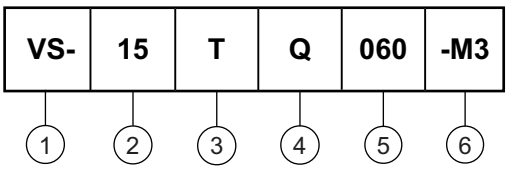
Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}$/D) (see fig. 6);
Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code



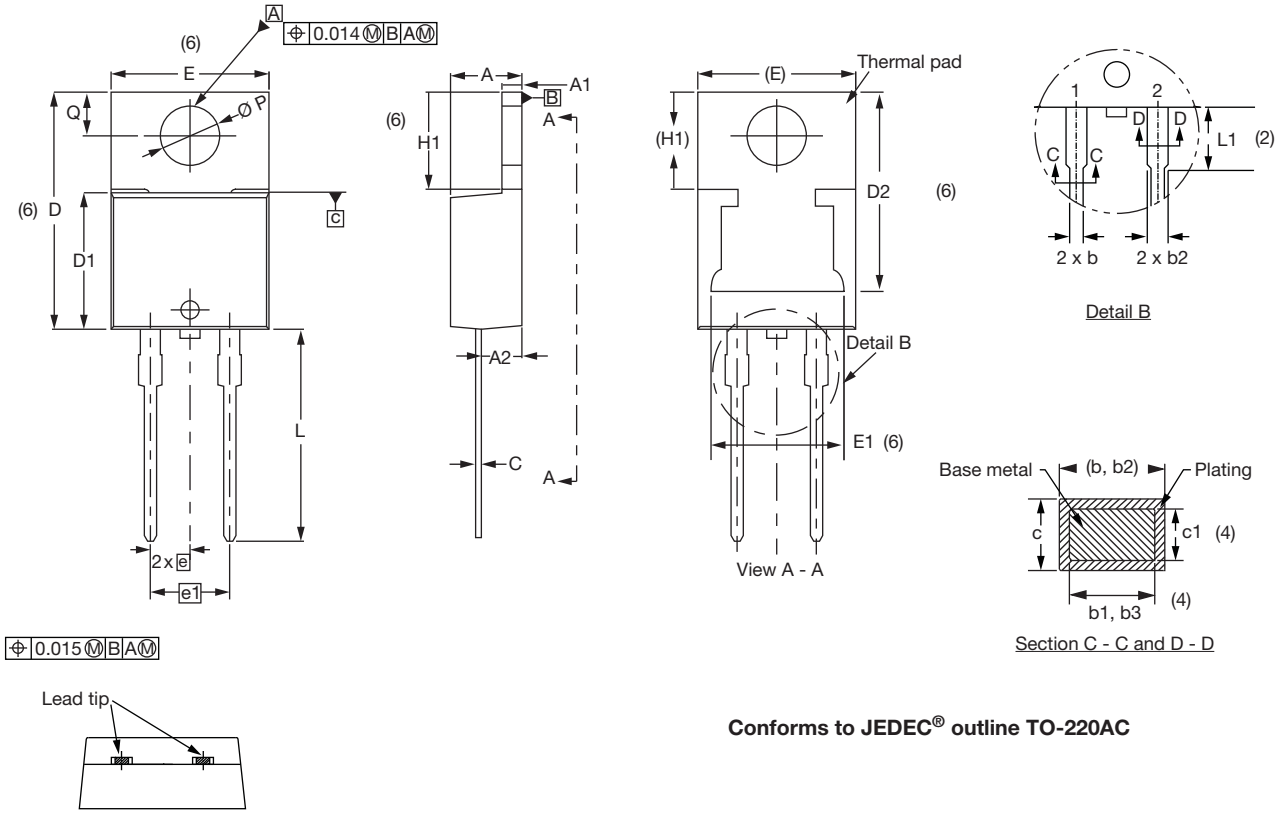
1 - Vishay Semiconductors product
2 - Current rating (15 = 15 A)
3 - Package:
T = TO-220
4 - Schottky “Q” series
5 - Voltage rating (060 = 60 V)
6 - Environmental digit
-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	**QUANTITY PER T/R**	**MINIMUM ORDER QUANTITY**	**PACKAGING DESCRIPTION**
VS-15TQ060-M3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96156
Part marking information	www.vishay.com/doc?95391
SPICE model	www.vishay.com/doc?95600

2L TO-220AC

DIMENSIONS in millimeters and inches



Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
c	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
e	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
Ø P	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Notes

(1) Dimensioning and tolerancing as per ASME Y14.5M-1994
(2) Lead dimension and finish uncontrolled in L1
(3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
(4) Dimension b1, b3, and c1 apply to base metal only
(5) Controlling dimensions: inches
(6) Thermal pad contour optional within dimensions E, H1, D2, and E1
(7) Outline conforms to JEDEC® TO-220, except D2

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