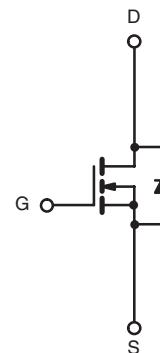
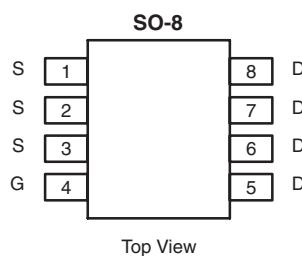


## N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY		
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
20	0.025 at $V_{GS} = 4.5$ V	$\pm 8.5$
	0.035 at $V_{GS} = 2.5$ V	$\pm 7.1$

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC



**Ordering Information:** Si4426DY-T1-E3 (Lead (Pb)-free)  
Si4426DY-T1-GE3 (Lead (Pb)-free and Halogen free)

N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	10 s	Steady State	Unit
Drain-Source Voltage	$V_{DS}$	20	$\pm 12$	V
Gate-Source Voltage	$V_{GS}$			
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$I_D$ ( $T_A = 25$ °C)	$\pm 8.5$	$\pm 6.5$	A
	$I_D$ ( $T_A = 70$ °C)	$\pm 6.8$	$\pm 5.2$	
Pulsed Drain Current (10 $\mu$ s Pulse Width)	$I_{DM}$	$\pm 40$		
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	2.1	2.1	
Maximum Power Dissipation <sup>a</sup>	$P_D$ ( $T_A = 25$ °C)	2.5	1.5	W
	$P_D$ ( $T_A = 70$ °C)	1.6	0.9	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ s	38	50	°C/W
	Steady State	70	85	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$ (Steady State)	20	25	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

**SPECIFICATIONS**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$	0.6		1.4	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 12 \text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 20 \text{ V}$ , $V_{GS} = 0 \text{ V}$		1		$\mu\text{A}$
		$V_{DS} = 20 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $T_J = 55^\circ\text{C}$		5		
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{DS} \geq 5 \text{ V}$ , $V_{GS} = 4.5 \text{ V}$	40			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = 4.5 \text{ V}$ , $I_D = 8.5 \text{ A}$		0.019	0.025	$\Omega$
		$V_{GS} = 2.5 \text{ V}$ , $I_D = 7.1 \text{ A}$		0.025	0.035	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 10 \text{ V}$ , $I_D = 8.5 \text{ A}$		27		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 2.1 \text{ A}$ , $V_{GS} = 0 \text{ V}$		0.8	1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 10 \text{ V}$ , $V_{GS} = 4.5 \text{ V}$ , $I_D = 8.5 \text{ A}$		25	50	nC
Gate-Source Charge	$Q_{gs}$			6.5		
Gate-Drain Charge	$Q_{gd}$			4		
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = 10 \text{ V}$ , $R_L = 10 \Omega$ $I_D \approx 1 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ , $R_g = 6 \Omega$		40	60	ns
Rise Time	$t_r$			40	60	
Turn-Off Delay Time	$t_{d(\text{off})}$			90	150	
Fall Time	$t_f$			40	60	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 2.1 \text{ A}$ , $di/dt = 100 \text{ A}/\mu\text{s}$		40	60	

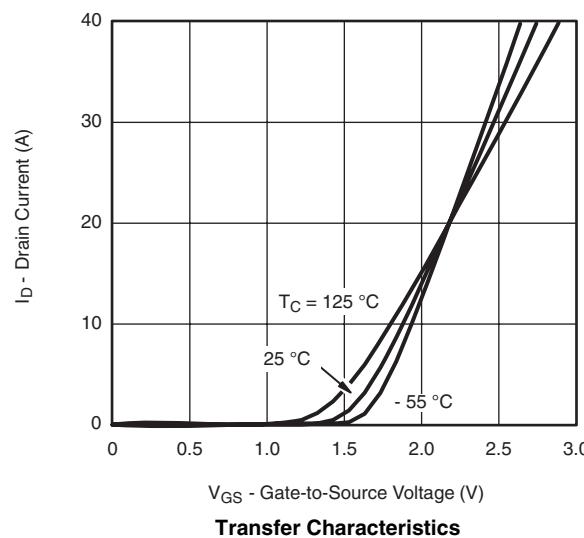
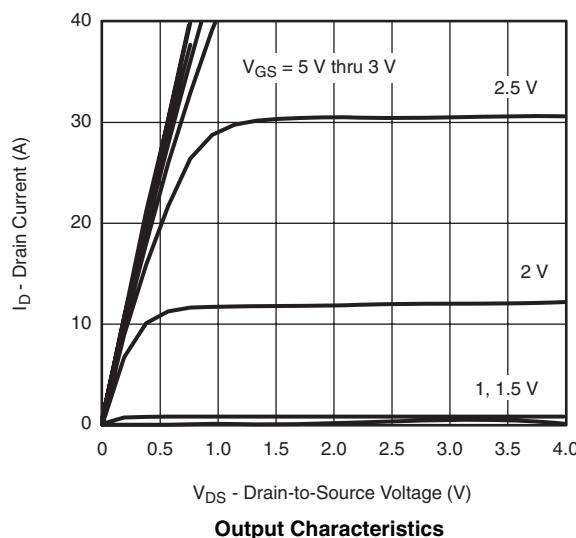
Notes:

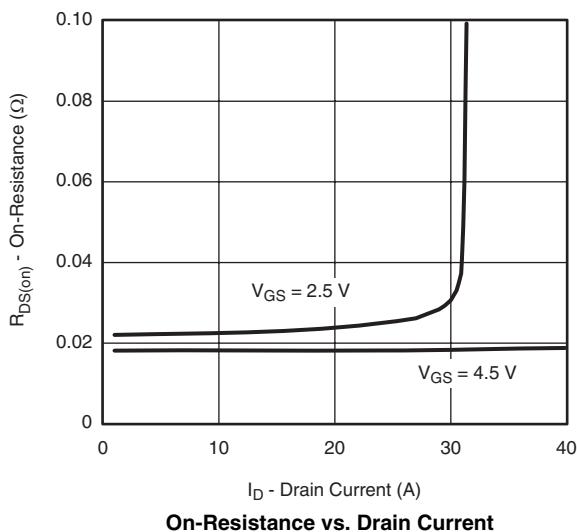
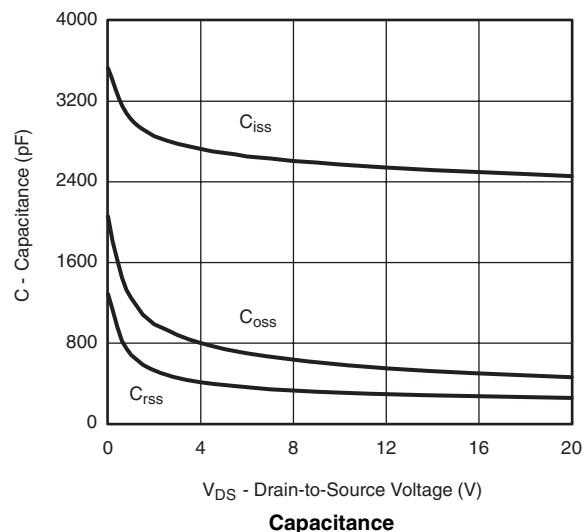
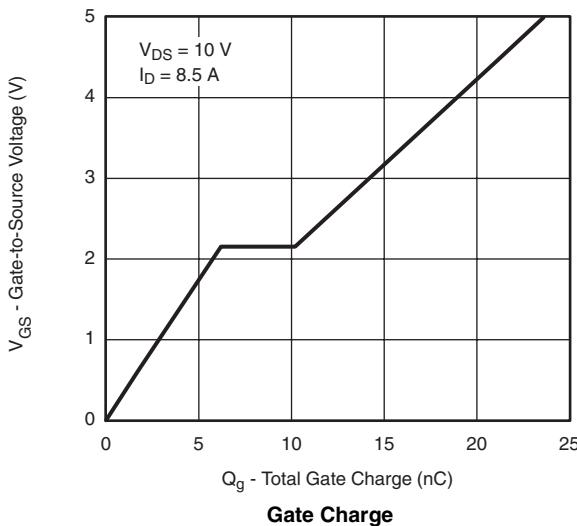
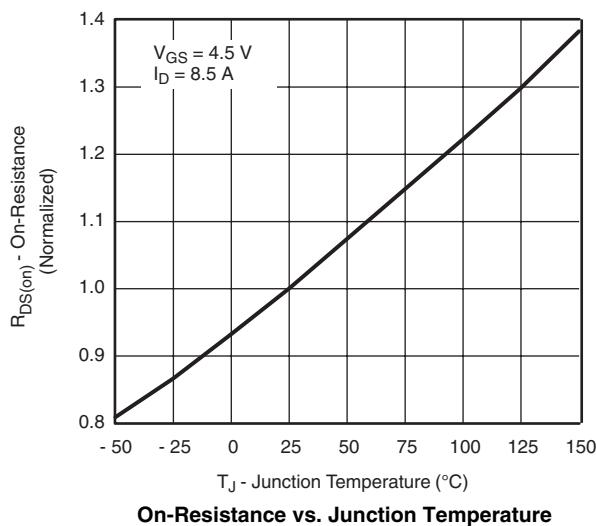
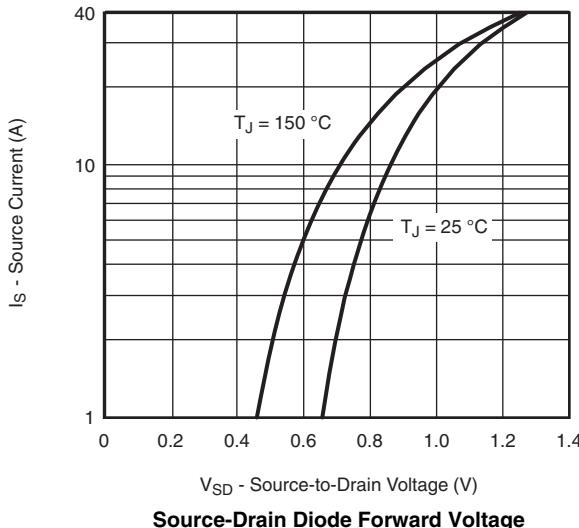
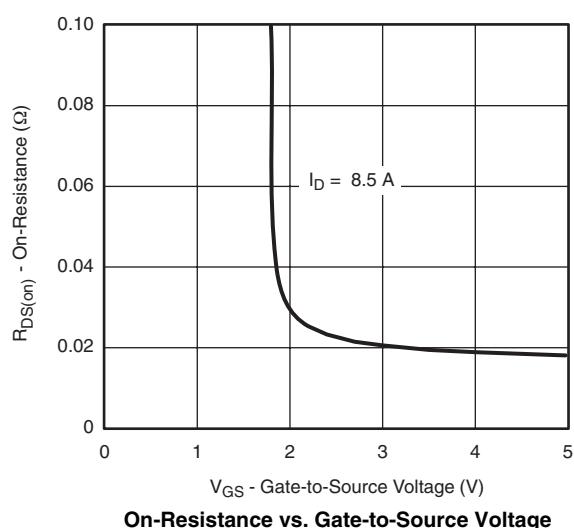
a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

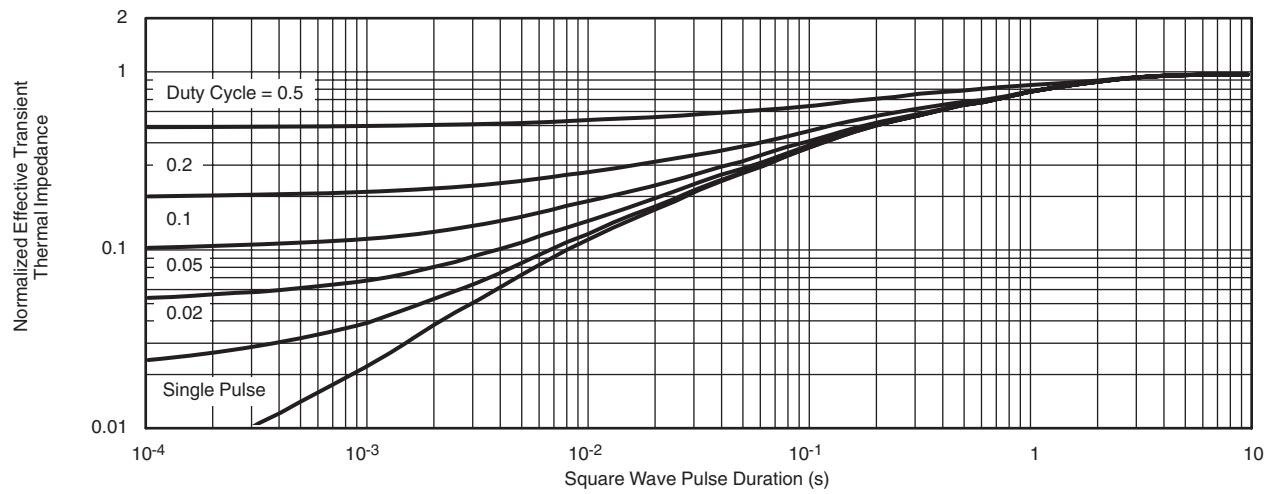
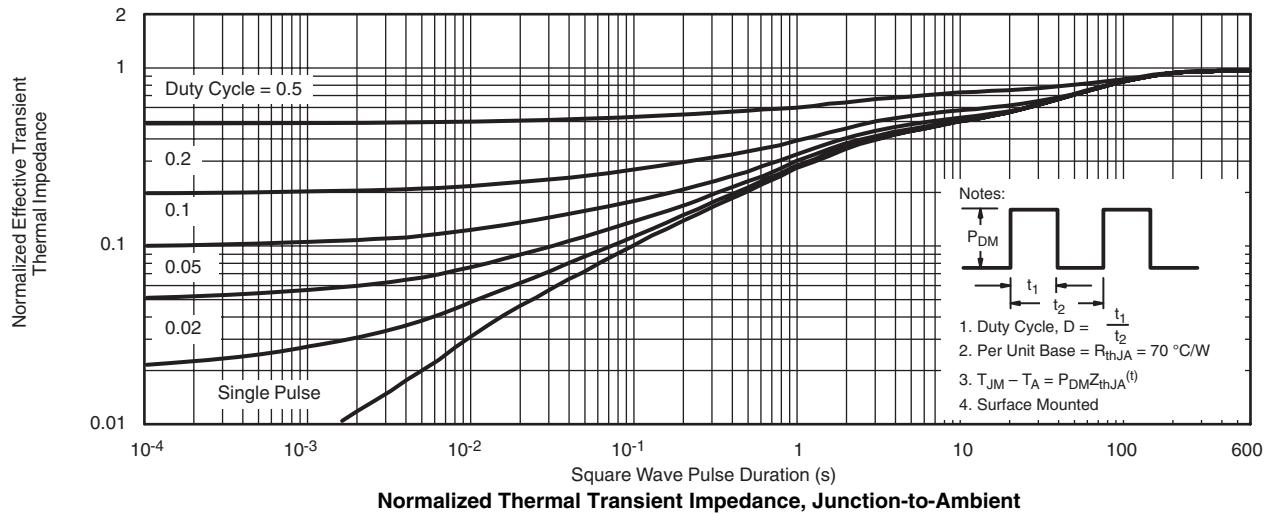
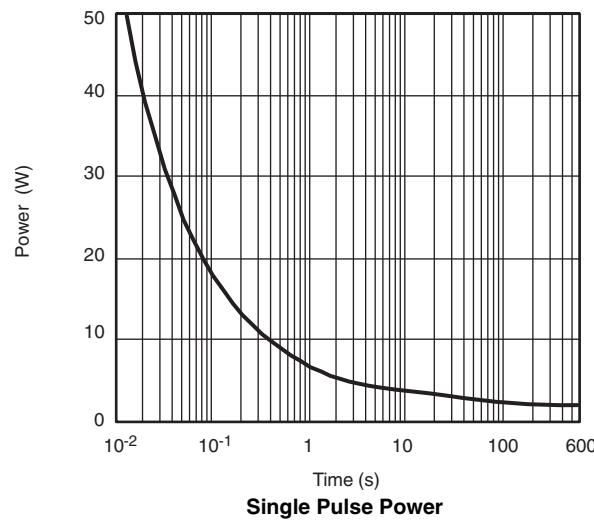
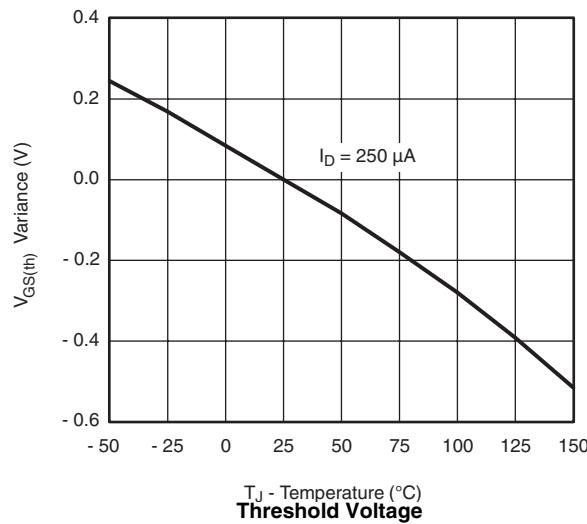
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS**  $25^\circ\text{C}$ , unless otherwise noted



**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

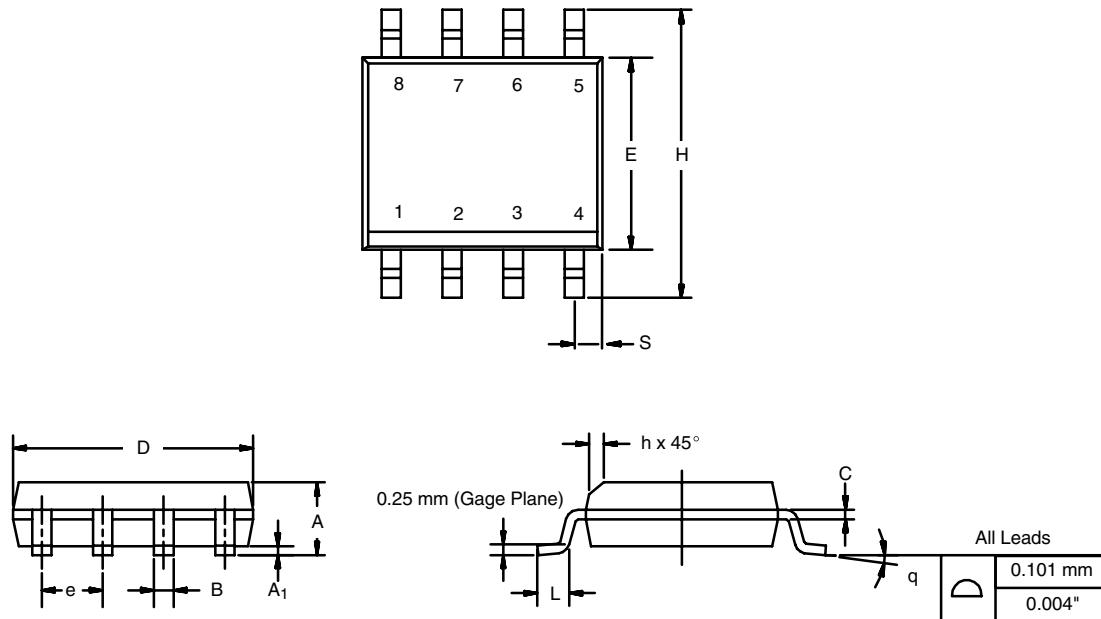
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see [www.vishay.com/ppg?71107](http://www.vishay.com/ppg?71107).

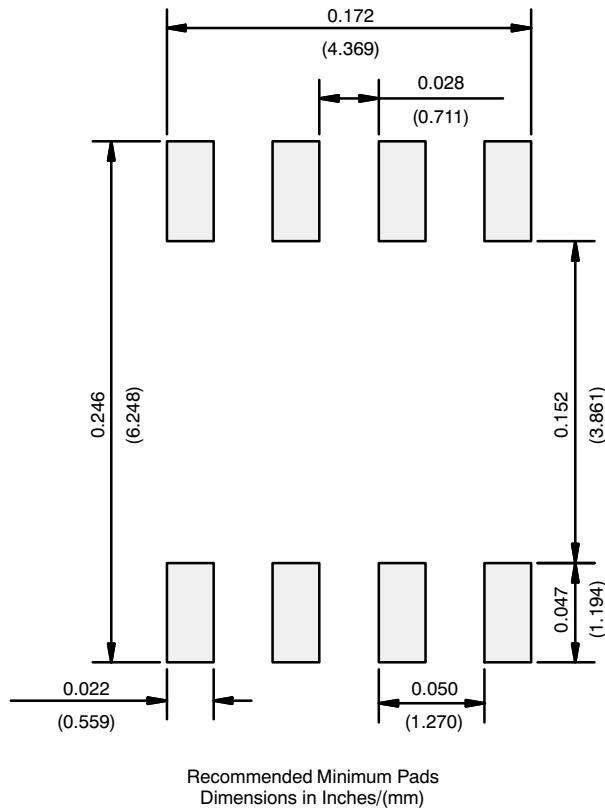
### SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

## RECOMMENDED MINIMUM PADS FOR SO-8



[Return to Index](#)

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