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### Vishay Semiconductors

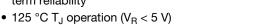
# High Performance Schottky Rectifier, 1.0 A



PRODUCT SUMMARY				
Package	SMB			
I <sub>F(AV)</sub>	1.0 A			
$V_{R}$	15 V			
V <sub>F</sub> at I <sub>F</sub>	0.21 V			
I <sub>RM</sub>	35 mA at 100 °C			
T <sub>J</sub> max.	125 °C			
Diode variation	Single die			
E <sub>AS</sub>	1.0 mJ			

#### **FEATURES**

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- · Optimized for OR-ing applications
- optimized for OTI-ing application
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-10BQ015-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	1.0	A		
V <sub>RRM</sub>		15	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	140	Α		
V <sub>F</sub>	1.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.21	V		
T <sub>J</sub>	Range	-55 to +125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ015-M3	UNITS	
Maximum DC reverse voltage	$V_{R}$	15	V	
Maximum working peak reverse voltage	$V_{RWM}$	25	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 134 °C	, rectangular waveform	1.0	А
Maximum peak one cycle non-repetitive surge current	I	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	140	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	40	A
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 2 mH		1.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	А



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	)/ (1)	1 A	T <sub>J</sub> = 25 °C	0.33	V
Maximum forward voltage drop		2 A		0.39	
See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	1 A	T <sub>J</sub> = 125 °C	0.21	
		2 A		0.29	
Maximum reverse leakage current		T <sub>J</sub> = 25 °C	V Batad V-	0.5	m A
See fig. 2	I <sub>RM</sub>	$V_R = Rated V_R$		35	- mA
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		-	V
Forward slope resistance	r <sub>t</sub>			-	mΩ
Typical junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		390	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}</sup>$  Pulse width = 300  $\mu$ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T <sub>J</sub> <sup>(1)</sup>		-55 to +125	°C
Maximum storage temperature range	T <sub>Stg</sub>		-55 to +150	C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> (2)	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	OZ.
Marking device		Case style SMB (similar to DO-214AA)	1	С

#### Notes

 $<sup>\</sup>frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ 

<sup>(2)</sup> Mounted 1" square PCB

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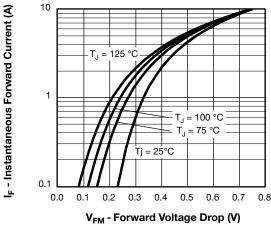


Fig. 1 - Maximum Forward Voltage Drop Characteristics

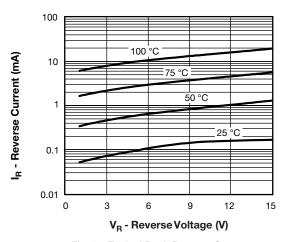


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

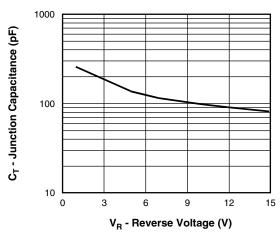


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

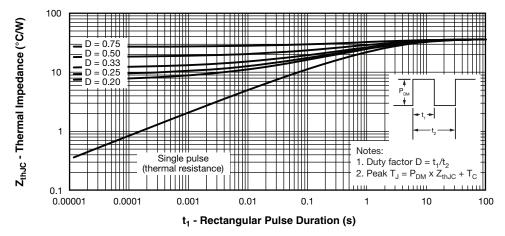


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)



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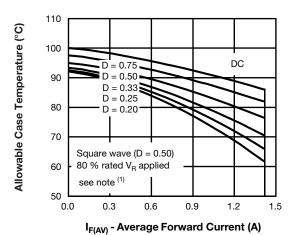


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

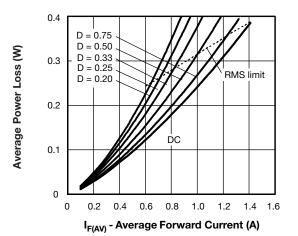


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

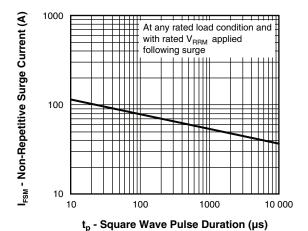


Fig. 7 - Maximum Non-Repetitive Surge Current

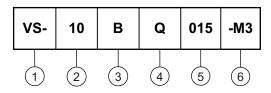
#### Note



### Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- 2 Current rating
- 3 B = SMB
- 4 Q = Schottky "Q" series
- 5 Voltage rating (015 = 15 V)
- 6 Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-10BQ015-M3/5BT	5BT	3200	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95401</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?95666			



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