AUTOMOTIVE

ROHS

HALOGEN

FREE GREEN

(5-2008)



Vishay Semiconductors

Silicon PIN Photodiode



DESCRIPTION

VEMD2523SLX01 is a high speed and high sensitive PIN photodiode in a miniature side looking, surface mount package (SMD) with dome lens. The clear epoxy allows light detection of a wide wavelength range from 350 nm to 1120 nm. The photo sensitive area of the chip is 0.23 mm².

FEATURES

- Package type: surface mount
- Package form: side view
- Dimensions (L x W x H in mm): 2.3 x 2.55 x 2.3
- AEC-Q101 qualified
- · High radiant sensitivity
- Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 35^{\circ}$
- Package matched with IR emitter series VSMB2943SLX01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>



- · High speed photo detector
- · Light curtain
- · Detector for optical switch

| PRODUCT SUMMARY | | | | |
|-----------------|----------------------|---------|-----------------------|--|
| COMPONENT | I _{ra} (μΑ) | φ (deg) | λ _{0.1} (nm) | |
| VEMD2523SLX01 | 10 | ± 35 | 350 to 1120 | |

Note

· Test conditions see table "Basic Characteristics"

| ORDERING INFORMATION | | | | | |
|----------------------|---------------|------------------------------|--------------|--|--|
| ORDERING CODE | PACKAGING | REMARKS | PACKAGE FORM | | |
| VEMD2523SLX01 | Tape and reel | MOQ: 3000 pcs, 3000 pcs/reel | Side view | | |

Note

• MOQ: minimum order quantity

| ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) | | | | |
|--|-----------------------------------|-------------------|---------------|------|
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| Reverse voltage | | V _R | 60 | V |
| Power dissipation | T _{amb} ≤ 25 °C | P _V | 215 | mW |
| Junction temperature | | Tj | 100 | °C |
| Operating temperature range | | T _{amb} | - 40 to + 100 | °C |
| Storage temperature range | | T _{stg} | - 40 to + 100 | °C |
| Soldering temperature | Acc. reflow solder profile fig. 7 | T _{sd} | 260 | °C |
| Thermal resistance junction/ambient | Acc. J-STD-051 | R _{thJA} | 250 | K/W |

| BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | |
|---|--|-------------------|------|-------------|------|------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Forward voltage | I _F = 50 mA | V_{F} | | 1 | | V |
| Breakdown voltage | $I_R = 100 \mu A, E = 0$ | V _(BR) | 32 | | | V |
| Reverse dark current | V _R = 10 V, E = 0 | I _{ro} | | 1 | 10 | nA |
| Diode capacitance | $V_R = 0 V, f = 1 MHz, E = 0$ | C_D | | 4 | | pF |
| | $V_R = 5 V, f = 1 MHz, E = 0$ | C_D | | 1.3 | | pF |
| Open circuit voltage | $E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$ | Vo | | 350 | | mV |
| Temperature coefficient of Vo | $E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$ | TK_Vo | | - 2.6 | | mV/K |
| Short circuit current | $E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$ | I _k | | 10 | | μA |
| Temperature coefficient of I _k | $E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$ | TK _{lk} | | 0.1 | | %/K |
| Reverse light current | $E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$, $V_R = 5 \text{ V}$ | I _{ra} | 7 | 10 | 14 | μA |
| Angle of half sensitivity | | φ | | ± 35 | | deg |
| Wavelength of peak sensitivity | | λ_{p} | | 900 | | nm |
| Range of spectral bandwidth | | λ _{0.1} | | 350 to 1120 | | nm |
| Rise time | $V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$ | t _r | | 100 | | ns |
| Fall time | $V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$ | t _f | | 100 | | ns |

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

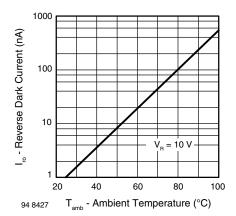
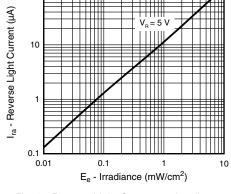


Fig. 1 - Reverse Dark Current vs. Ambient Temperature



100

Fig. 3 - Reverse Light Current vs. Irradiance

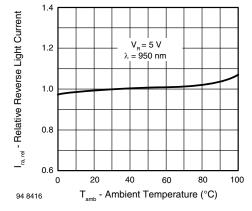


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

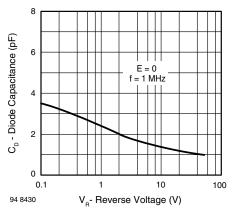


Fig. 4 - Diode Capacitance vs. Reverse Voltage

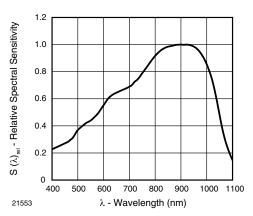


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

REFLOW SOLDER PROFILE

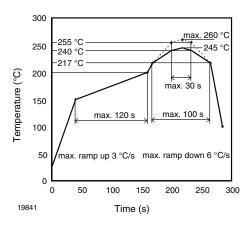


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020D

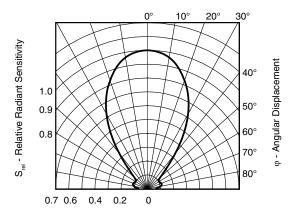


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

Conditions: T_{amb} < 30 °C, RH < 60 %

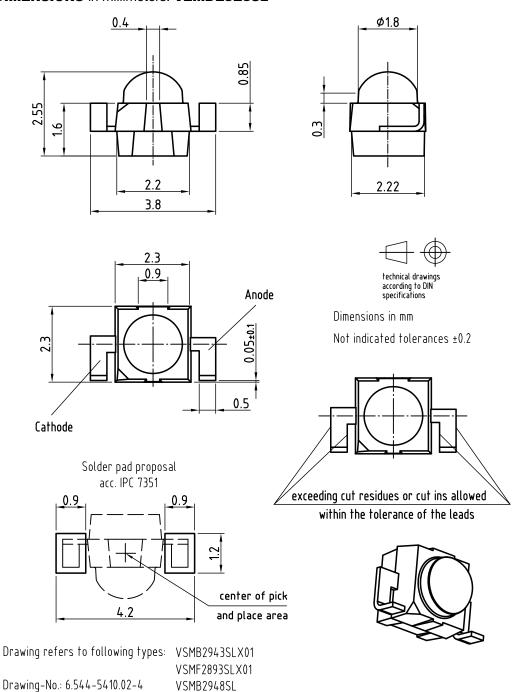
Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

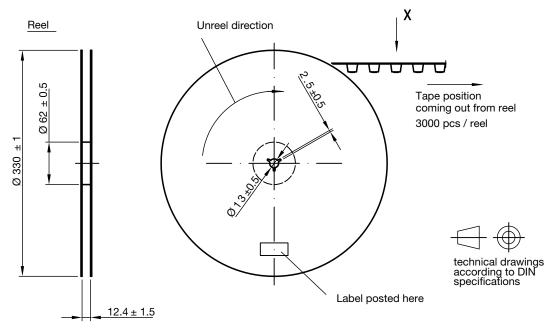


PACKAGE DIMENSIONS in millimeters: VEMD2523SL

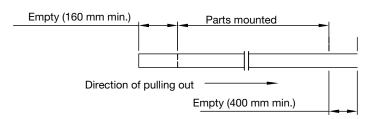




TAPING AND REEL DIMENSIONS in millimeters: VEMD2523SL

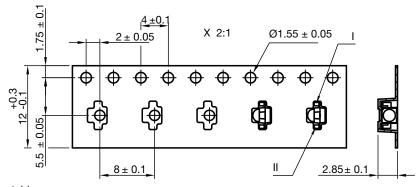


Leader and tailer tape:



Terminal position in tape

| Lead I | Lead II | |
|-----------|---------|--|
| | A | |
| 0-411- | | |
| Cathode | Anode | |
| | | |
| | | |
| 0-114 | Emitter | |
| Collector | | |
| Anode | Cathode | |
| | Cathode | |



Drawing refers to following types: see table

Reel dimensions and tape

Drawing-No.: 9.800-5123.01-4

Issue: 2; 19.02.13



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