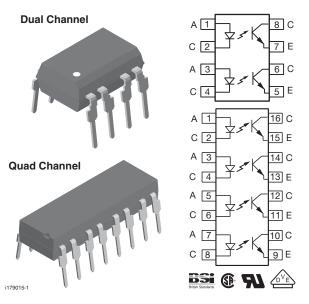
### Vishay Semiconductors

## Optocoupler, Phototransistor Output, (Dual, Quad Channel), 110 °C Rated



### DESCRIPTION

The ILD1615, ILQ1615 are multi-channel 110 °C rated phototransistor optocouplers that use GaAs IRLED emiters and high gain NPN phototransistors. These devices are constructed using over/under leadframe optical coupling and double molded insulation technology resulting a withstand test voltage of 7500 V<sub>AC PEAK</sub> and a working voltage of 1700 V<sub>RMS</sub>.

The binned min./max. and linear CTR characteristics make these devices well suited for DC or AC voltage detection. Eliminating the phototransistor base connection provides added electrical noise immunity from the transients found in many industrial control environments.

Because of guaranteed maximum non-saturated and saturated switching characteristics, the ILD1615, ILQ1615 can be used in medium speed data I/O and control systems. The binned min./max. CTR specification allow easy worst case interface calculations for both level detection and switching applications. Interfacing with a CMOS logic is enhanced by the guaranteed CTR at  $I_F = 1.0$  mA.

### **FEATURES**

- Operating temperature from - 55 °C to + 110 °C
- · Identical channel to channel footprint
- Dual and quad packages feature:
  - Reduced board space
  - Lower pin and parts count
  - Better channel to channel CTR match
  - Improved common mode rejection
- Isolation test voltage, 5300 V<sub>RMS</sub>
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending

ORDERING INFORMATION					
	1 6 1 5	- 4	DIP-#		
	T NUMBER ual) or Q (Quad)	CTR BIN	7.62 mm		
AGENCY CERTIFIED/PACKAGE	DUAL CHANNEL	QUAD CHANNEL			
AGENCT CERTIFIED/PACKAGE		CTR (%)	CTR (%)		
UL, CSA, BSI	160 to 320		160 to 320		
DIP-8	ILD1615-4		-		
DIP-16	-	ILQ1615-4			

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PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT			· ·		
Peak reverse voltage		V <sub>R</sub>	6.0	V	
Forward current		١ <sub>F</sub>	60	mA	
Surge current		I <sub>FSM</sub>	1.5	А	
Power dissipation		P <sub>diss</sub>	100	mW	
Derate linearly from 25 °C			1.0	mW/°C	
OUTPUT					
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V	
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7.0	V	
Collector current		I <sub>C</sub>	50	mA	
	t < 1.0 ms	Ι <sub>C</sub>	100	mA	
Power dissipation		P <sub>diss</sub>	150	mW	
Derate linearly from 25 °C			1.5	mW/°C	
COUPLER					
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C	
Operating temperature		T <sub>amb</sub>	- 55 to + 110	°C	
Soldering temperature <sup>(1)</sup>	2.0 mm distance from case bottom	T <sub>sld</sub>	260	°C	
Package power dissipation ILD1615			400	mW	
Derate linearly from 25 °C			5.33	mW/°C	
Package power dissipation ILQ1615			500	mW	
Derate linearly from 25 °C			6.67	mW/°C	
Isolation test voltage	t = 1.0 s	V <sub>ISO</sub>	5300	V <sub>RMS</sub>	
Creepage distance			≥ 7.0	mm	
Clearance distance			≥ 7.0	mm	
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω	
ISUIALIUTI TESISLATICE	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω	

### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to wave profile for soldering conditions for through hole devices.

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT				•		
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	1.0	1.15	1.3	V
Breakdown voltage	I <sub>R</sub> = 10 μA	V <sub>BR</sub>	6.0	30		V
Reverse current	V <sub>R</sub> = 6.0 V	I <sub>R</sub>		0.01	10	μA
Capacitance	V <sub>R</sub> = 0 V, f = 1.0 MHz	Co		25		pF
OUTPUT						
Collector emitter capacitance	V <sub>CE</sub> = 5.0 V, f = 1.0 MHz	C <sub>CE</sub>		6.8		pF
Collector emitter leakage current	V <sub>CE</sub> = 10 V	I <sub>CEO</sub>		5.0	100	nA
Collector emitter breakdown voltage	I <sub>CE</sub> = 0.5 mA	BV <sub>CEO</sub>	70			V
Emitter collector breakdown voltage	I <sub>E</sub> = 0.1 mA	BV <sub>ECO</sub>	7.0			V
PACKAGE TRANSFER CHARACTER	STICS					
Channel/channel CTR match	$I_F = 10 \text{ mA}, V_{CE} = 5.0 \text{ V}$	CTRX/CTRY	1 to 1		2 to 1	
COUPLER						
Capacitance (input to output)	V <sub>IO</sub> = 0 V, f = 1.0 MHz	C <sub>IO</sub>		0.8		pF
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{A} = 25 ^{\circ}\text{C}$	R <sub>S</sub>	10 <sup>12</sup>	10 <sup>14</sup>		Ω
Channel to channel isolation			500			V <sub>AC</sub>

#### Note

Minimum and maximum values are tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

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<b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Current transfer ratio (collector emitter saturated)	$I_F = 1.0 \text{ mA}, V_{CE} = 0.4 \text{ V}$	ILD1615-4	CTR <sub>CEsat</sub>		100		%	
		ILQ1615-4					70	
Current transfer ratio (collector emitter)		ILD1615-4		160 200	000	000	%	
	l <sub>F</sub> = 10 mA, V <sub>CE</sub> = 5.0 V	ILQ1615-4	CTR <sub>CE</sub>		320	70		
	$I_F = 1.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$	ILD1615-4		56	90		%	
		ILQ1615-4					70	

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED							
Turn-on time	$I_{F} = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, \text{ R}_{L} = 75 \ \Omega, \\ 50 \ \% \text{ of } \text{ V}_{PP}$		t <sub>on</sub>		3.0		μs
Rise time	$I_{F} = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 75 \ \Omega, \\ 50 \ \% \text{ of } V_{PP}$		tr		2.0		μs
Turn-off time	$I_{F} = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 75 \ \Omega, \\ 50 \ \% \text{ of } V_{PP}$		t <sub>off</sub>		2.3		μs
Fall time	$I_{F} = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 75 \ \Omega, \\ 50 \ \% \text{ of } V_{PP}$		t <sub>f</sub>		2.0		μs
Propagation H to L	$I_{F} = 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 75 \ \Omega, \\ 50 \ \% \text{ of } V_{PP}$		t <sub>PHL</sub>		1.1		μs
Propagation L to H	I <sub>F</sub> = 10 mA, V <sub>CC</sub> = 5.0 V, R <sub>L</sub> = 75 Ω, 50 % of V <sub>PP</sub>		t <sub>PLH</sub>		2.5		μs
SATURATED							
Turn-on time	$I_{F} = 5.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, \text{ R}_{L} = 1.0 \text{ k}\Omega, \\ V_{HT} = 1.5 \text{ V}$		t <sub>on</sub>		6.0		μs
Rise time	$I_{F} = 5.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 1.0 \text{ k}\Omega, \\ V_{HT} = 1.5 \text{ V}$		t <sub>r</sub>		4.6		μs
Turn-off time	$I_{F} = 5.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 1.0 \text{ k}\Omega, \\ V_{HT} = 1.5 \text{ V}$		t <sub>off</sub>		25		μs
Fall time	$I_{F} = 5.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 1.0 \text{ k}\Omega, \\ V_{HT} = 1.5 \text{ V}$		t <sub>f</sub>		15		μs
Propagation H to L	$I_{F} = 5.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_{L} = 1.0 \text{ k}\Omega, \\ V_{HT} = 1.5 \text{ V}$		t <sub>PHL</sub>		5.4		μs
Propagation L to H	$I_F = 5.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, \text{ R}_L = 1.0 \text{ k}\Omega, \\ V_{HT} = 1.5 \text{ V}$		t <sub>PLH</sub>		7.4		μs

COMMON MODE TRANSIENT IMMUNITY (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER TEST CONDITION SYMBOL MIN. TYP. MAX. U						UNIT		
Common mode rejection output high	$V_{CM} = 50 V_{P-P}, R_L = 1.0 \text{ k}\Omega, I_F = 0 \text{ mA}$	CM <sub>H</sub>		5000		V/µs		
Common mode rejection output low	$V_{CM}$ = 50 $V_{P-P}$ , $R_L$ = 1.0 k $\Omega$ , $I_F$ = 10 mA	CML		5000		V/µs		
Common mode coupling capacitance		C <sub>CM</sub>		0.01		pF		

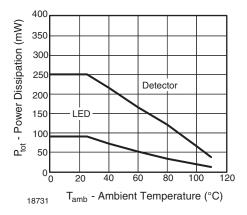
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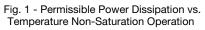


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TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)





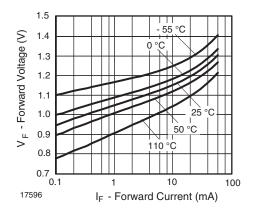


Fig. 2 - Forward Voltage vs. Forward Current

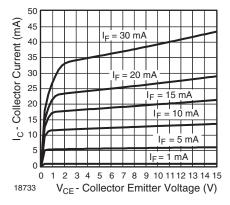


Fig. 3 - Collector Current vs. Collector Emitter Voltage

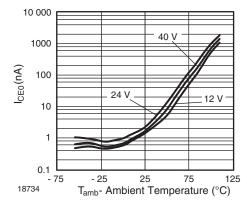


Fig. 4 - Collector to Emitter Dark Current vs. Ambient Temperature

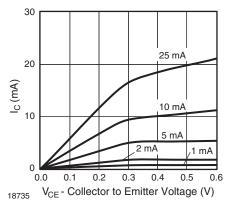
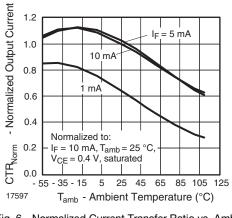
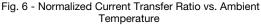


Fig. 5 - Normalized Current vs. Collector Emitter Saturation Voltage





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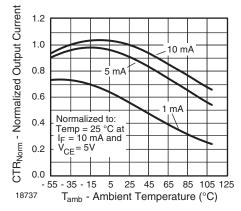


Fig. 7 - Normalized CTR vs. Temperature

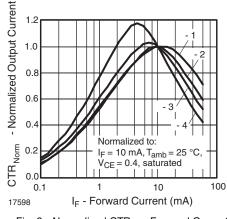


Fig. 8 - Normalized CTR vs. Forward Current

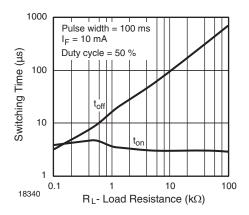


Fig. 10 - Forward Resistance vs. Forward Current

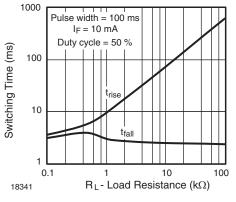


Fig. 11 - Forward Resistance vs. Forward Current

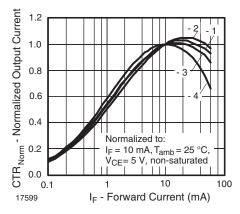


Fig. 9 - Normalized CTR vs. Forward Current

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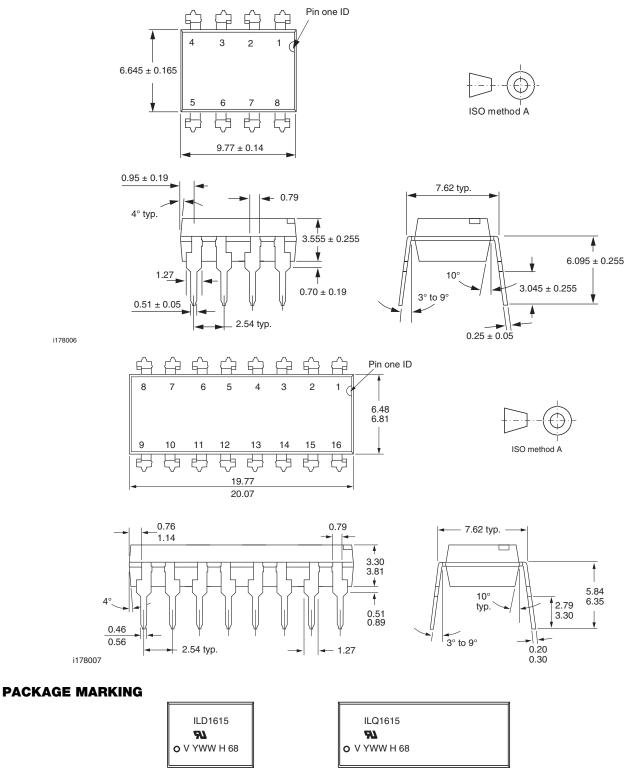
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### **PACKAGE DIMENSIONS** in millimeters



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