



P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 20	0.057 at V _{GS} = - 4.5 V	- 3.3		
	0.076 at V _{GS} = - 2.5 V	- 2.8		
	0.110 at V _{GS} = - 1.8 V	- 2.3		

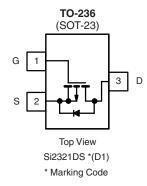
FEATURES

- Halogen-free Option Available
- TrenchFET® Power MOSFETS



APPLICATIONS

- Load Switch
- PA Switch



Ordering Information: Si2321DS-T1-E3 (Lead (Pb)-free) Si2321DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T	_A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		٧
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current /T 150 °C\3	T _A = 25 °C	- I _D	- 3.3	- 2.9	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 2.6	- 2.3	
Pulsed Drain Current		I _{DM}	- 12		A
Continuous Source Current (Diode Conduction) ^a		I _S	- 0.74	- 0.59	
D D: : :: 3	T _A = 25 °C	- P _D	0.89	0.71	W
Power Dissipation ^a	T _A = 70 °C		0.57	0.45	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manipular landing to Austriant	t ≤ 5 s	- R _{thJA}	115	140	°C/W
Maximum Junction-to-Ambient ^a	Steady State		140	175	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75	

Notes:

a. Surface Mounted on FR4 board.

 $b.\ t \leq 5\ s.$

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm

Si2321DS

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SPECIFICATIONS $T_J = 25$	°C, unless	otherwise noted					
-			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V, } I_{D} = -10 \mu\text{A}$	- 20			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.40		- 0.90	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zava Cata Valtaga Dvain Current	1	V _{DS} = 16 V, V _{GS} = 0 V			- 1	- μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 5 V, $V_{GS} =$ - 4.5 V	- 6			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -3.3 \text{ A}$		0.044	0.057		
Drain-Source On-Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -2.8 \text{ A}$		0.061	0.076	Ω	
		$V_{GS} = -1.8 \text{ V}, I_D = -2.3 \text{ A}$		0.084	0.110		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -5 \text{ V}, I_D = -3.3 \text{ A}$		3		S	
Diode Forward Voltage	V _{SD}	I _S = - 1.6 A, V _{GS} = 0 V			- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg	V 0VV 45V		8	13	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}$ $I_{D} \cong -3.3 \text{ A}$		1.2			
Gate-Drain Charge	Q _{gd}	ID = - 0.0 A		2.2		1	
Input Capacitance	C _{iss}			715			
Output Capacitance	C _{oss}	$V_{DS} = -6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		170		pF	
Reverse Transfer Capacitance	C _{rss}			120		1	
Switching ^b				•	•		
Turn-On Time	t _{d(on)}	V 0VP 00		15	25		
ium-on time	t _r	$V_{DD} = -6 \text{ V}, R_L = 6 \Omega$ $I_D \cong -1.0 \text{ A}, V_{GEN} = -4.5 \text{ V}$		35	55	- ns	
Turn-Off Time	t _{d(off)}	$R_G = 6 \Omega$		60	90		
Tutti-Oil Tillie	t _f	- · · · · · · · · · · · · · · · · · · ·		40	60		

Notes:

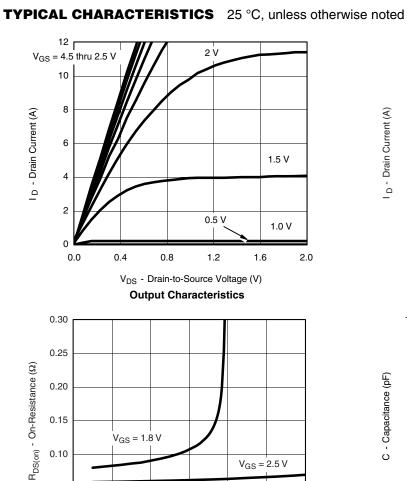
- a. For DESIGN AID ONLY, not subject to production testing.
- b. Pulse test: PW \leq 300 $\mu s,$ duty cycle \leq 2 %.
- c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.









I_D - Drain Current (A) On-Resistance vs. Drain Current

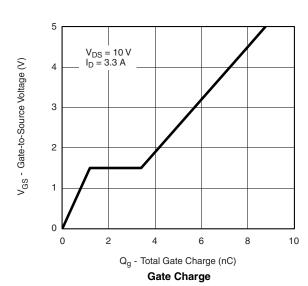
6

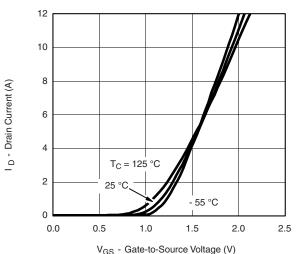
8

 $V_{GS} = 4.5 \text{ V}$

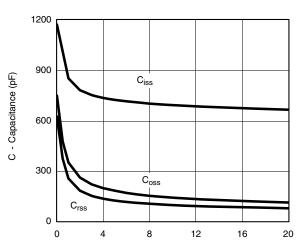
10

12

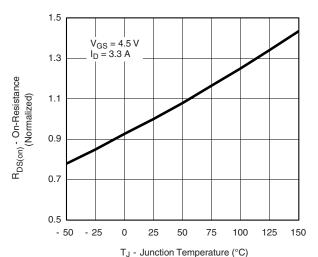




Transfer Characteristics



V_{DS} - Drain-to-Source Voltage (V) Capacitance



Normalized On-Resistance vs. Junction Temperature

0.05

0.00

0

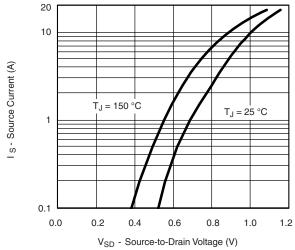
2

4

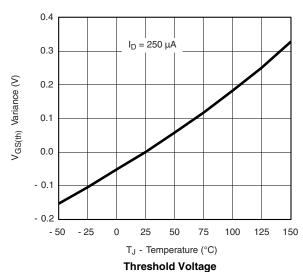
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

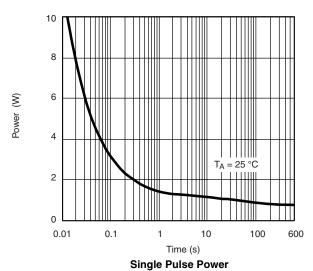


Source-Drain Diode Forward Voltage

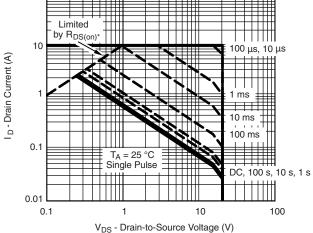


0.5 0.4 $R_{DS(on)}$ - On-Resistance (Ω) 0.3 0.2 $I_D = 3.3 A$ 0.1 0.0 0 2 3 4 5 6 7 8

 $\label{eq:VGS} V_{GS} \text{ - Gate-to-Source Voltage (V)}$ On-Resistance vs. Gate-to-Source Voltage



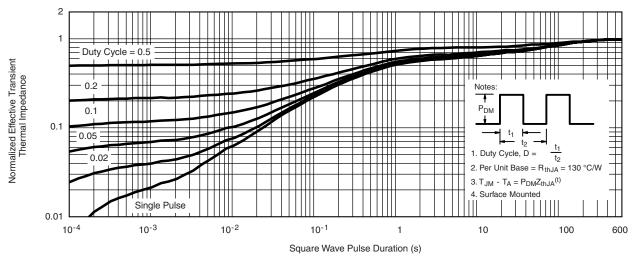
100



 v_{DS} - Drain-to-source voltage (v) * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72210.



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