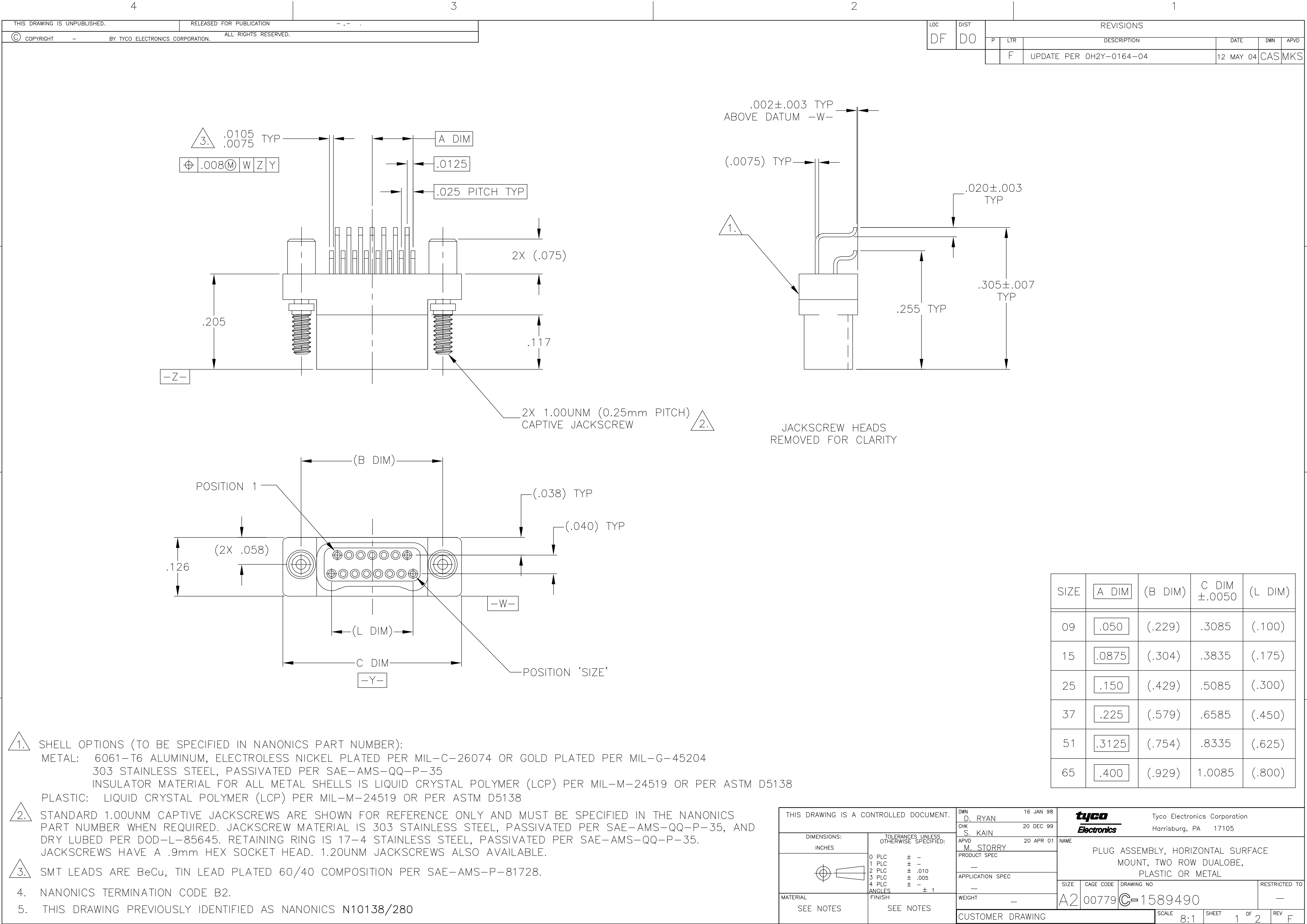
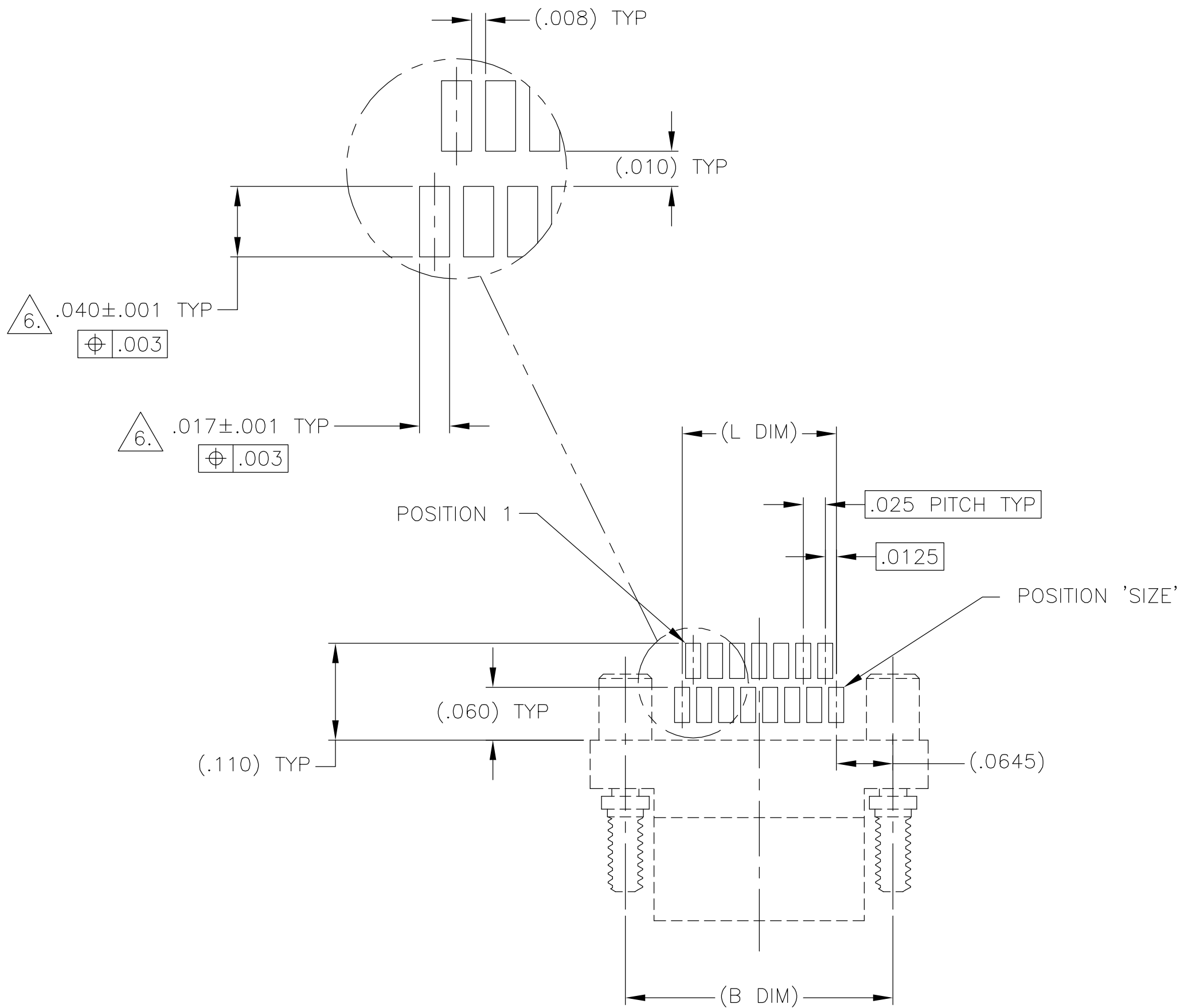
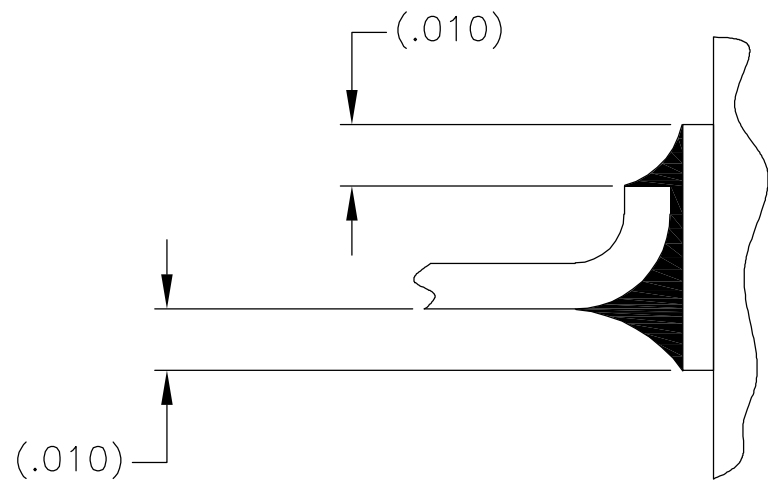




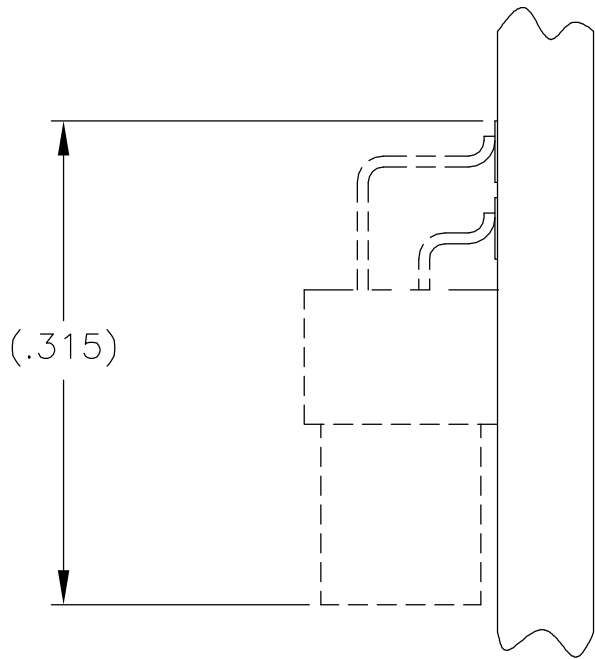
LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
DF	D0		F	UPDATE PER 0H2Y-0164-04	12 MAY 04	CAS	MKS



TYPICAL PCB LAYOUT



TYPICAL FOOT PLACEMENT
ON SOLDER PAD



6. POSITIONAL TOLERANCES FOR BASIC DIMENSIONED FEATURES ARE RELATIVE TO FIDUCIALS OR SOME SIMILAR DATUM REFERENCE DEFINED BY THE PCB DESIGNER.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN D. RYAN 16 JAN 98	tyco Electronics Tyco Electronics Corporation Harrisburg, PA 17105	
DIMENSIONS: INCHES		CHK S. KAIN 10 DEC 99		
	TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD M. STORRY 20 APR 01	
	0 PLC ± -		NAME	
	1 PLC ± -		PRODUCT SPEC	
2 PLC ± -		APPLICATION SPEC		
3 PLC ± -		—		
4 PLC ± -		—		
ANGLES ± -		—		
FINISH		—		
MATERIAL		—		
		WEIGHT		
		—		
		CUSTOMER DRAWING		
		SCALE 8:1		
		SHEET 2 OF 2		
		REV F		

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