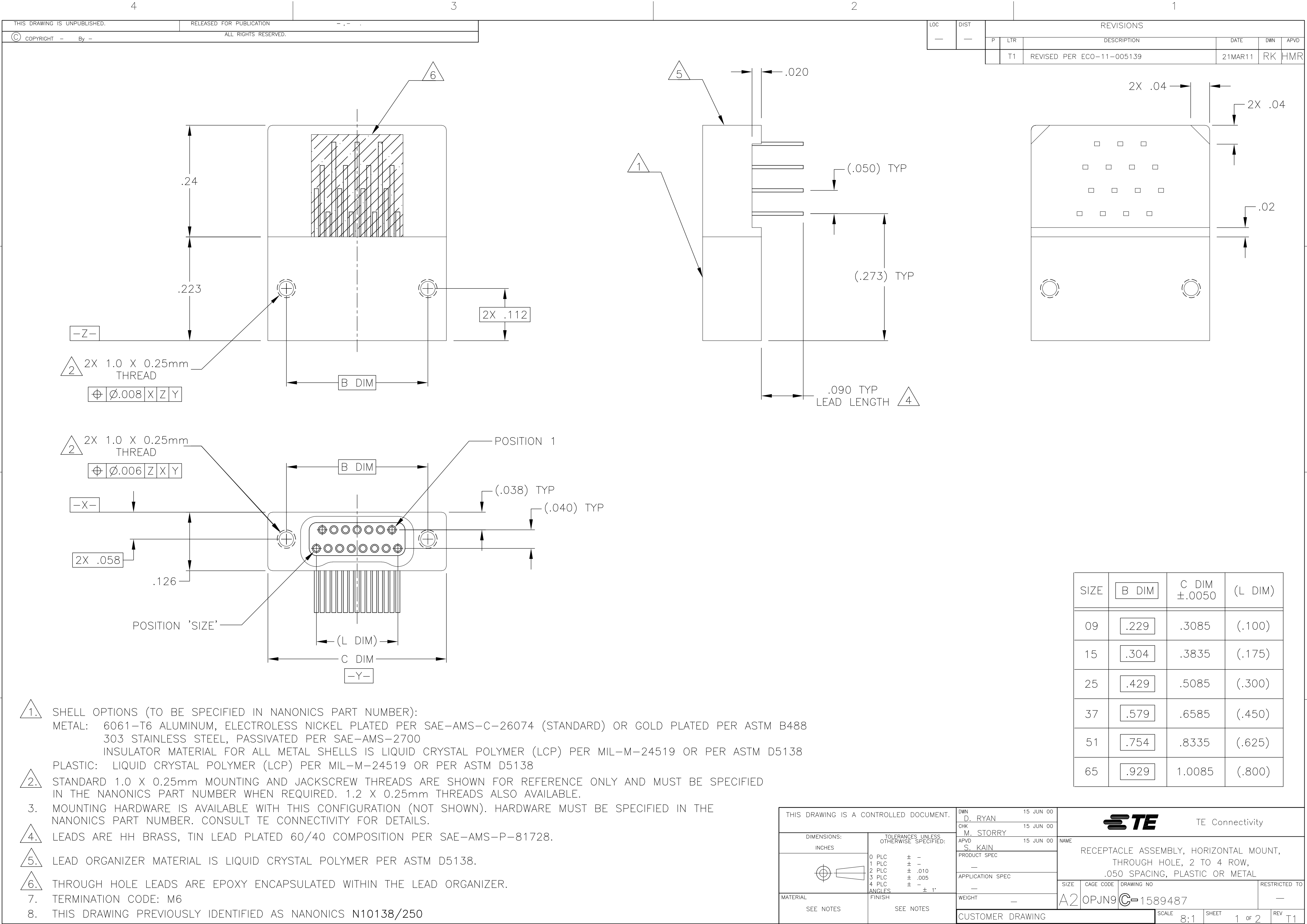




LOC		DIST		REVISIONS			
P	LTR	DESCRIPTION		DATE	DWN	APVD	
	T1	REVISED PER ECO-11-005139		21MAR11	RK	HMR	

SIZE	B DIM	C DIM ±.0050	(L DIM)
09	.229	.3085	(.100)
15	.304	.3835	(.175)
25	.429	.5085	(.300)
37	.579	.6585	(.450)
51	.754	.8335	(.625)
65	.929	1.0085	(.800)

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS:
INCHES

MATERIAL

SEE NOTES

TOLERANCES UNLESS OTHERWISE SPECIFIED:

0 PLC ± -

1 PLC ± -

2 PLC ± .010

3 PLC ± .005

4 PLC ± -

ANGLES ± 1°

FINISH

SEE NOTES

DWN
D. RYAN
15 JUN 00

CHK
M. STORRY
15 JUN 00

APVD
S. KAIN
15 JUN 00

PRODUCT SPEC
—

APPLICATION SPEC
—

WEIGHT
—

CUSTOMER DRAWING

TE Connectivity

NAME
RECEPTACLE ASSEMBLY, HORIZONTAL MOUNT,
THROUGH HOLE, 2 TO 4 ROW,
.050 SPACING, PLASTIC OR METAL

SIZE
A2

CAGE CODE
OPJN9

DRAWING NO
C=1589487

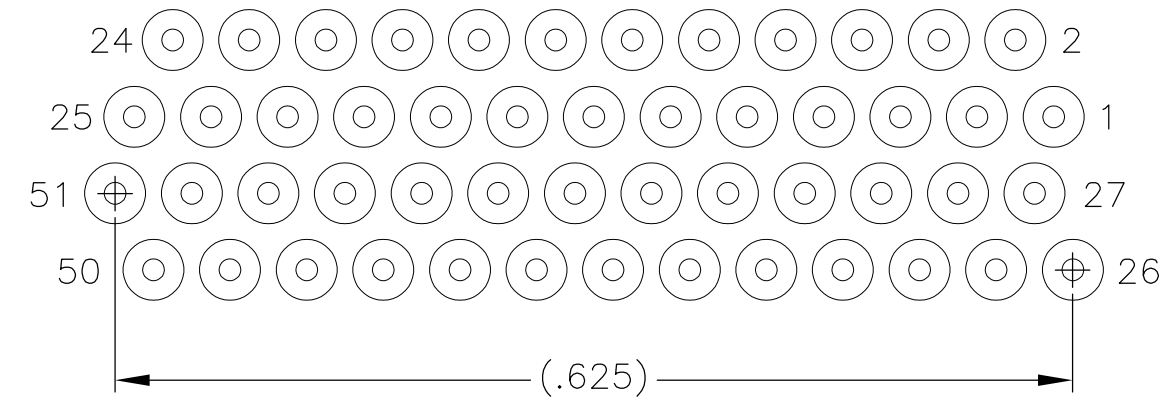
RESTRICTED TO
—

SCALE
8:1

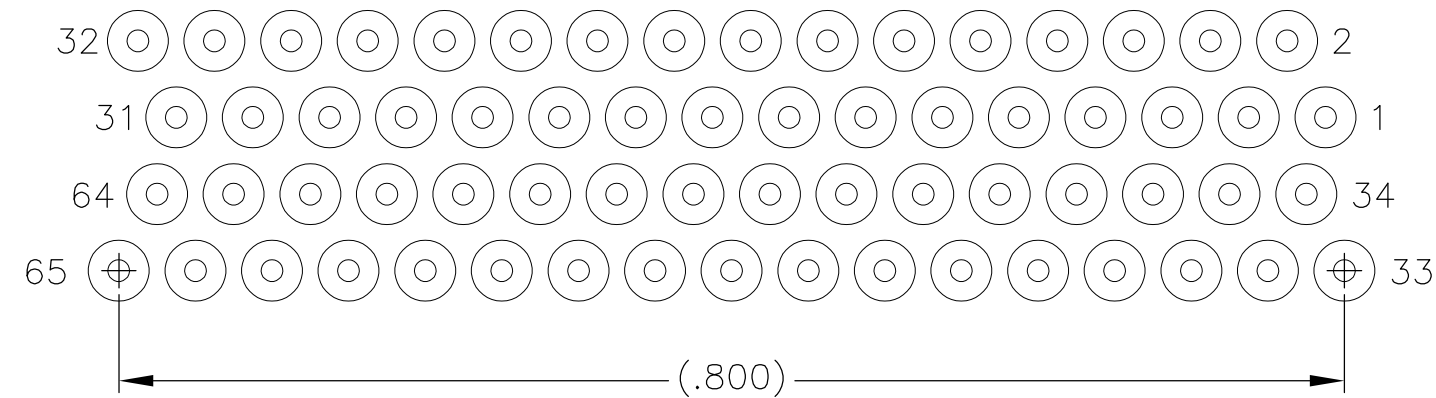
SHEET
1 of 2

REV
T1

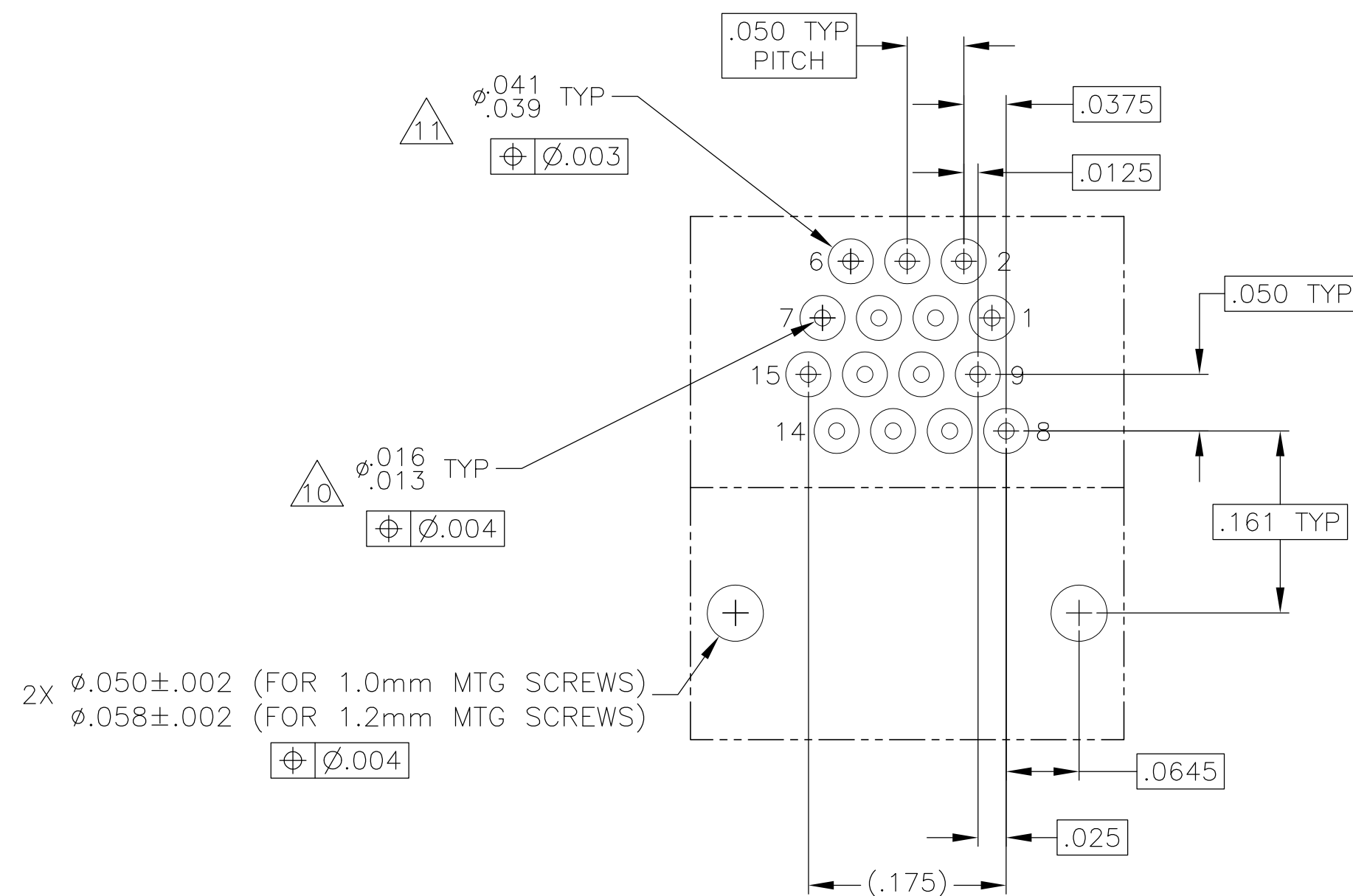
LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
			—	SEE SHEET 1	—	—	—



51 POSITION

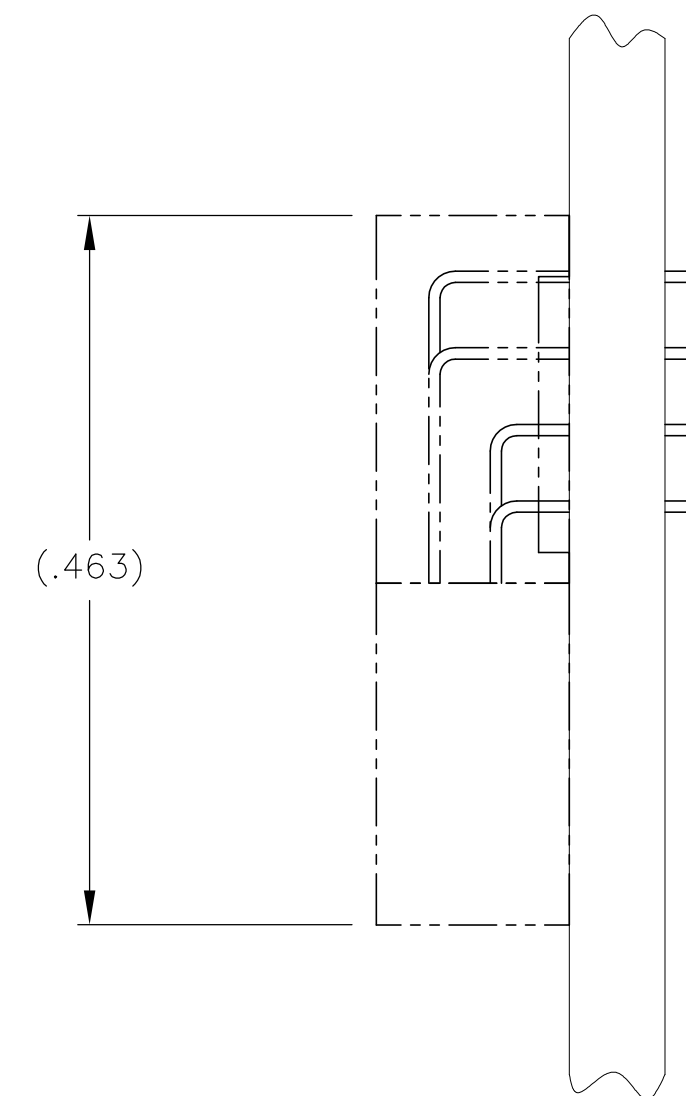


65 POSITION

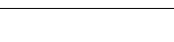


TYPICAL PCB LAYOUT  9.

SIZE 15 SHOWN FOR REFERENCE



9. POSITIONAL TOLERANCES FOR BASIC DIMENSIONED FEATURES ARE RELATIVE TO FIDUCIALS OR SOME SIMILAR DATUM REFERENCES DEFINED BY PCB DESIGNER.
10. PLATED THROUGH HOLES
11. SOLDER PADS
12. ALL THROUGH HOLE LAYOUTS ARE AS VIEWED FROM TOP OF PCB.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN D. RYAN 15 JUN 00		 TE Connectivity				
		CHK M. STORRY 15 JUN 00						
DIMENSIONS: INCHES		TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME RECEPTACLE ASSEMBLY, HORIZONTAL MOUNT, THROUGH HOLE, 2 TO 4 ROW, .050 SPACING, PLASTIC OR METAL				
		APVD S. KAIN 15 JUN 00						
		PRODUCT SPEC —						
		APPLICATION SPEC —						
MATERIAL		WEIGHT —		SIZE A2	CAGE CODE 0PJN9	DRAWING NO C-1589487	RESTRICTED TO —	
SEE NOTES		SEE NOTES		CUSTOMER DRAWING		SCALE 8:1	SHEET 2 of 2	REV T1

Mouser Electronics

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