

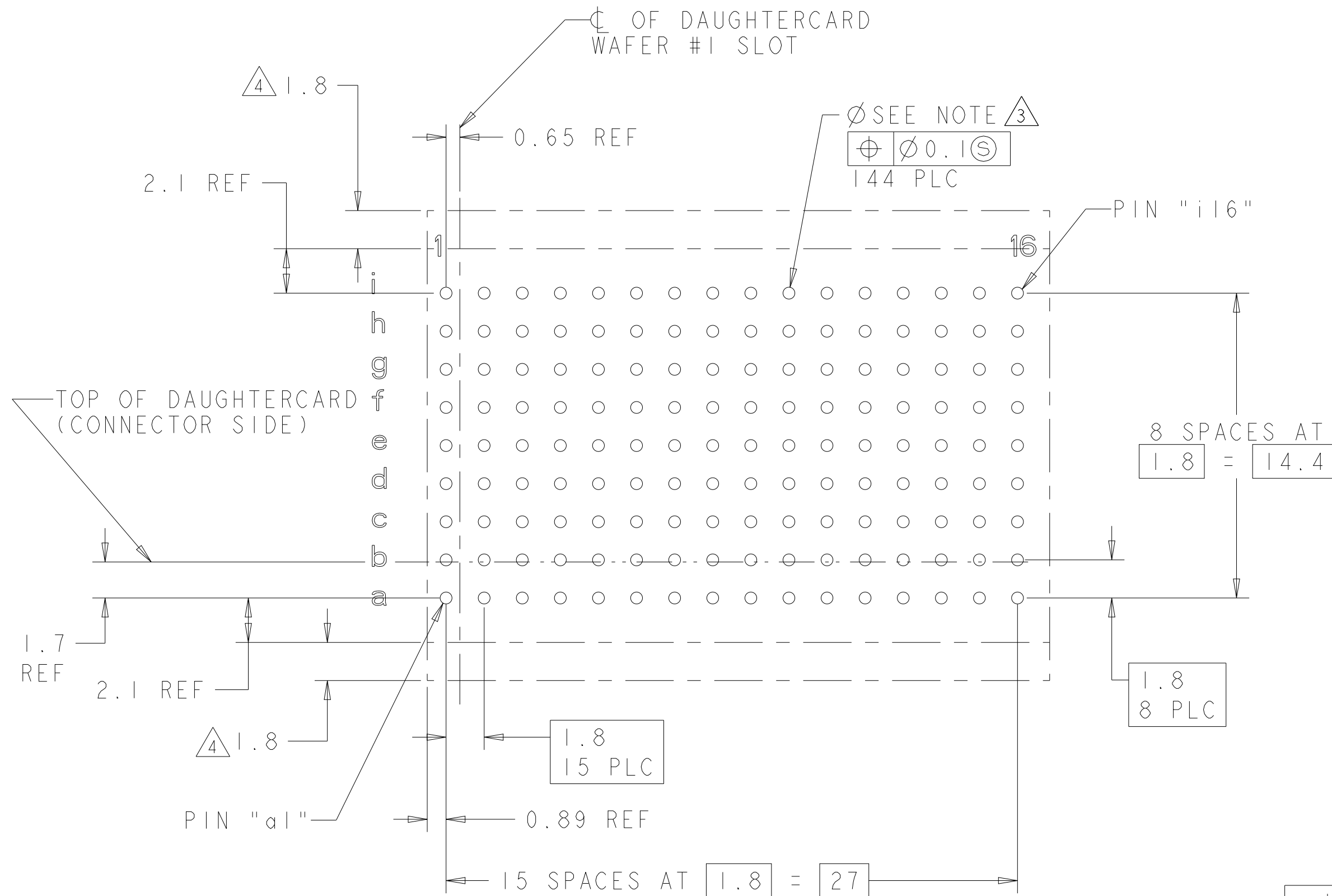
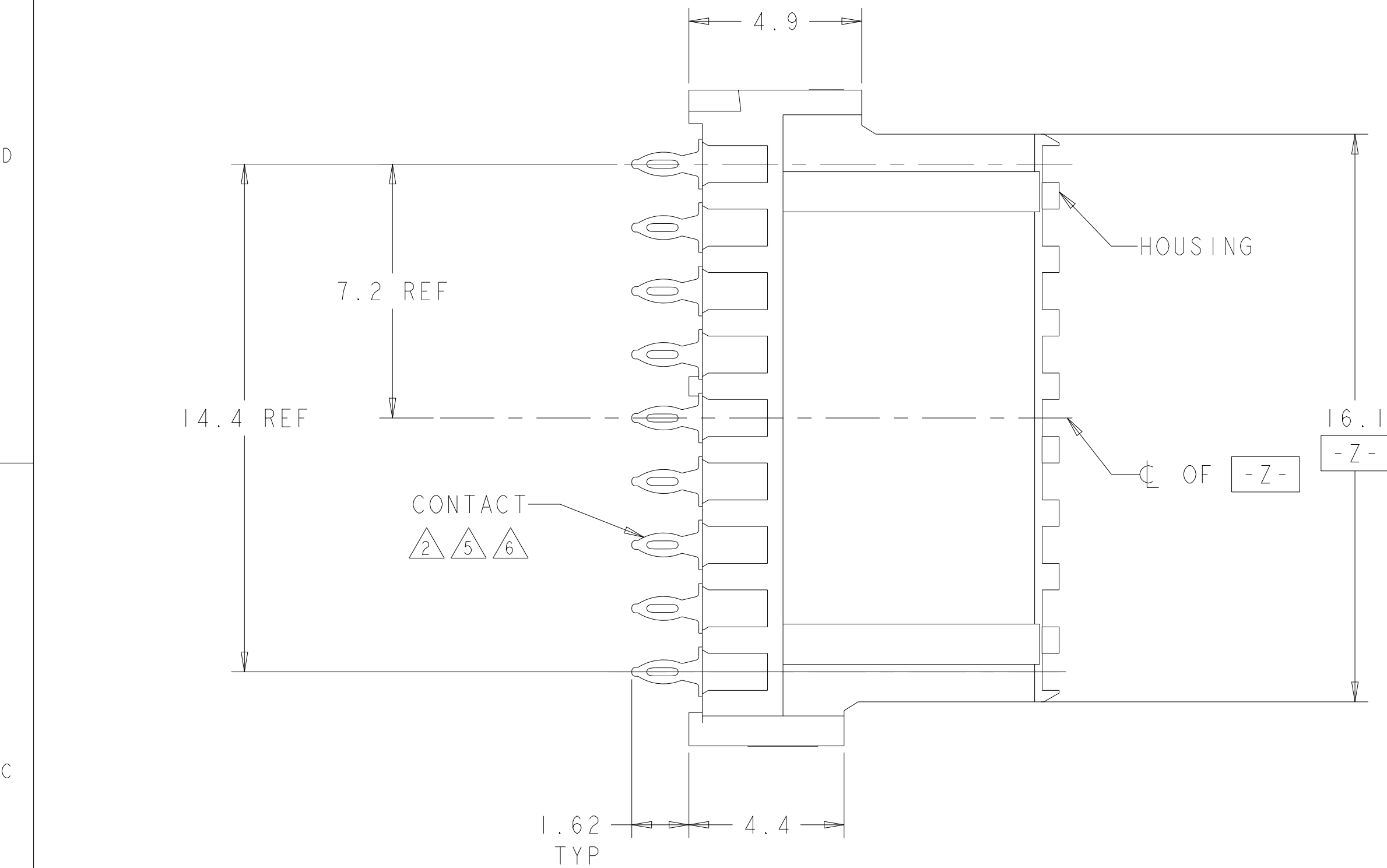
LOC		DIST	REVISIONS				
P	LYR	DESCRIPTION			DATE	DWN	APVD
B		RELEASED PER ECO-13-004502			12MAR2013	KM	DH
C		REVISED PER ECO-13-008265			20MAY2013	DB	DH
D		REVISED PER ECN-21-117413			17SEP2021	JAB	DH

D

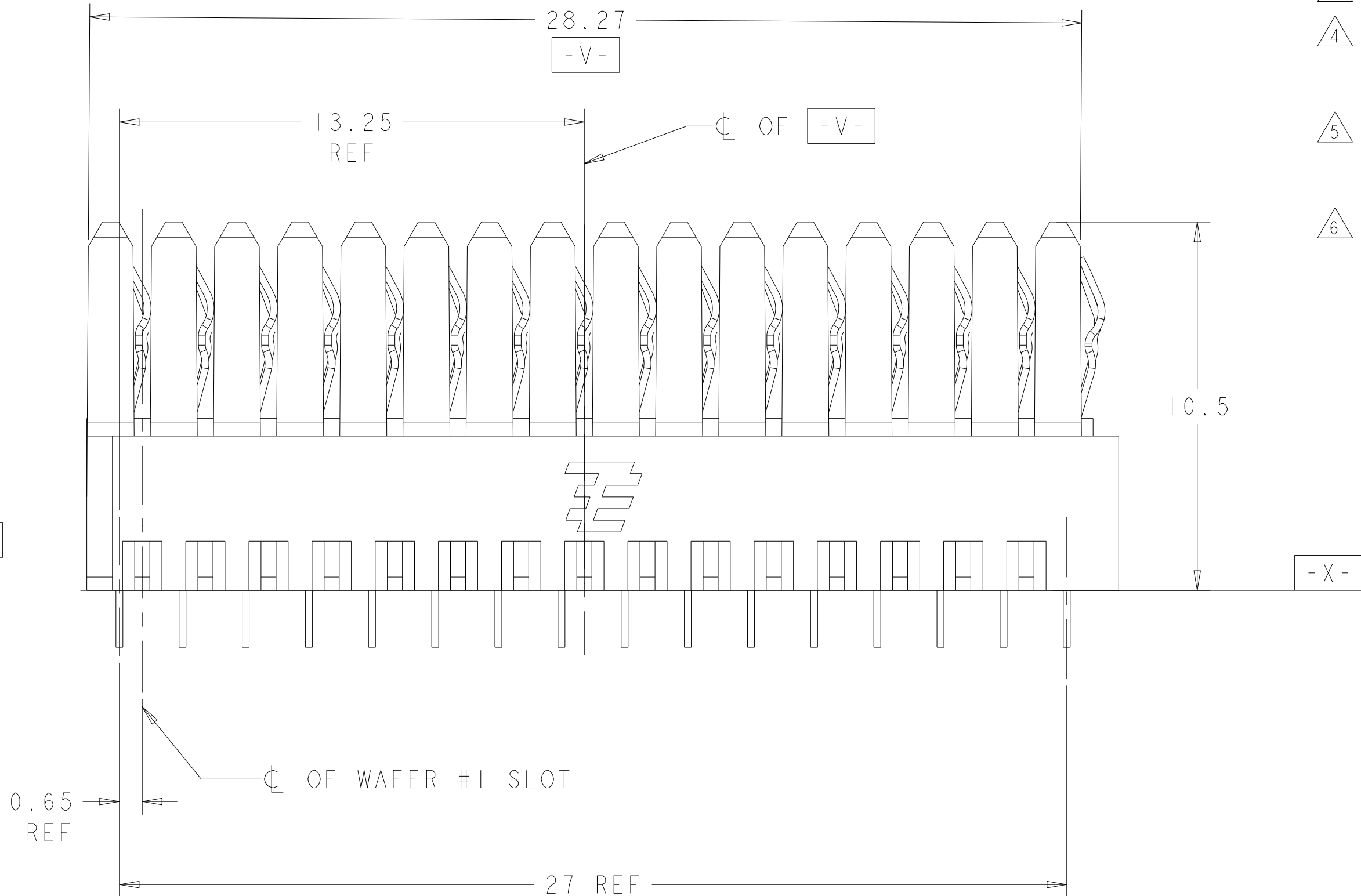
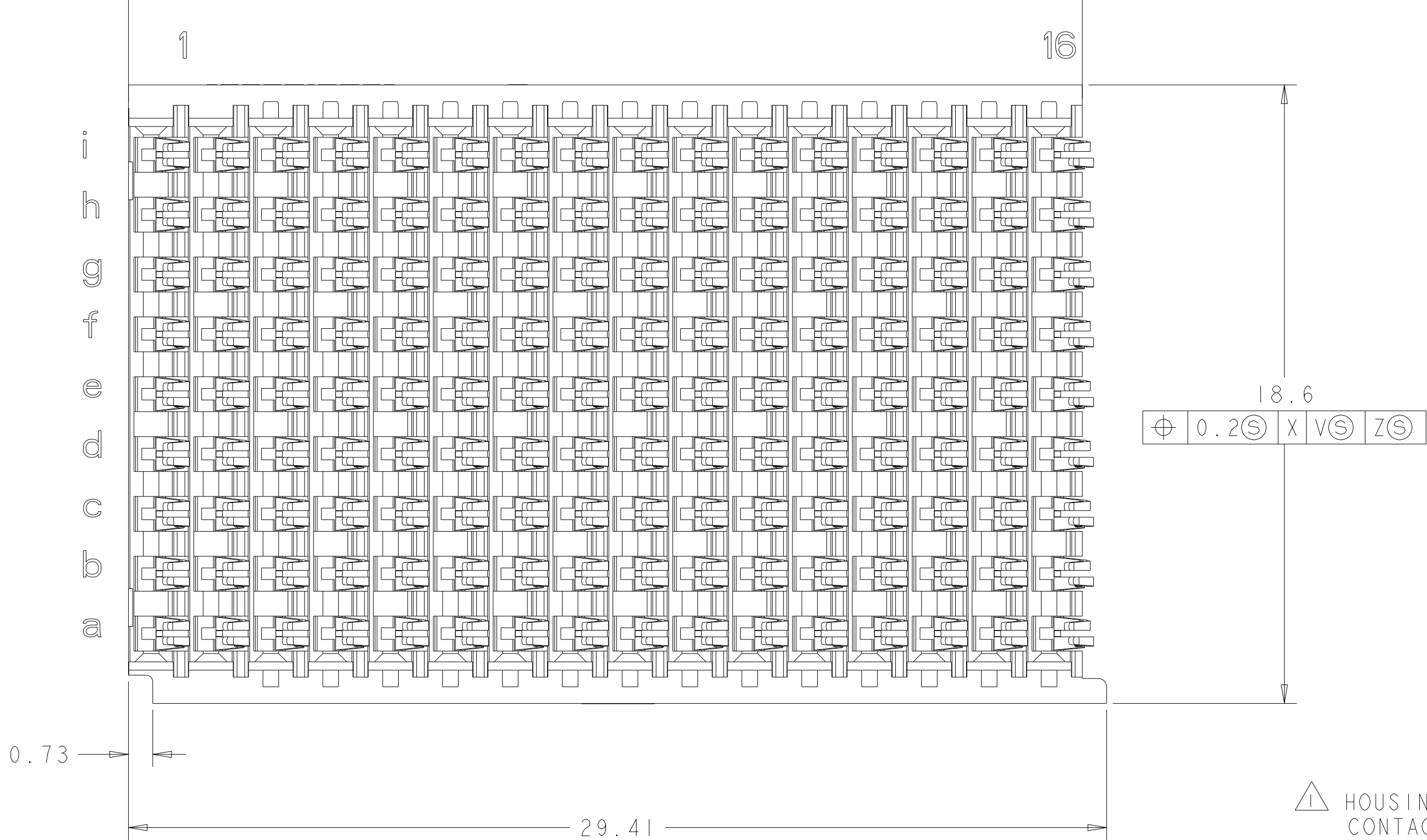
C

B

A



PC BOARD LAYOUT
(CONNECTOR SIDE)
(SEE SHEET 2 FOR RTM)
SCALE 5:1

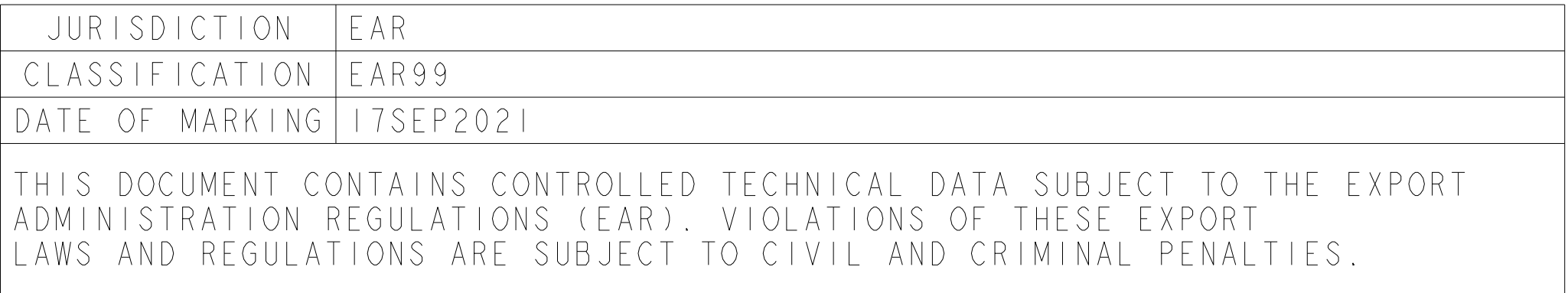
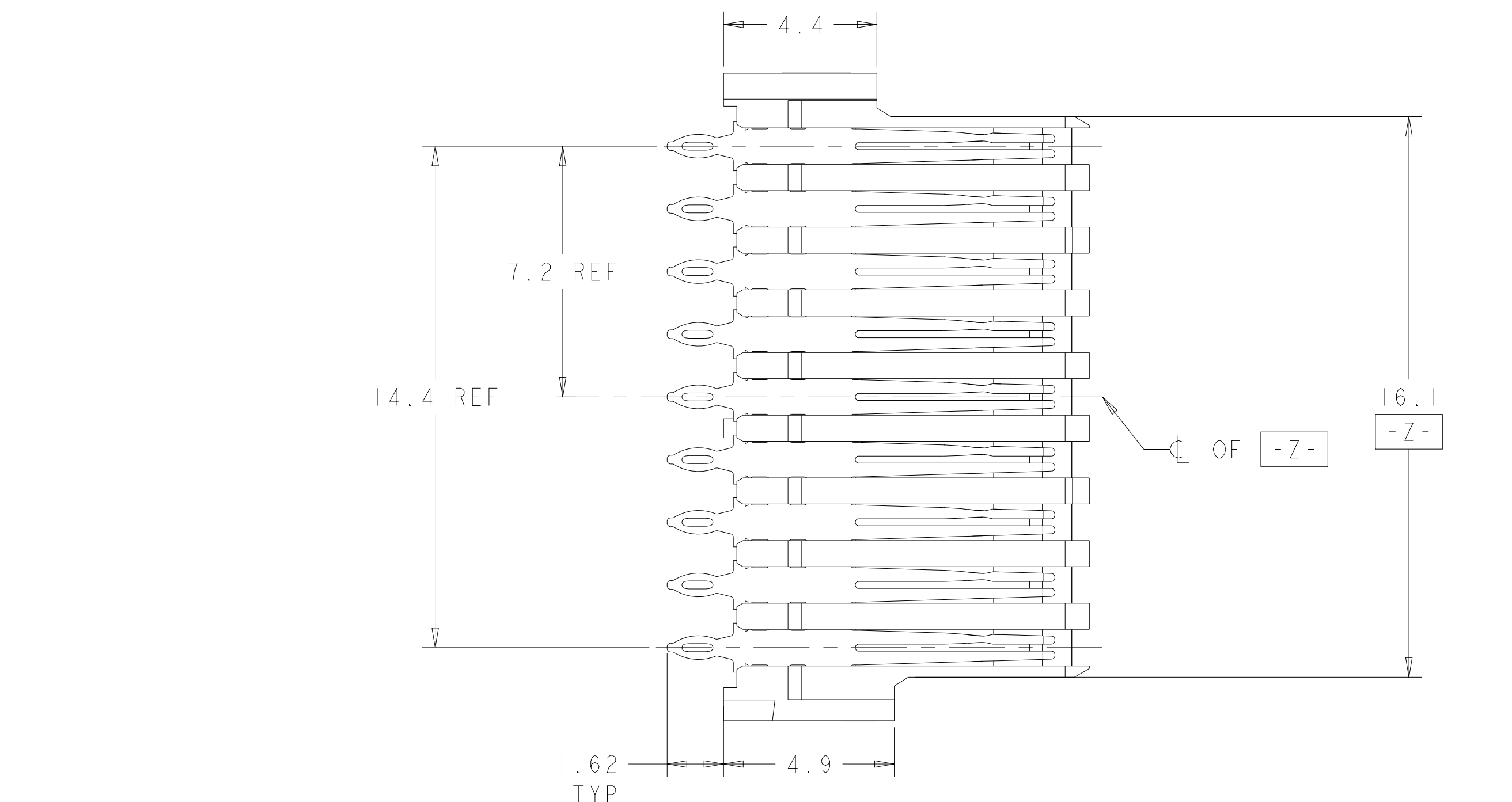


- 1 HOUSING: LCP, UL94V0, COLOR: BLACK. CONTACT: PHOSPHOR BRONZE.
- 2 FINISH: 1.27µm GOLD IN CONTACT AREA 1.27µm MIN. TIN-LEAD (93/7) ON PCB TAIL OVER 2.54µm MIN. NICKEL OVER ALL.
- 3 PLATED THROUGH HOLE PER 114-13056, FIGURE 4
- 4 ADDITIONAL CONNECTOR-SIDE KEEPOUT AREA REQUIRED IF CONNECTOR-SIDE EXTRACTION TOOL IS TO BE USED.
- 5 FINISH: 1.27µm MIN GOLD IN CONTACT AREA, 0.50µm MIN TIN ON PCB TAIL OVER 2.54µm MIN. NICKEL OVER ALL.
- 6 FINISH: 1.27µm GOLD IN CONTACT AREA 1.27µm MIN. TIN-LEAD (60/40) ON PCB TAIL OVER 2.54µm MIN. NICKEL OVER ALL.

JURISDICTION	EAR
CLASSIFICATION	EAR99
DATE OF MARKING	17SEP2021
THIS DOCUMENT CONTAINS CONTROLLED TECHNICAL DATA SUBJECT TO THE EXPORT ADMINISTRATION REGULATIONS (EAR). VIOLATIONS OF THESE EXPORT LAWS AND REGULATIONS ARE SUBJECT TO CIVIL AND CRIMINAL PENALTIES.	

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN K. MILLER CHK K. THACKSTON APVD M. McALONIS	07SEP2011 12SEP2011 10SEP2012	NAME	
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:		PRODUCT SPEC	
0 PLC ±°		1 PLC ±0.5		2 PLC ±0.13	
3 PLC ±°		4 PLC ±°		APPLICATION SPEC	
MATERIAL		114-13056		WEIGHT	
SEE TABLE		6.51g		Customer Drawing	
SIZE		CAGE CODE		DRAWING NO	
A1		00779		C=2102736	
RESTRICTED TO		SCALE		SHEET	
-		2:1		1 OF 2	
REV		D			

Technical drawing of a 16x9 array of microfluidic chips. The drawing shows a grid of 144 chips (16 columns by 9 rows). Dimensions are provided: total width 29.41, total height 18.6, chip width 28.68, and chip height 0.73. The chips are labeled 1 to 16 across the top and a to i down the left side.

4805 (3/11)

Mouser Electronics

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