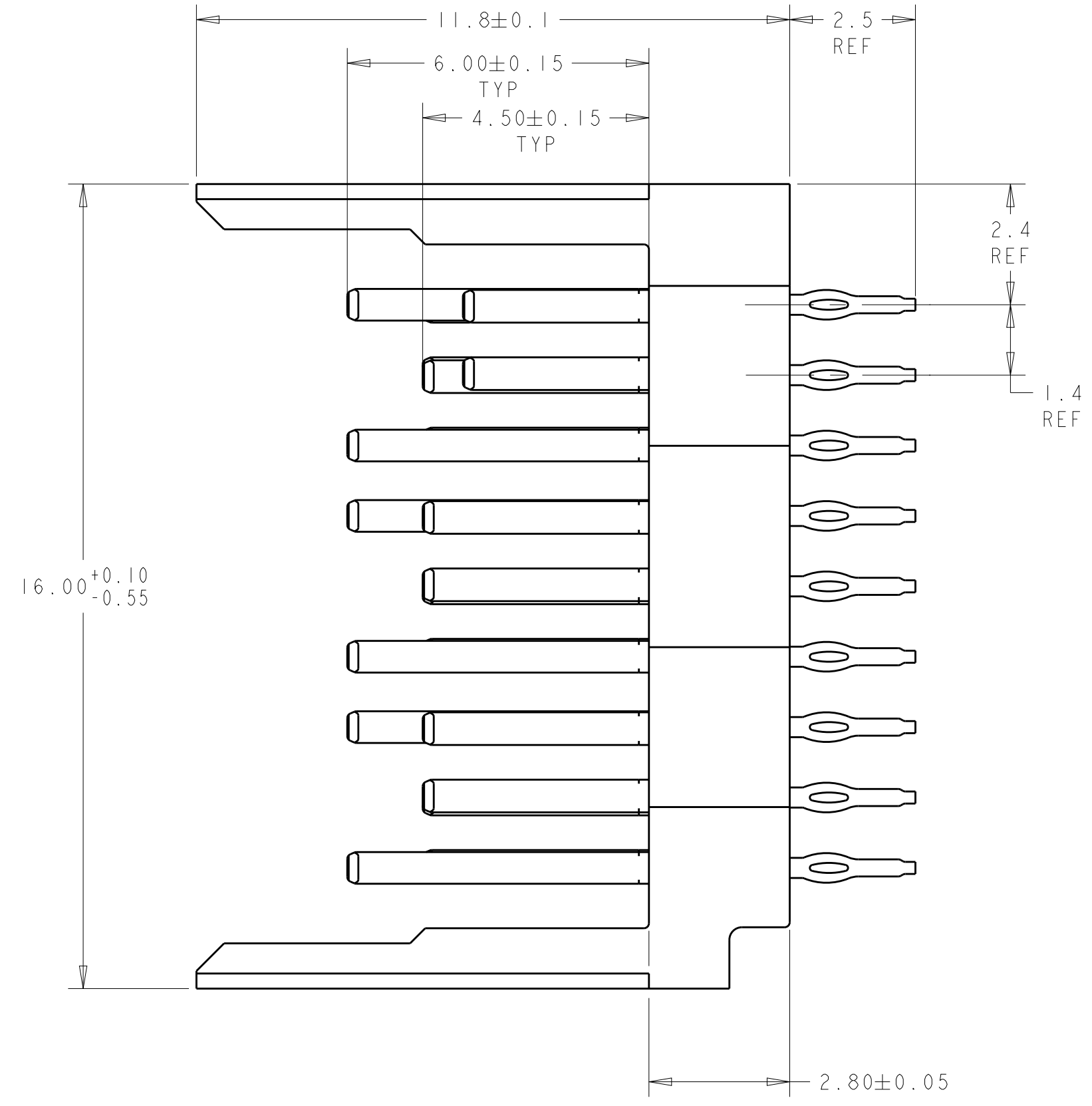
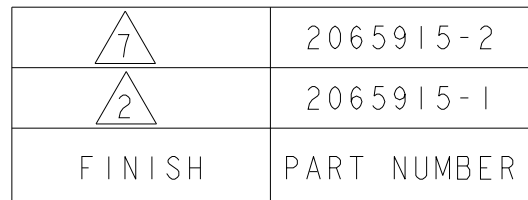




-
- Figure 1 is a technical drawing of a recommended PC board layout backplane, component side shown. The drawing illustrates a grid of holes (rows A through I, columns 1 through 8) used for component placement. Key dimensions and features include:
- Hole Size:** The finished hole size and plated thru holes are specified as $\varnothing 0.10$.
 - Grid Dimensions:** The grid is defined by rows A through I and columns 1 through 8.
 - Reference Dimensions:**
 - Horizontal reference dimensions: 0.625 REF, 1.425 REF, 1.9 TYP, and 13.3 REF.
 - Vertical reference dimensions: 2.4 REF, 11.2 REF, and 2.4 REF.
 - Connector Outline:** The connector outline is shown as a dashed line.
 - Legend:**
 - = SIGNAL
 - ⊗ = GROUND
 - Scale:** The scale is 6:1.

CONTACT LAYOUT									
A = 4.50mm (DIM M)									
B = 3.70mm (DIM M)									
G = 3.00mm 6.00mm									
		COLUMNS							
ROWS		8	7	6	5	4	3	2	1
	A	G	A	G	A	G	A	G	B
	B	A	A	A	A	A	A	A	B
	C	A	G	A	G	A	G	A	G
	D	G	A	G	A	G	A	G	A
	E	A	A	A	A	A	A	A	A
	F	A	G	A	G	A	G	A	G
	G	G	A	G	A	G	A	G	A
	H	B	A	A	A	A	A	A	A
	I	B	G	A	G	A	G	A	G

4805 (3/11)

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[TE Connectivity:](#)

[2065915-1](#)