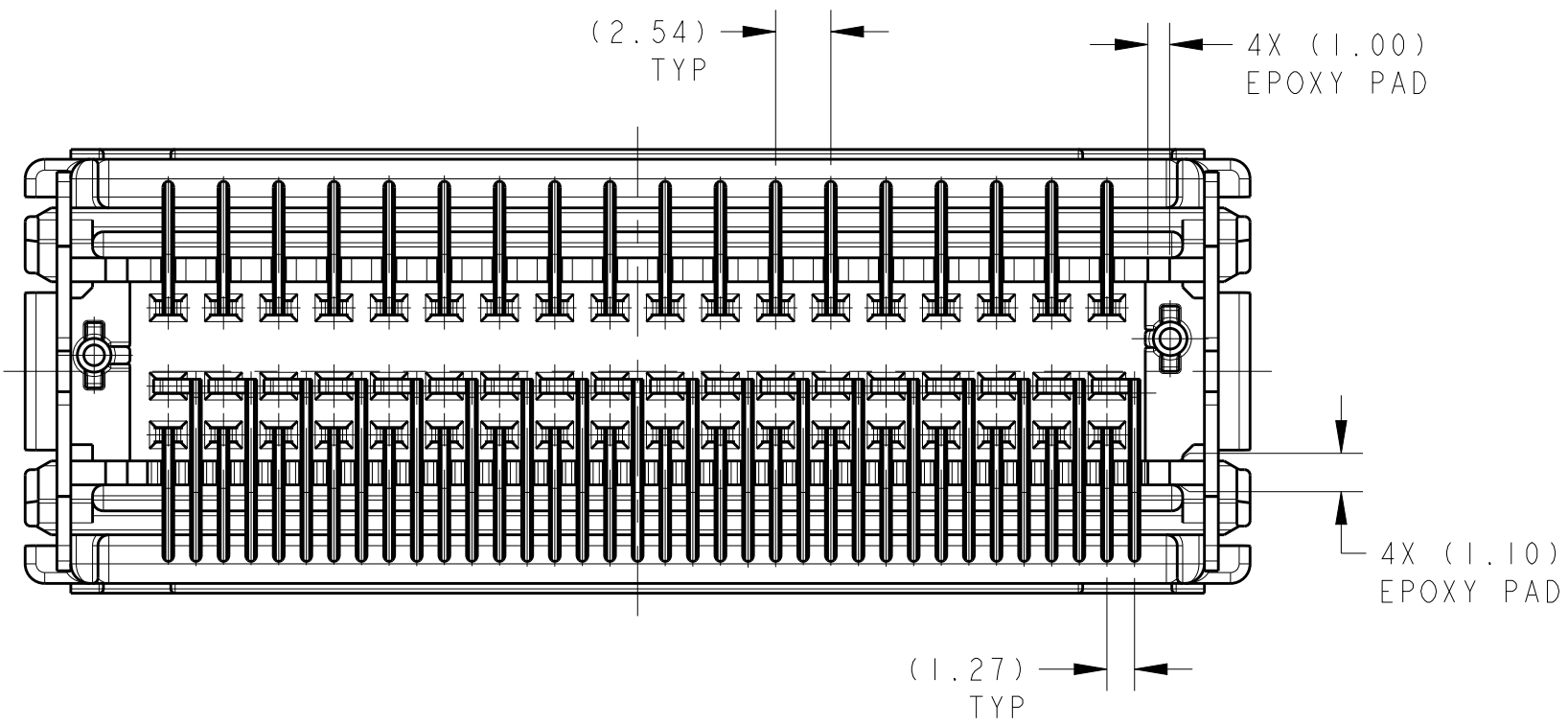
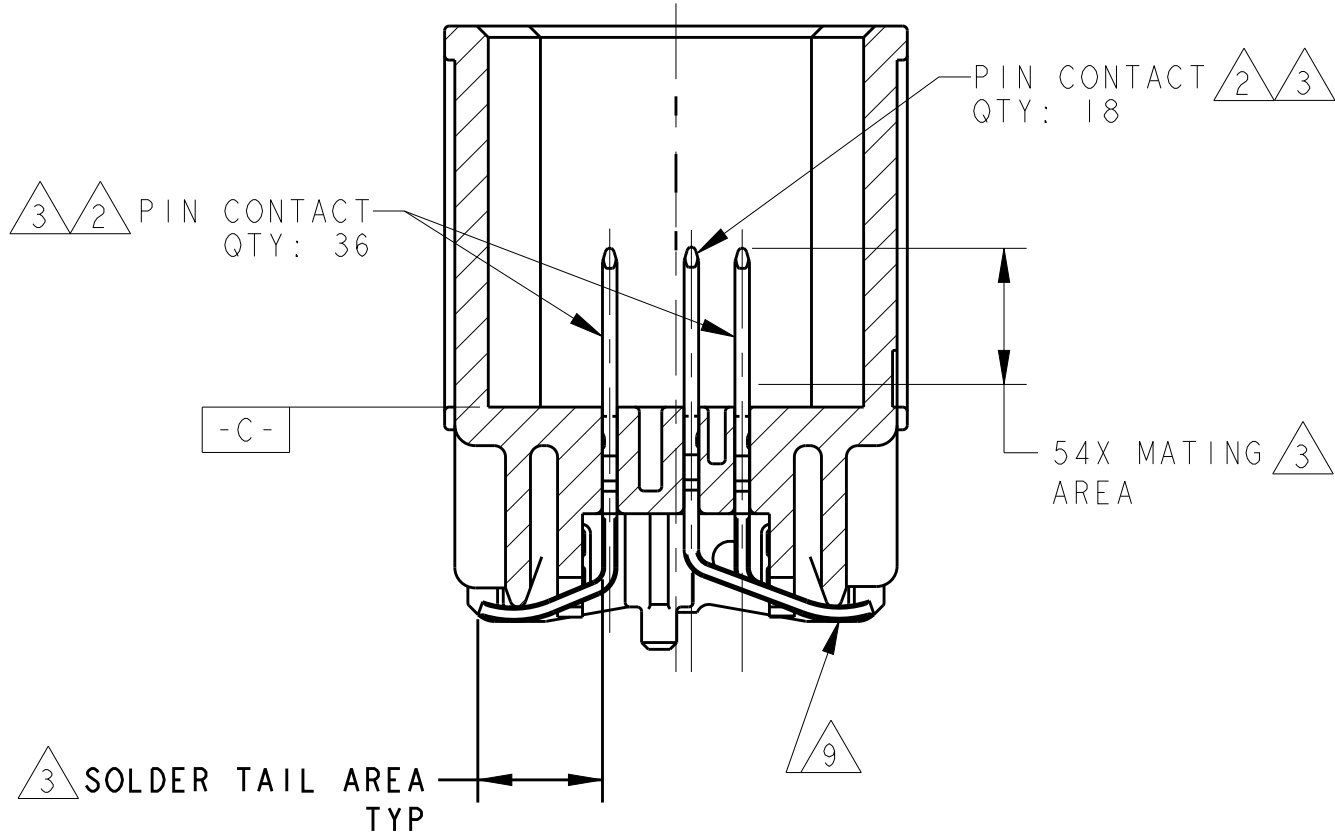
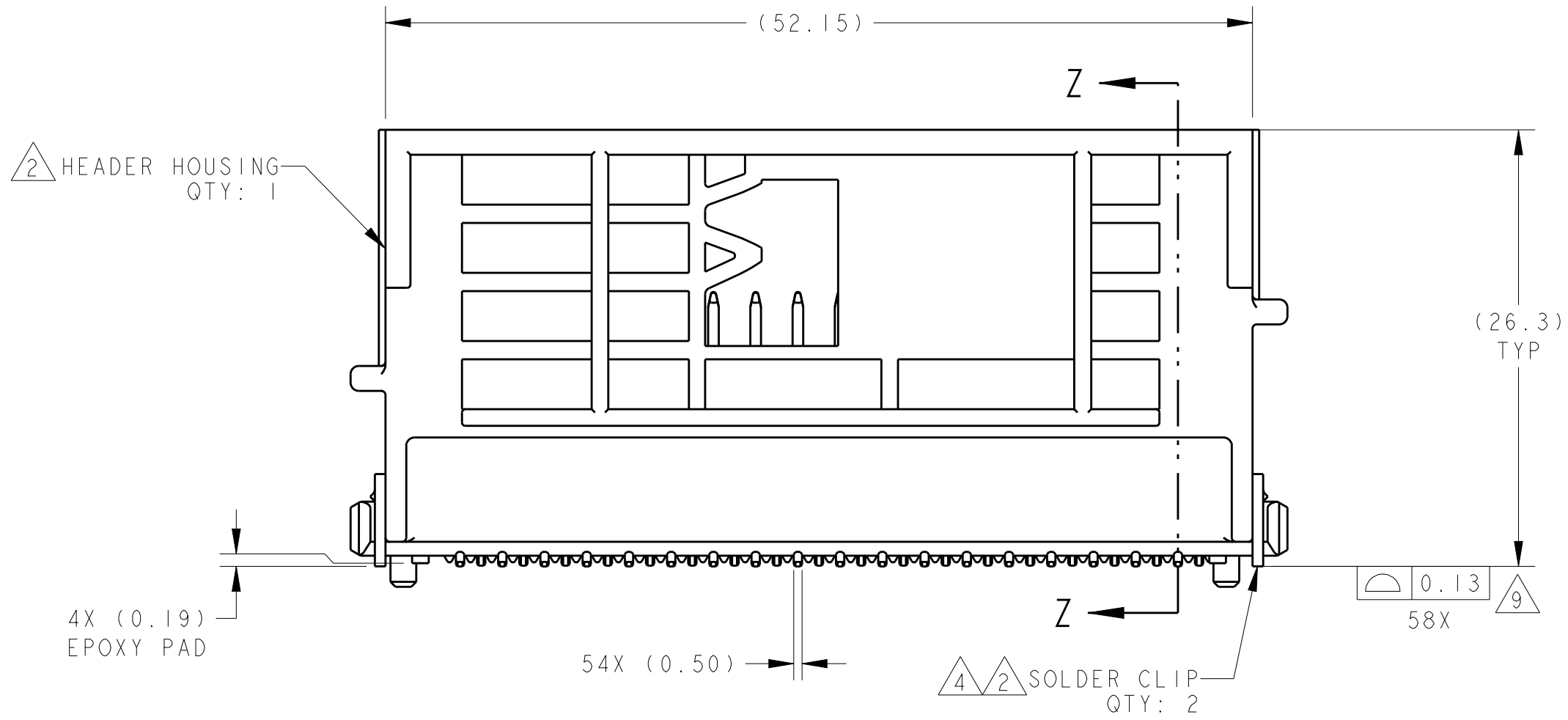
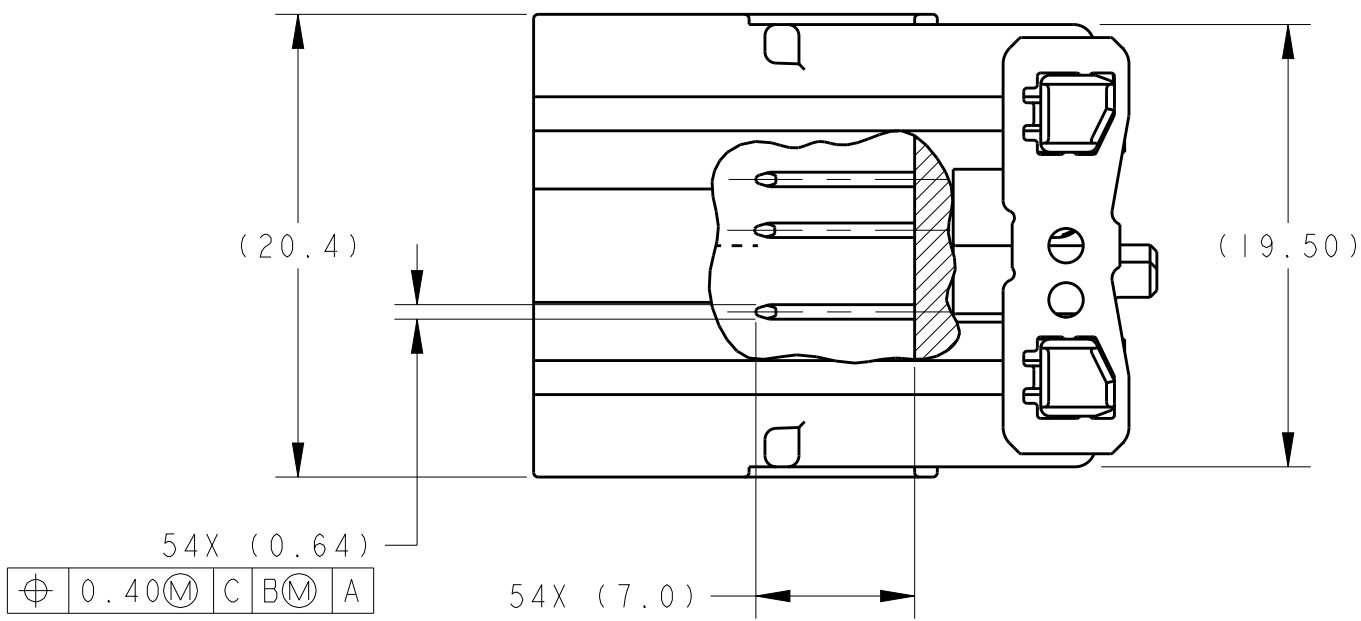
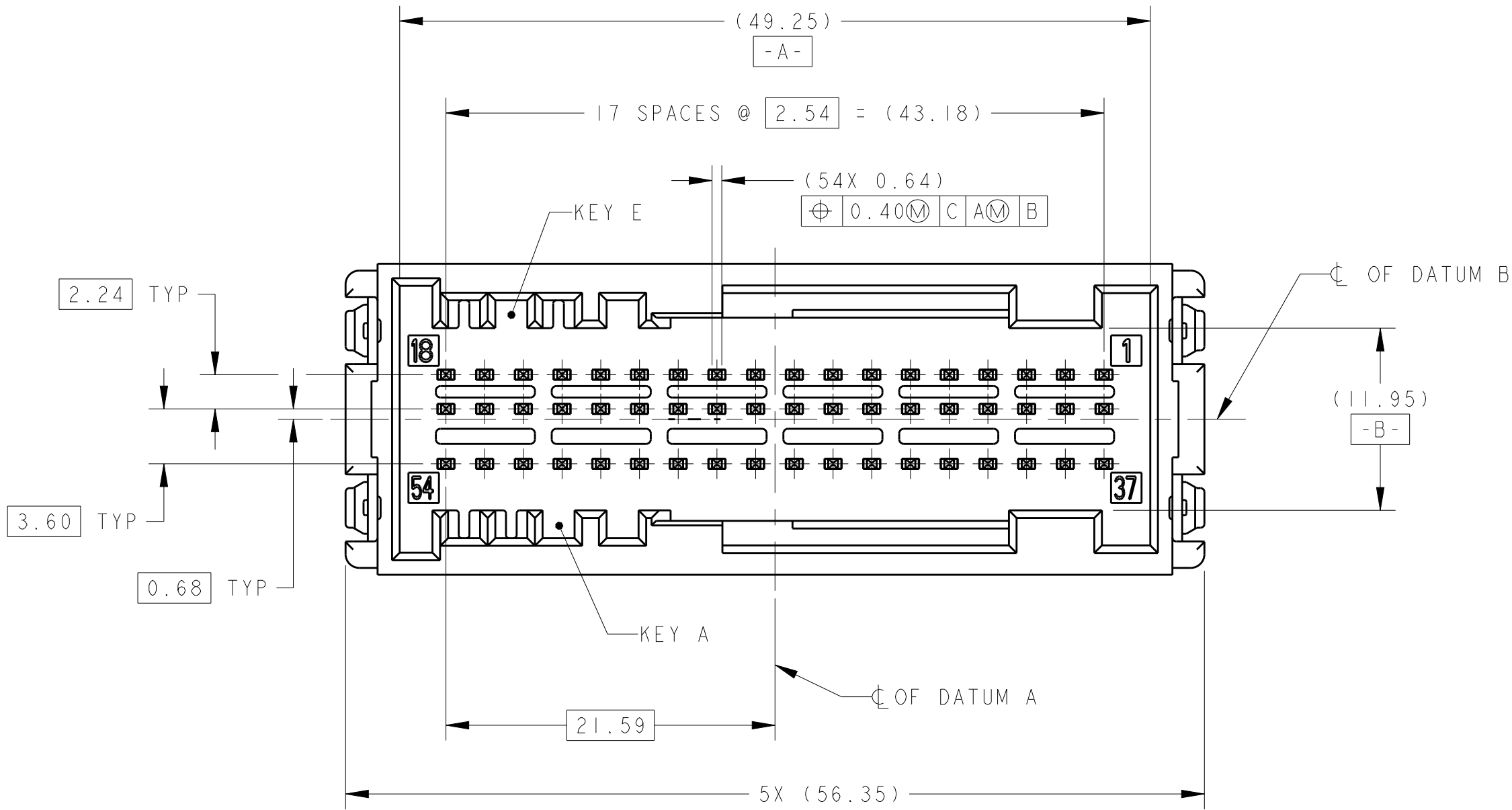
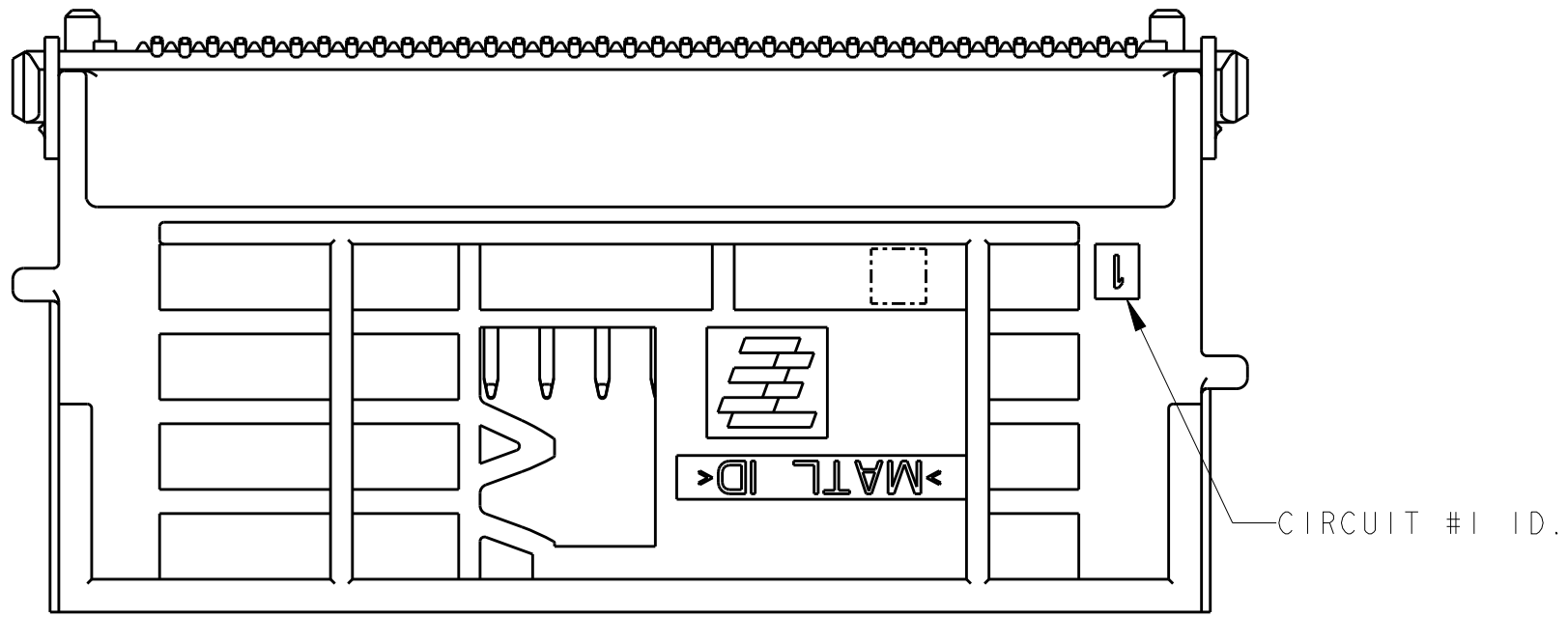


REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	D2	REVISED PER ECO-15-004309	19AUG2015	DLD	CJS
	D3	REVISED PER ECO-19-001555	31JAN2019	JMS	CS



1. 2035239-1 SHOWN.

△2 HEADER HOUSING: POLYESTER PCT, BLACK.
PIN CONTACTS: BRASS.
SOLDER CLIPS: BRASS

△3 PIN CONTACTS: 0.8µm MIN TIN IN MATING AREA AND 2.5µm MIN TIN IN SOLDER TAIL AREA ALL OVER 1.00µm NICKEL.

△4 SOLDER CLIPS: 3.8µm MIN TIN OVER 1.27µm MIN NICKEL.

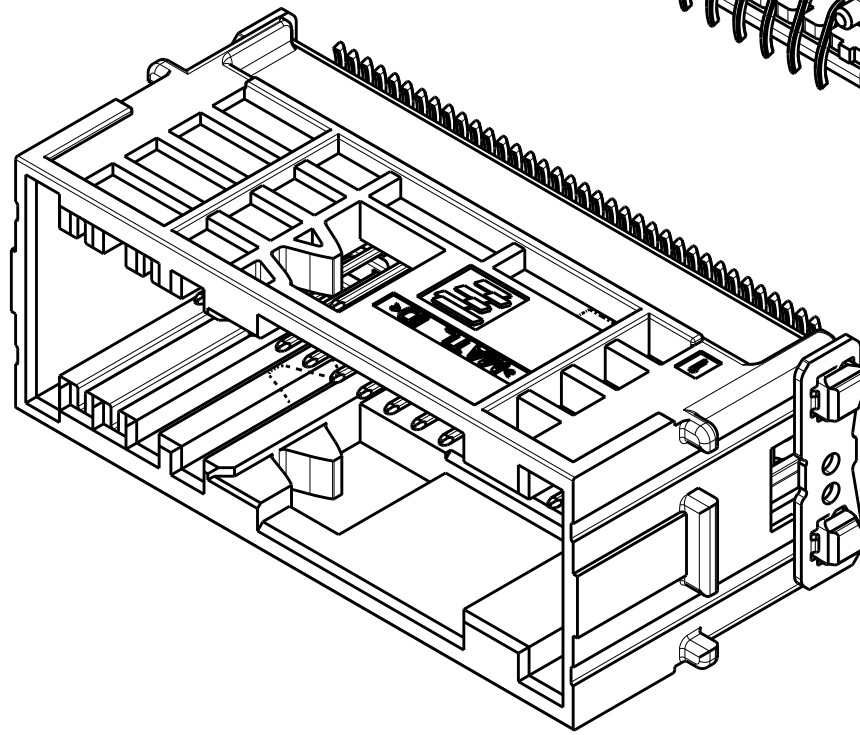
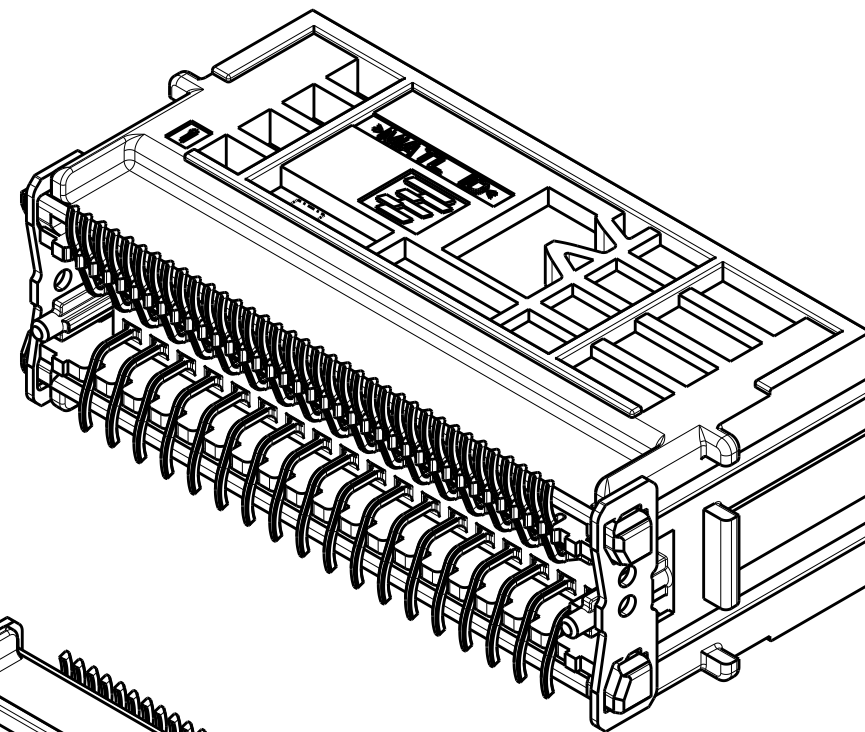
△5 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER.

△6 ALLOW FOR OVER PRINTING ON PC BOARD.

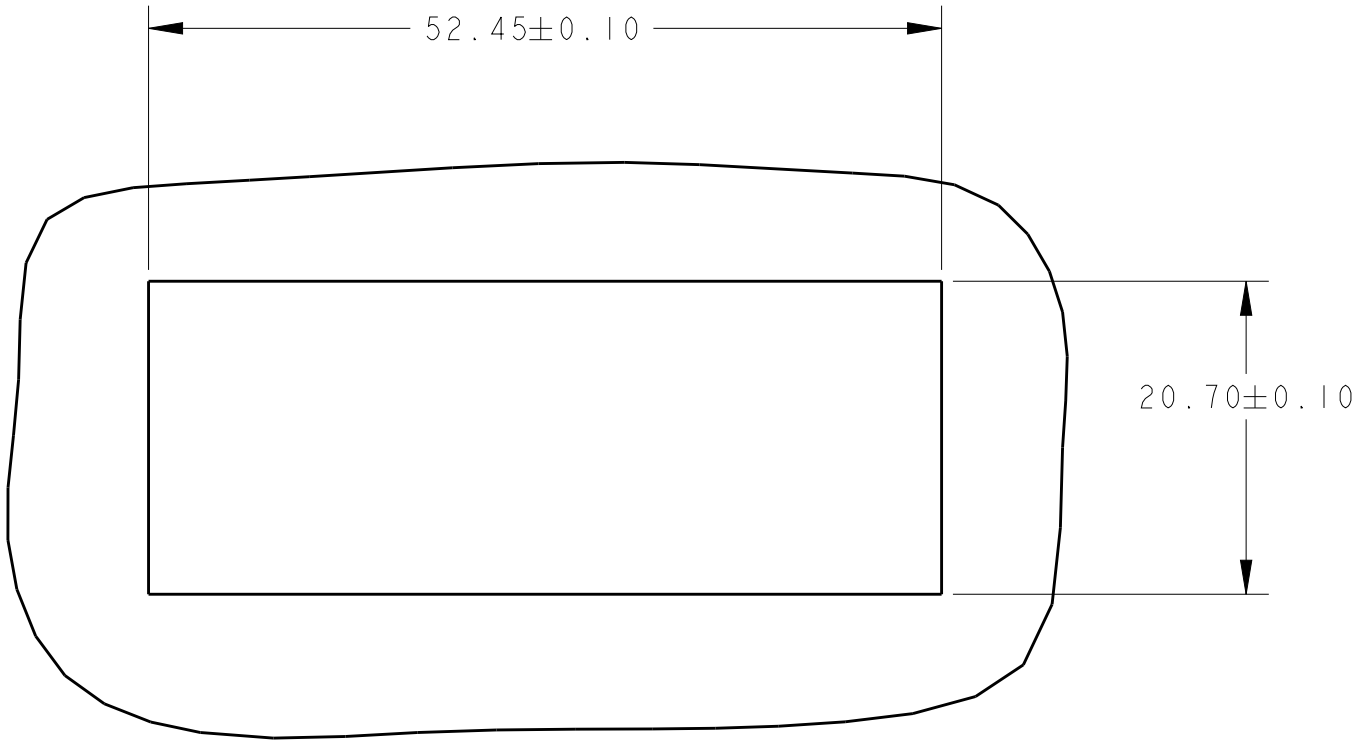
△7 SOLDER PASTE VOLUME PER LAYOUT
SOLDER TAIL, ROW 1 (1.27ϕ) = 0.938mm³
SOLDER TAIL, ROW 2 (2.54ϕ) = 0.953mm³
SOLDER PASTE THICKNESS CAN CHANGE BASED ON APERTURE SIZE BUT SHOULD NOT VIOLATE 0.15mm THICKNESS.

8. THIS PRODUCT HAS COMPLETED VALIDATION TESTING.

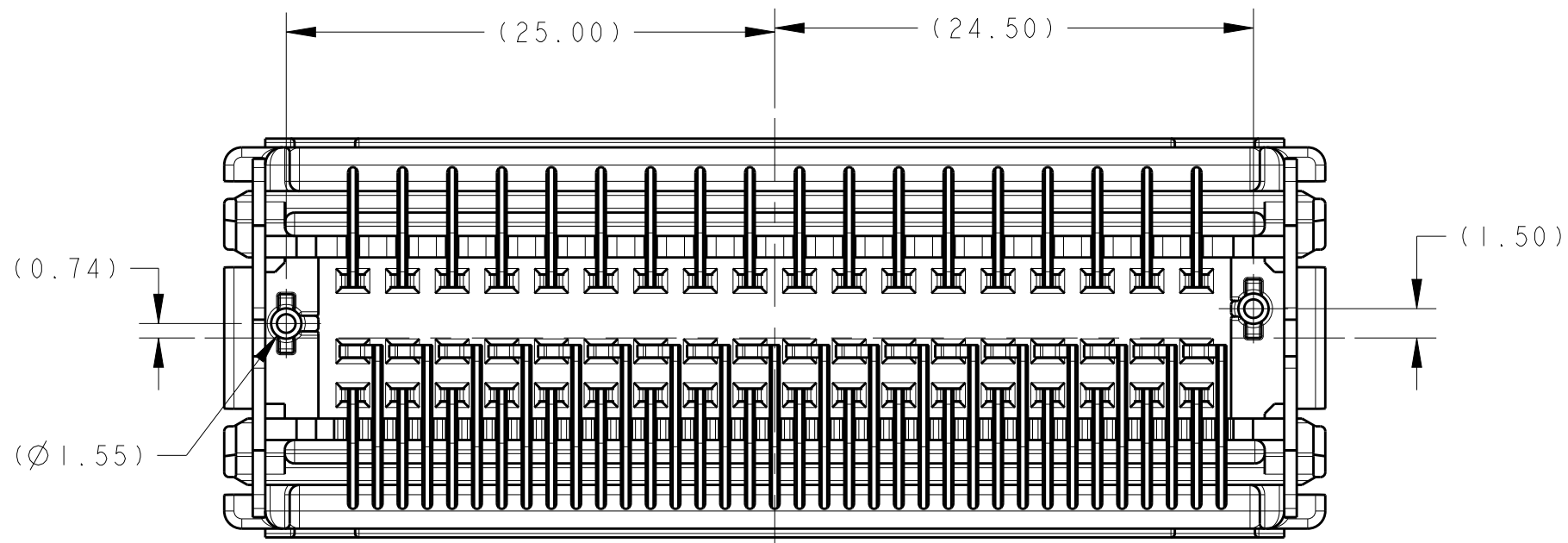
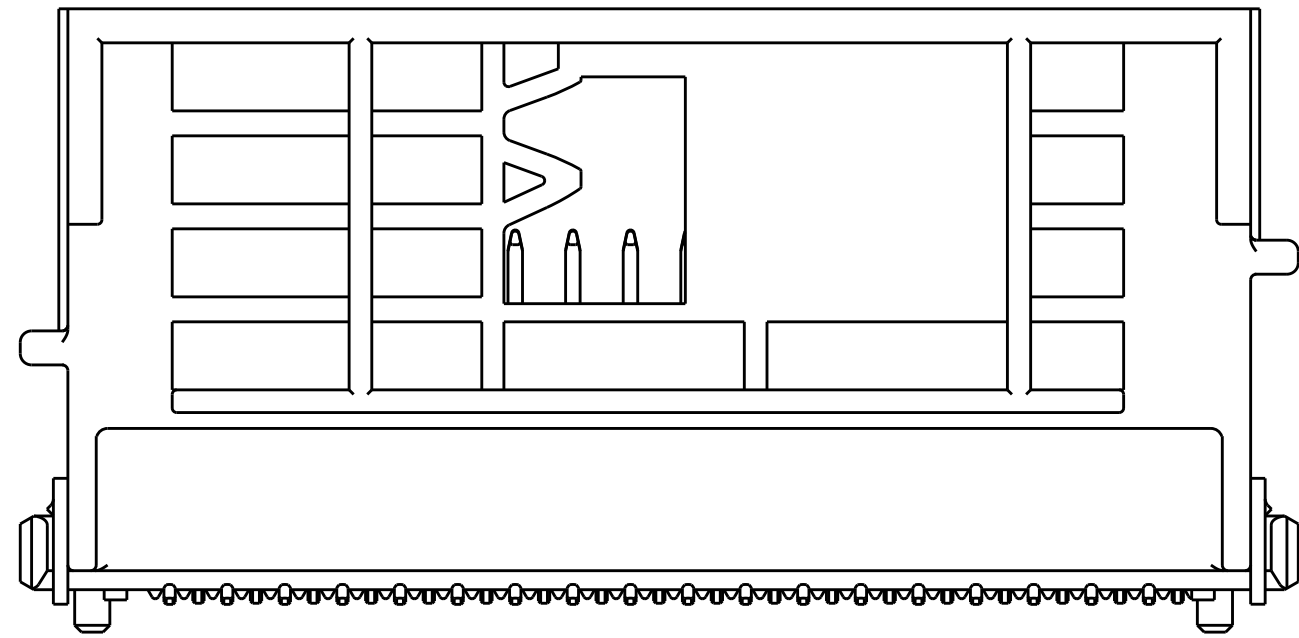
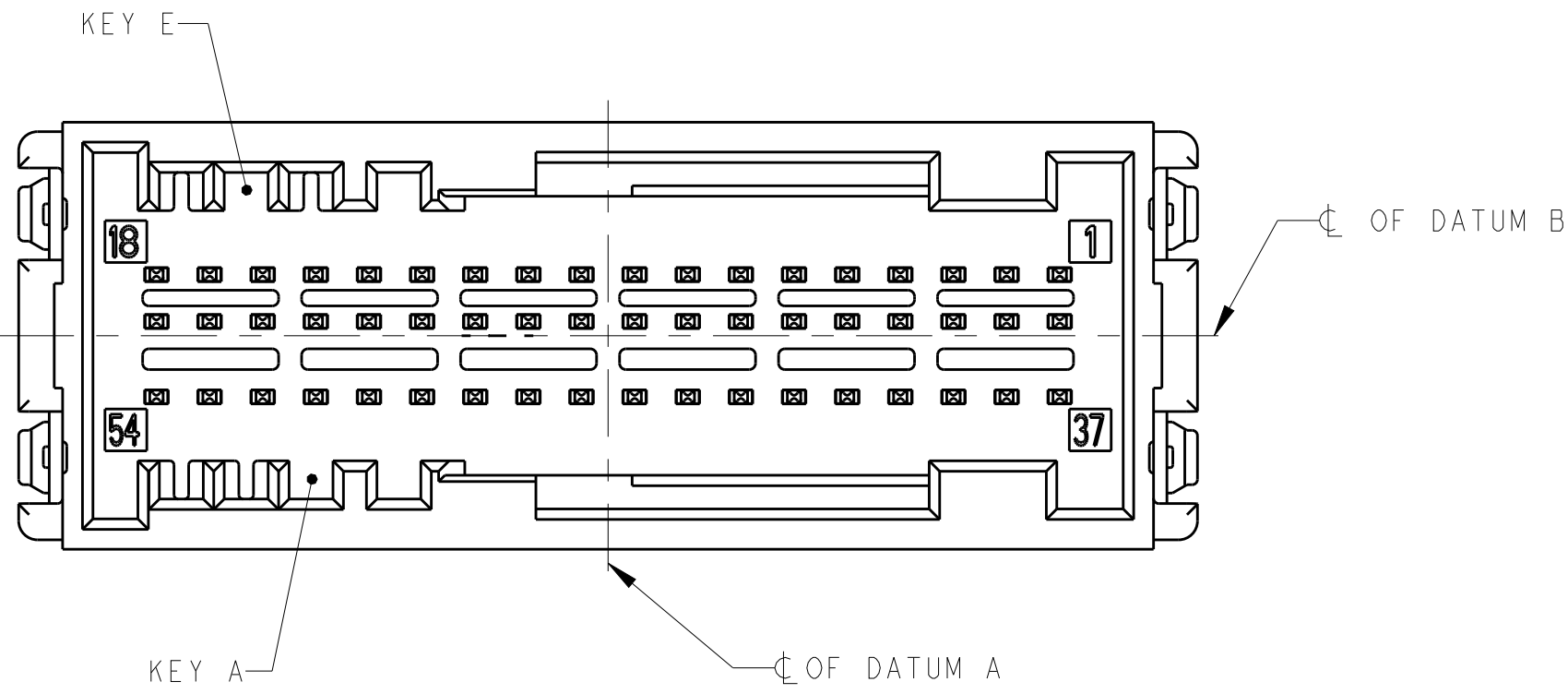
△9 THE COPLANARITY REQUIREMENT APPLIES AT THE TANGENT OF THE RADIUS OF THE J-LEAD SOLDER TAILS AS SHOWN, NOT AT THE TIPS OF THE J-LEADS.



ISOMETRIC VIEWS
SCALE 2:1

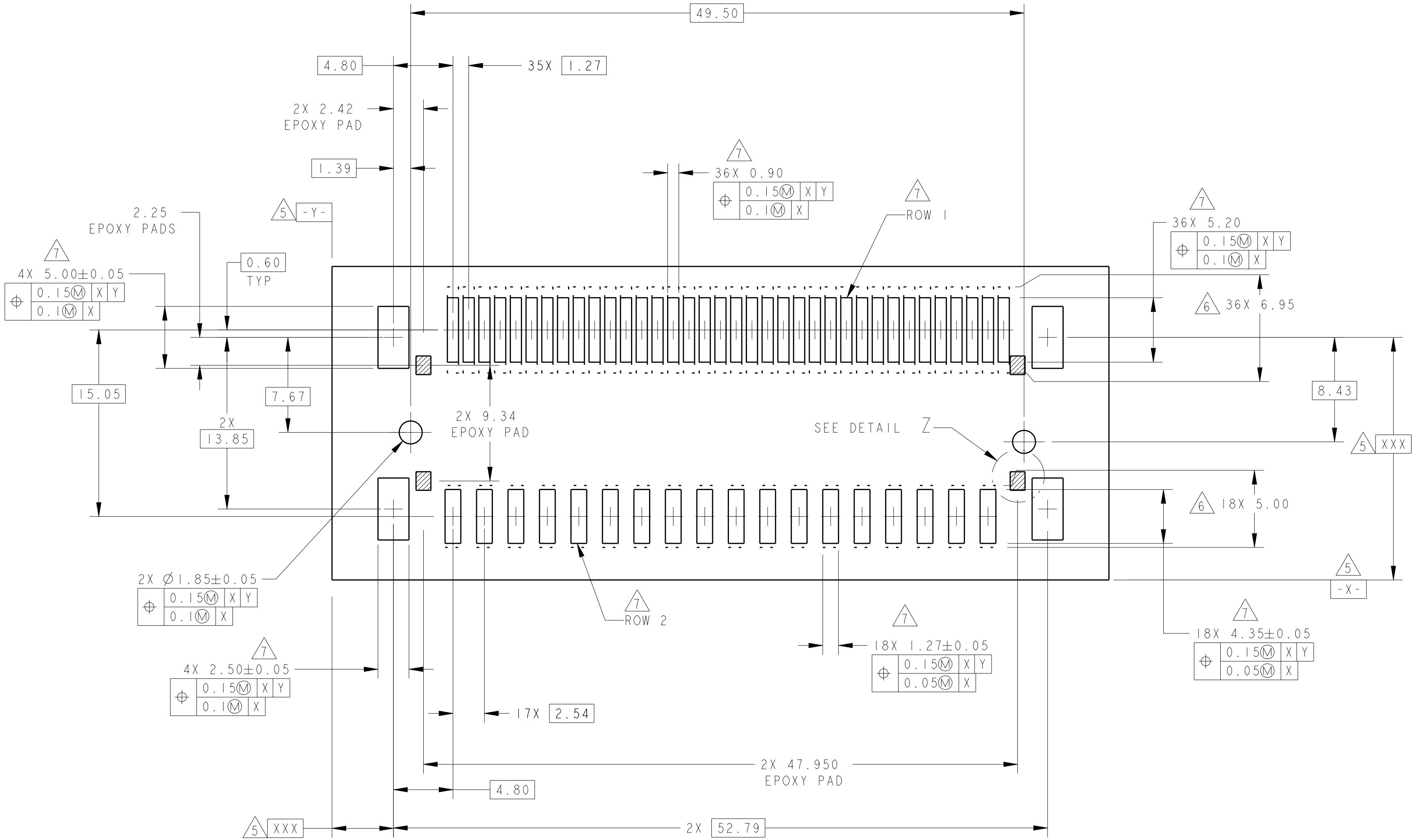


RECOMMENDED PANEL CUT-OUT
SCALE 2:1

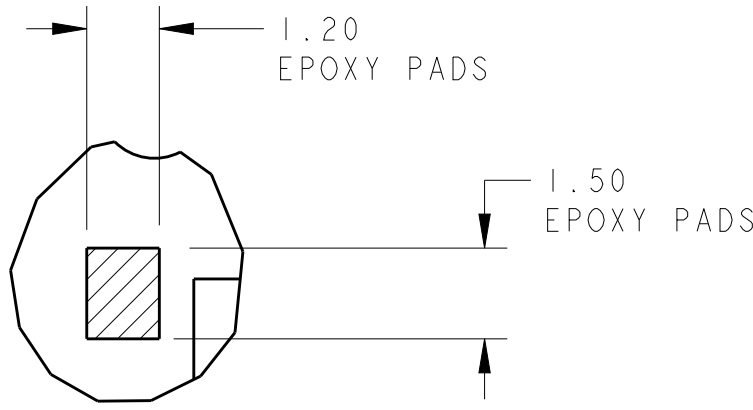


KEY CODE AE
2035239-1 ONLY
SCALE 3:1

REVISIONS				
P	LTR	DESCRIPTION	DATE	DWN
-	-	SEE SHEET 1	-	-



RECOMMENDED PC BOARD LAYOUT
CONNECTOR SIDE OF BOARD
SCALE 4:1



DETAIL Z
SCALE 8:1

THIS DRAWING IS A CONTROLLED DOCUMENT. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.		DWN A. FOLTZ 08MAY2008	TE Connectivity	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		CHK D. FRY 09MAY2008		
DIMENSIONS:	mm	APVD D. FRY 09MAY2008	NAME	
	0 PLC	PRODUCT SPEC	54 POSITION, VERTICAL, SURFACE MOUNT, 0.64mm GENERATION Y, LAC	
	1 PLC	APPLICATION SPEC		
MATERIAL	-	WEIGHT	SIZE	RESTRICTED TO
-	SEE NOTES AND NOTE CALLOUTS	-	A100779C=2035239	-
-	-	CUSTOMER DRAWING	SCALE	REV
-	-	-	3:1	D3

Mouser Electronics

Authorized Distributor

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[TE Connectivity:](#)

[2035239-1](#)