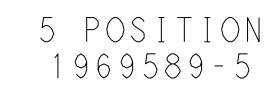
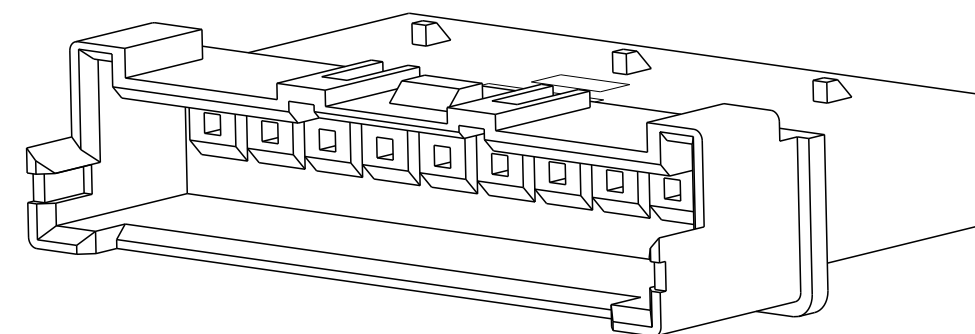


LOC	DIST	REVISIONS					
-	-	P	LTR	DESCRIPTION	DATE	DWN	APVD
			B1	RELEASED PER ECR-15-008087	29MAY2015	NK	JR



-
- A schematic diagram of a 10-port RJ45 patch panel. On the left side, there is a 12-pin D-sub connector. On the right side, there are ten RJ45 ports arranged in a single row. Each port is represented by a rectangular box with a central square and four smaller squares on the sides, indicating the internal wiring structure. The ports are connected to the D-sub connector via a series of horizontal lines representing the patch panel's internal wiring.



15	40.00	38.00	1-1969589-5
14	37.50	35.50	1-1969589-4
13	35.00	33.00	1-1969589-3
12	32.50	30.50	1-1969589-2
11	30.00	28.00	1-1969589-1
10	27.50	25.50	1-1969589-0
9	25.00	23.00	1969589-9
8	22.50	20.50	1969589-8
7	20.00	18.00	1969589-7
6	17.50	15.50	1969589-6
5	15.00	13.00	1969589-5
4	12.50	10.50	1969589-4
3	10.00	8.00	1969589-3
2	7.50	5.50	1969589-2
POS.	DIM.B	DIM.A	PART NO.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN A. NOEL 030C2012		 TE Connectivity	
		CHR -			
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME ECONOMY POWER 2.5, HOUSING WIRE TO WIRE, GLOW WIRE -	
		APVD -			
		PRODUCT SPEC 108-2418			
		APPLICATION SPEC 114-13315			
MATERIAL SEE NOTES		0 PLC ± 1 PLC ± 2 PLC ±0.20 3 PLC ± 4 PLC ± ANGLES ±		SIZE A2	
FINISH		WEIGHT -		CAGE CODE 00779	
SEE NOTES		SEE NOTES		DRAWING NO C-1969589	
		CUSTOMER DRAWING		RESTRICTED TO -	
				SCALE 4:1	
				SHEET 1 OF 1	
				REV B1	

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