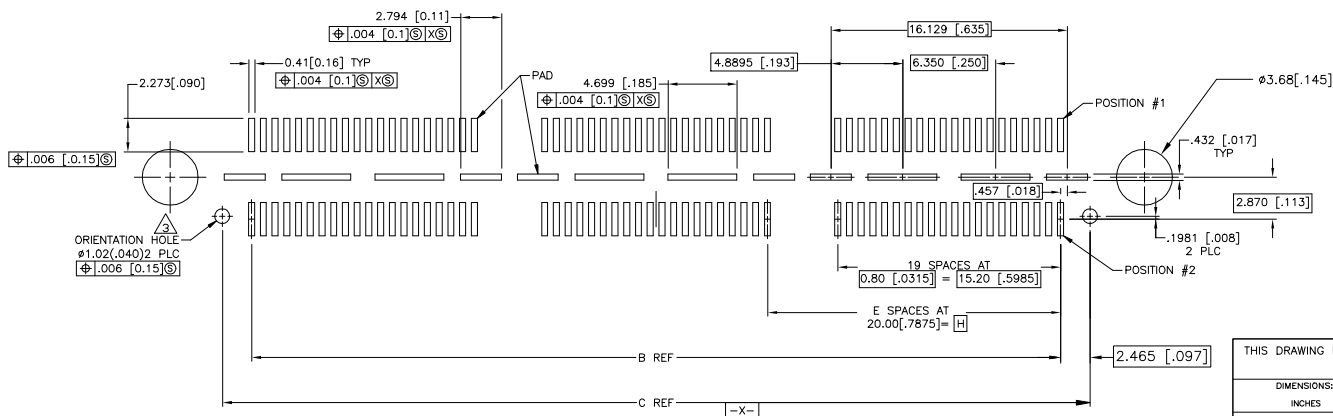
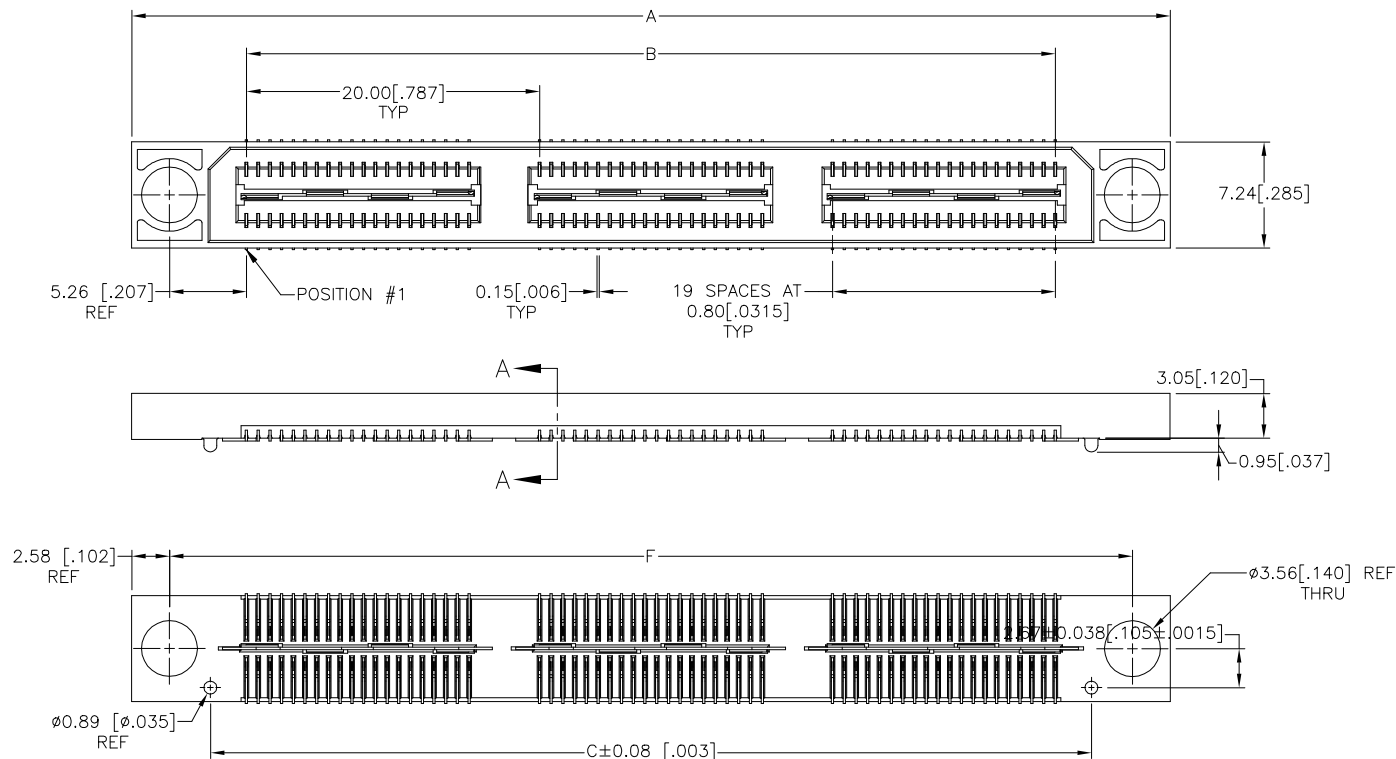
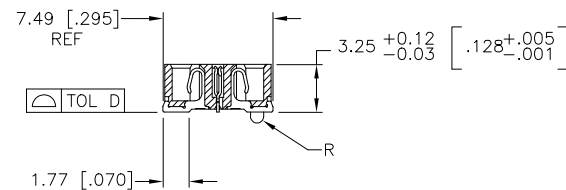
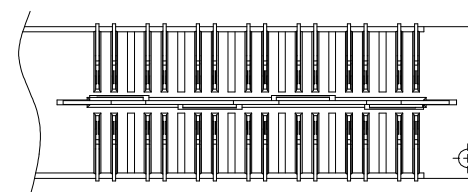


LOC	DIST	REVISIONS				
P	LTR	DESCRIPTION		DATE	DWN	APVD
J1		REV PER ECD-19-010757		16MAY2019	AL	HL

- 1 HOUSING: LCP, BLACK.
CONTACT & GROUND PLANE: PHOS BRONZE.
- 2 CONTACT PLATING: SELECT 0.76 MICROMETERS (.000030 IN.) MIN. GOLD.
- 3 FOR STANDARD APPLICATIONS, ORIENTATION HOLE TO BE $\phi 1.02$ [.040].



RECOMMENDED PC BOARD LAYOUT
 (VIEWED FROM CONNECTOR SIDE)



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN V. FURLER 03MAR03	29SEP03	Tyco Electronics Corporation Harrisburg, PA 17105-3608	
DIMENSIONS: INCHES		CHK D. DIXON 03MAR03	APVD D. DIXON 03MAR03	NAME RECEPTACLE ASSEMBLY, VERTICAL 0.80MM CL, MICTOR SB	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		PRODUCT SPEC		APPLICATION SPEC	
MATERIAL		WEIGHT		RESTRICTED TO	
1		2		SCALE 4:1 SHEET 1 of 2 REV J1	

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