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1

0.76µm GOLD IN LOCALIZED PLATE AREA, 3.81µm TIN-LEAD ON SOLDER TAILS ALL OVER 1.27µm NICKEL.

2

CIRCUIT IDENTIFICATION FEATURE OMITTED ON 10 POSITION HEADER ASSEMBLIES.

3

DIMENSION APPLIES AT BASE OF SHROUD.

4

NOTE DELETED.

5

NOTE DELETED.

6

VACUUM COVER DESIGNED FOR 4mm DIA NOZZLE. VACUUM COVER TO BE REMOVED AFTER SOLDERING.

7

PACKAGED IN TAPE AND REEL PER EIA-481 SPECIFICATIONS. SEE CHART FOR TAPE WIDTHS.

8

6.75mm MIN TARGET AREA FOR VACUUM PICK-UP.

9

HOUSING: LCP
CONTACT: COPPER ALLOY
VACUUM COVER: ALUMINUM.

10

VACUUM COVER SHOWN IN PHANTOM LINE.

11

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12

NOTE DELETED.

13

NO CIRCUIT IDENTIFICATION FEATURE ON 10,12,20,24 AND 30 POSITION PARTS.

14

FINISH: 0.025µm MIN GOLD ON GOLD PLATED AREA, 3.81-6.35µm MATTE TIN-LEAD ON TIN PLATED AREA. UNDER PLATING TO BE 1.27-2.54µm NICKEL ON ENTIRE CONTACT.

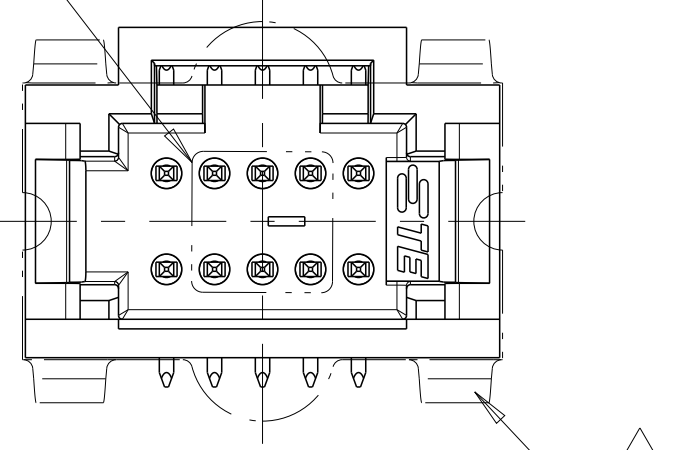
15

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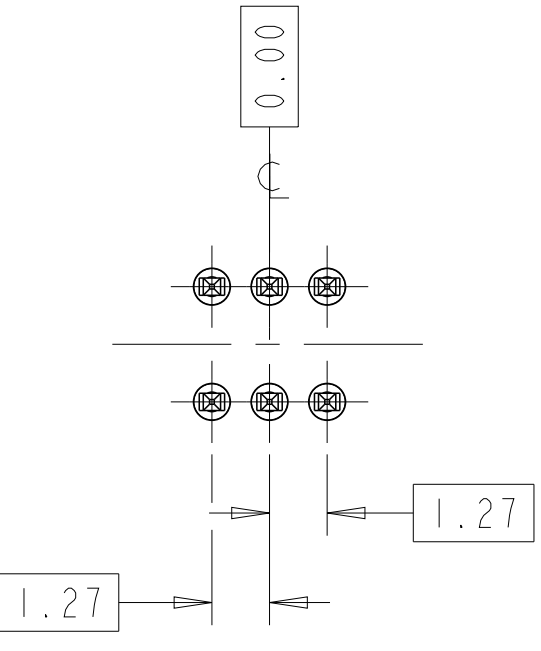
16

PRELIMINARY - NOT FOR PRODUCTION.

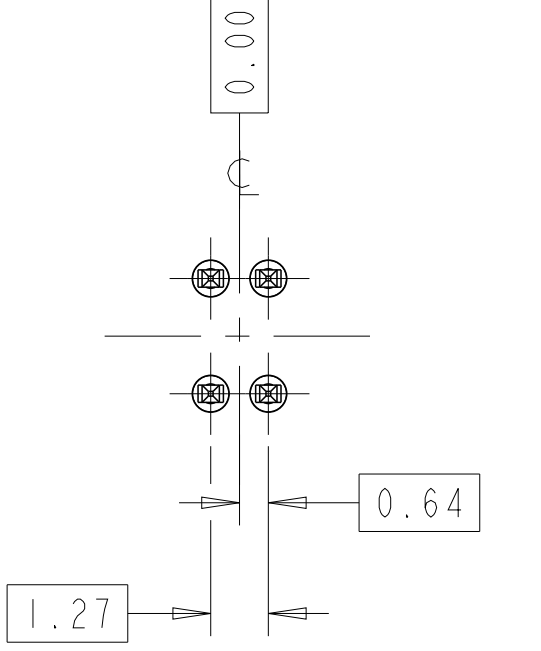
SEE DETAIL S



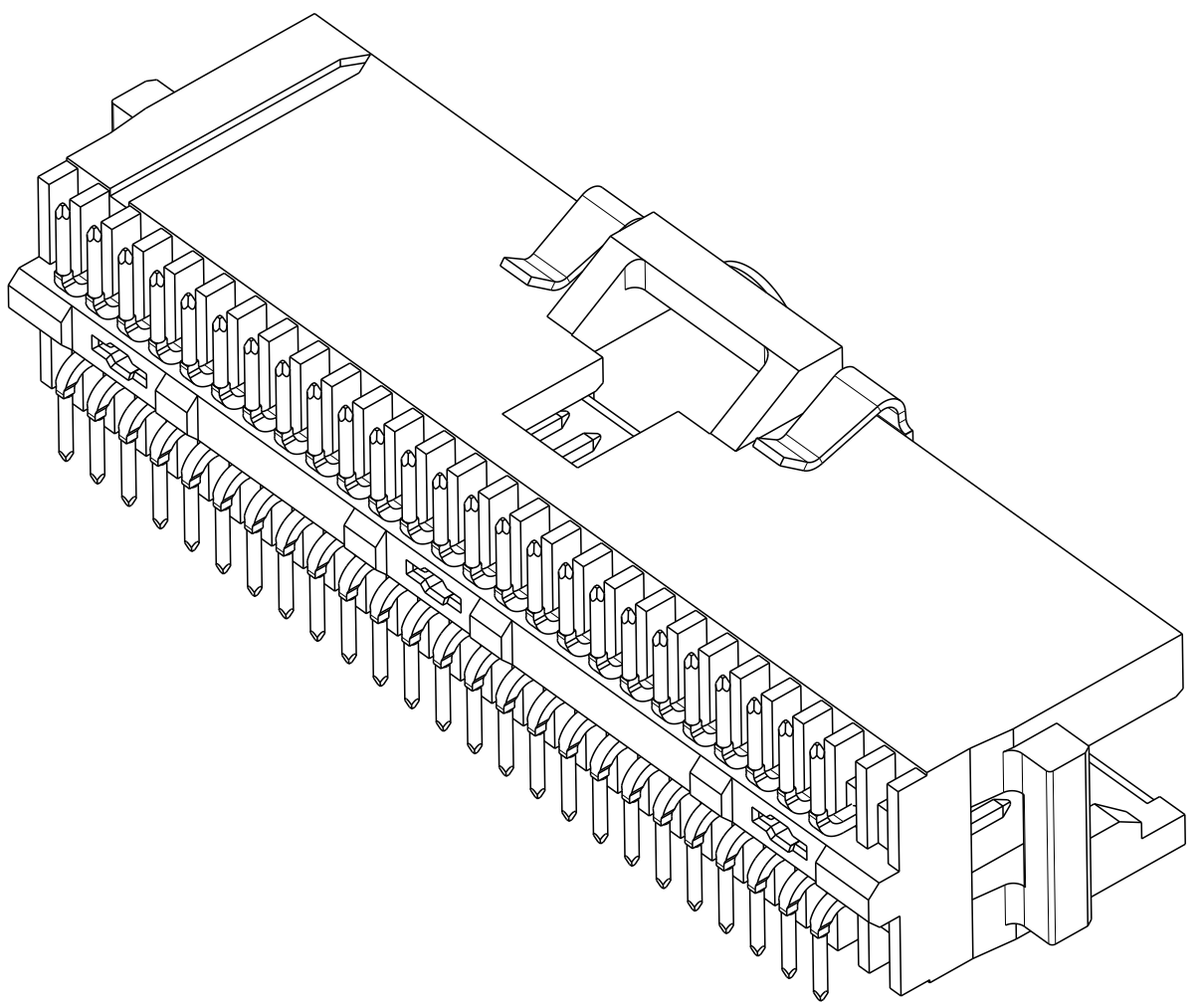
147377-1 SHOWN



BASIC DIMENSIONS FOR
EVEN NUMBER OF SPACES
(SEE TABLE)



BASIC DIMENSIONS FOR
ODD NUMBER OF SPACES
(SEE TABLE)



3.50±0.05

⊕ 0.23Ⓢ M N

0.05Ⓢ

PAD TYP

0.74±0.03

⊕ 0.23Ⓢ M N

0.05Ⓢ

PAD TYP

1.27±0.05

TYP

5.69

9.19

REF

2.84

OF PATTERN

.MMM

B SPACES AT @1.27 = DIM_C±0.05

TOLERANCE NON -CUMULATIVE

.NNN

RECOMMENDED BOARD LAYOUT

1.17

REF

0.77±0.25

2.54±0.05

3.175±0.10

1.39

LOCALIZED GOLD PLATE AREA

0.38±0.02

⊕ 0.15Ⓜ X YⓈ ZⓈ

SECTION A-A

1.45±0.25

1.27

11.00±0.18

22

1.27

2 PLC

1.45±0.25

1.27

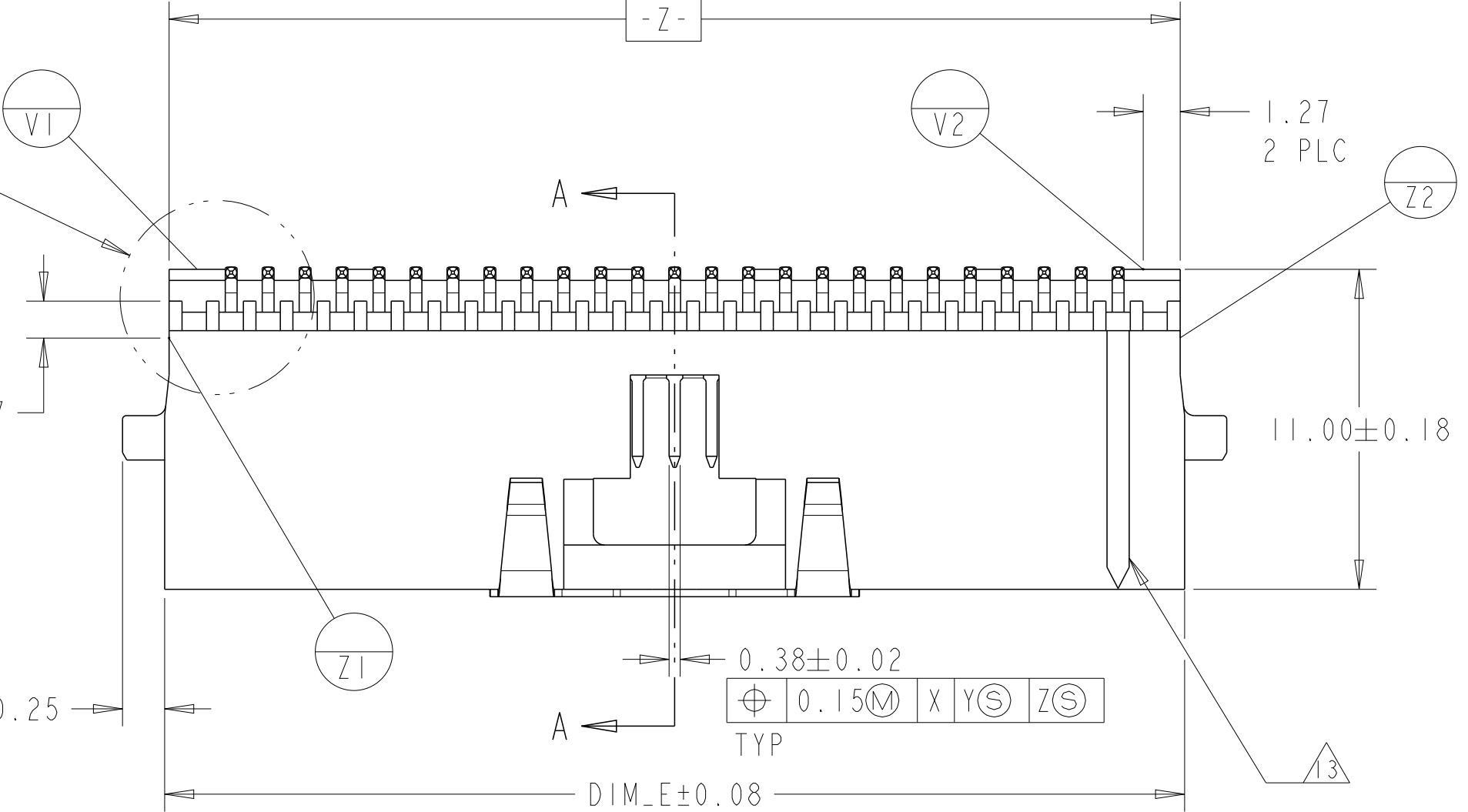
11.00±0.18

22

1.27

2 PLC

SEE DETAIL W



1.27

1.45±0.25

11.00±0.18

22

1.27

2 PLC

0.00

1.27

1.27

1.52±0.05

(1.27)

(2.54)

7.21±0.07

3

0.38±0.02

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TYP

B SPACES AT 1.27 = C

DETAIL W

SCALE 10:1

0.15

V

0.10

ALL SOLDER LEADS

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1.27

1.27

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