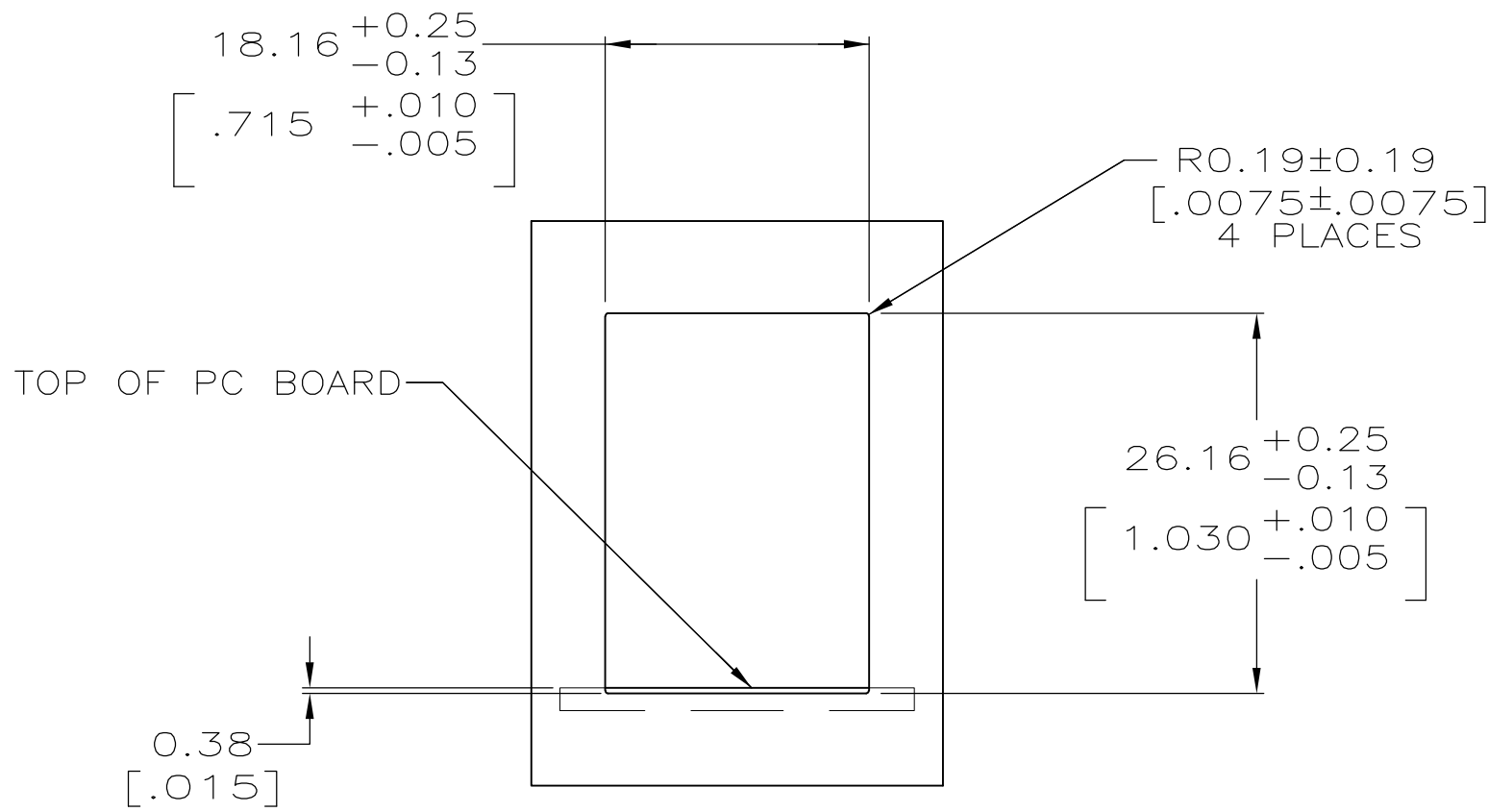
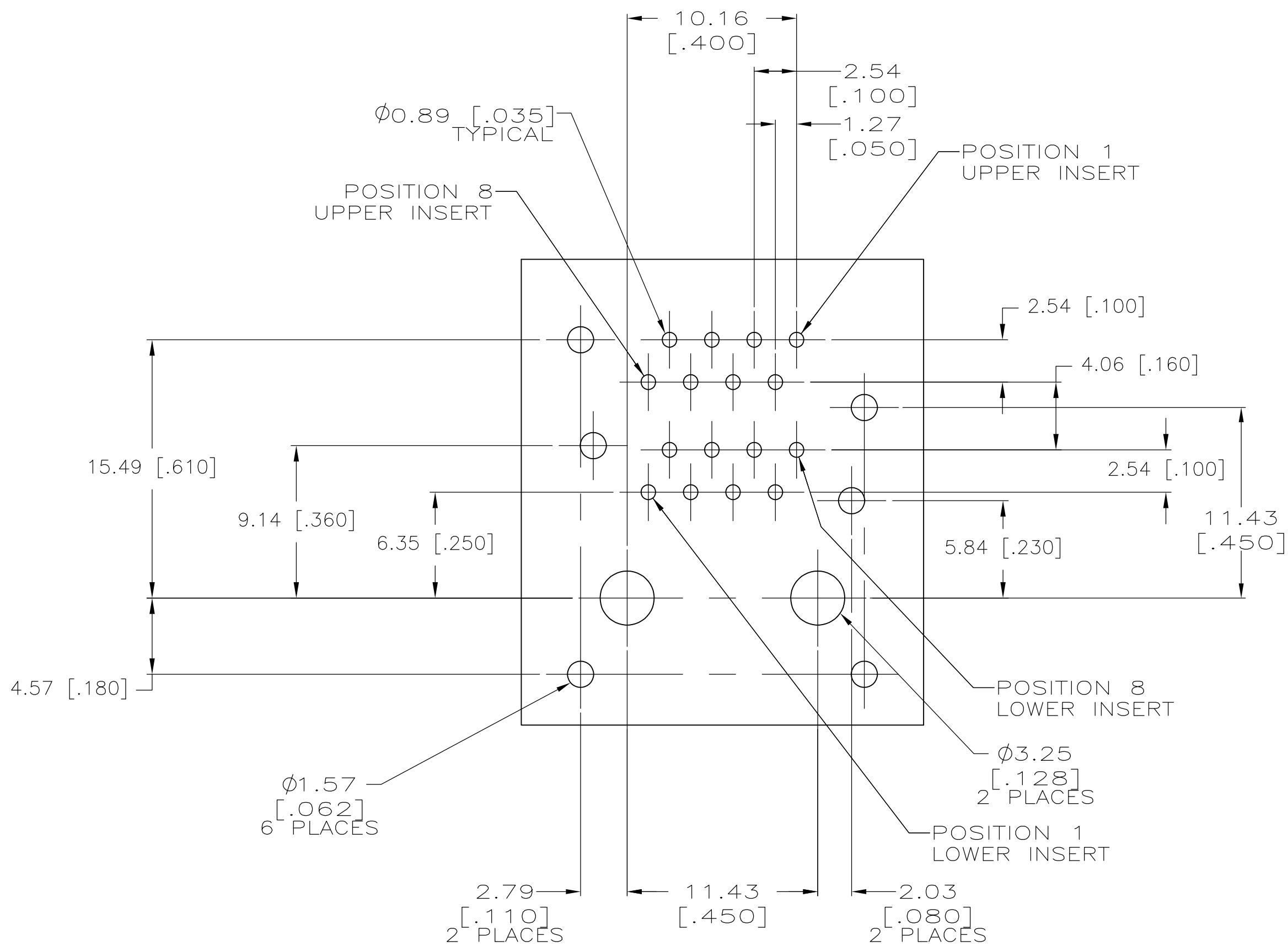
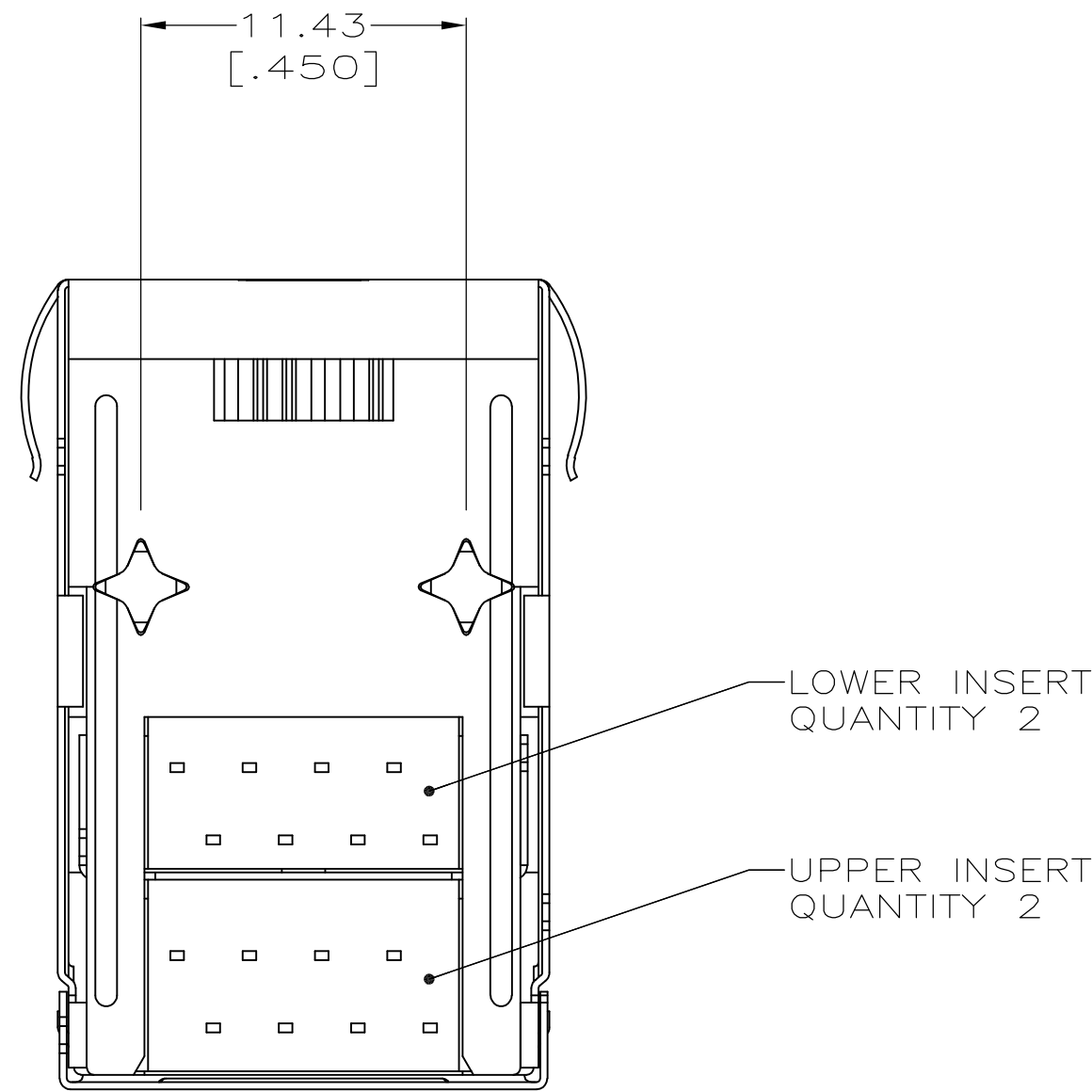
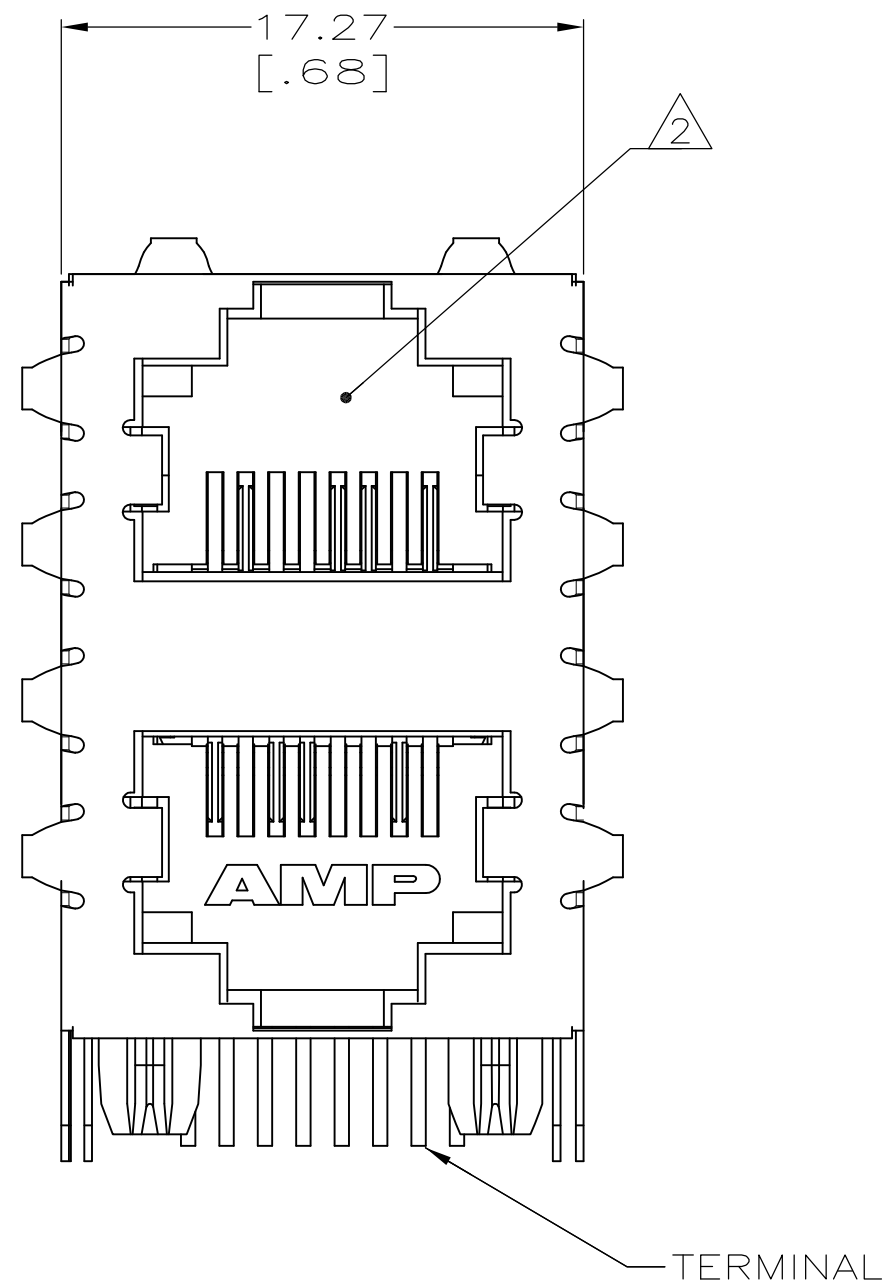
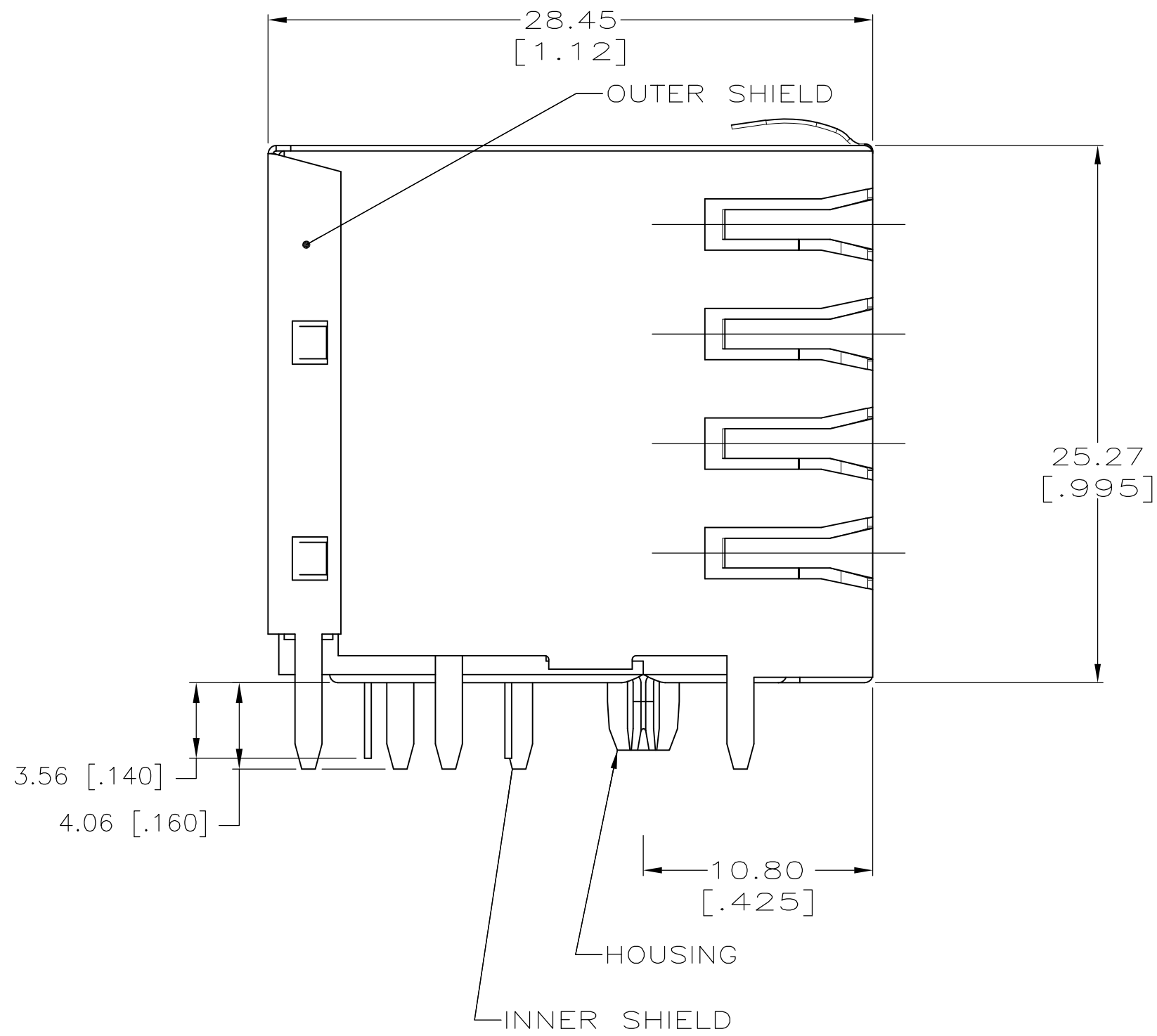
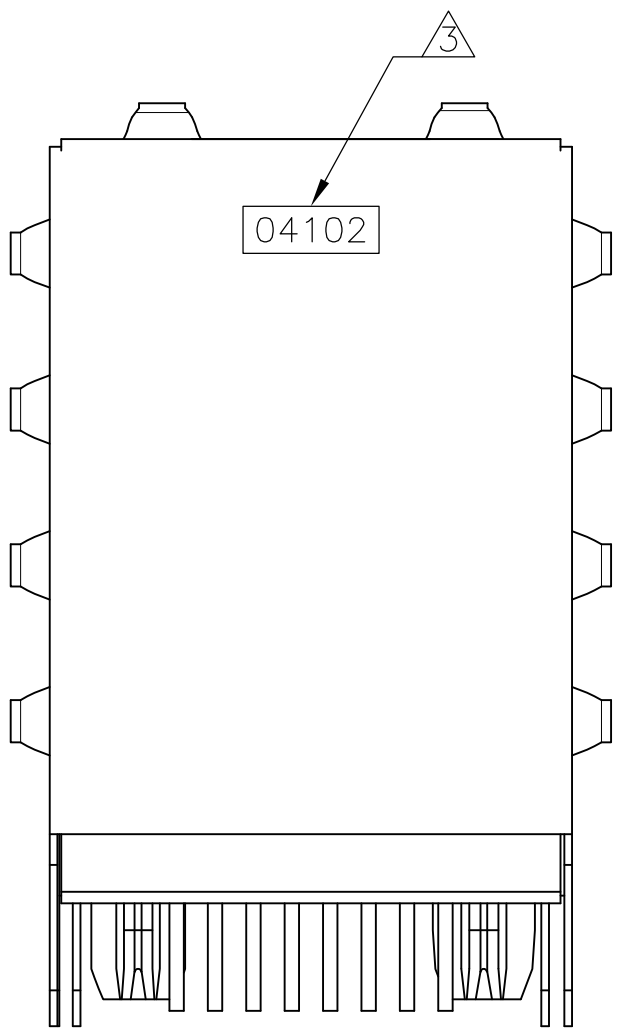


LOC		DIST		REVISIONS				
AA	00	P	LTR	DESCRIPTION	DATE	DWN	APVD	
		A		REV PER ECR-11-021661	28OCT2011	DZ	MQ.W	

1. MATERIAL:
- HOUSING – PCT POLYESTER, BLACK, UL94V-0.
- OVERMOLDED TERMINAL ARRAY – PBT, BROWN.
- TERMINALS – 0.33 [.013] THICK PHOS BRONZE
- PLATED WITH 1.27um [.000050] MIN THICK
- HARD GOLD IN LOCALIZED AREA AND 3.81um
- [.000150] MIN THICK MATTE TIN IN SOLDER
- AREA OVER 1.27um [.000050] MIN THICK
- NICKEL UNDERPLATE.
- SHIELDS – 0.25 [.010] THICK COPPER ALLOY
- PLATED WITH 1.27 um [.000050] MINIMUM NICKEL
- AND 2.03 um [.000080] MINIMUM HOT TIN DIP
- ON PCB GROUND TABS.
- △ JACK CAVITY CONFORMS TO FCC RULES AND
- REGULATIONS, PART 68 SUBPART F.
- △ DATE CODE LOCATED ON REAR OF PART
- APPROXIMATELY AS SHOWN:
- FIRST 2 DIGITS ARE LAST 2 DIGITS OF YEAR,
- SECOND 2 DIGITS ARE MANUFACTURING
- WORK WEEK,
- LAST DIGIT IS DAY OF WEEK, WITH SUNDAY = 1.



PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE

SUGGESTED PANEL CUTOUT DIMENSIONS
SCALE 2:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN D.ZHU	16JUN2011	 TE Connectivity		
		CHK D.ZHU	16JUN2011			
DIMENSIONS: mm [INCHES]	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD MQ.W		NAME	STACKED MODULAR JACK ASSEMBLY, 2x1 PORT, 8 POSITION, SHIELDED, PANEL GROUND, CATEGORY 5	
		PRODUCT SPEC				
	0 PLC ± - 1 PLC ± - 2 PLC ± 0.25[.01] 3 PLC ± 0.13[.005] 4 PLC ± - ANGLES ± -	APPLICATION SPEC		SIZE	CAGE CODE	
		114-2048				
MATERIAL SEE NOTE 1	FINISH SEE NOTE 1	WEIGHT	-	A1 00779  2170161		
CUSTOMER DRAWING		SCALE 4:1		SHEET 1 OF 1 REV A		

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