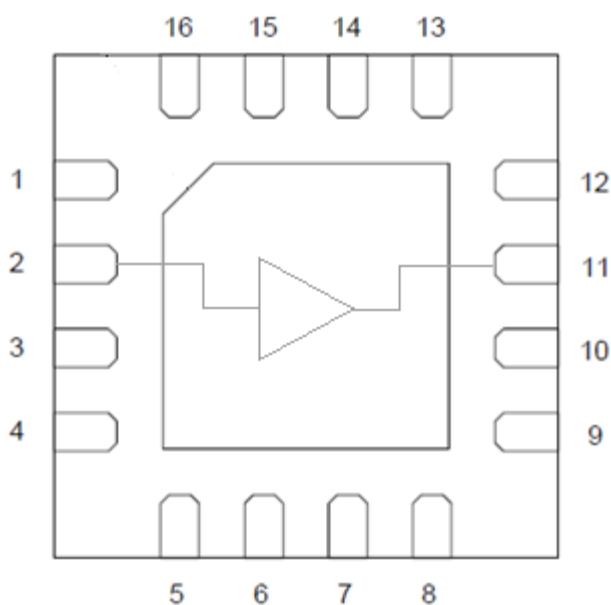


Product Overview

The QPL1818 is a GaAs pHEMT single ended MMIC RF amplifier IC featuring 15dB of gain and low noise. This high linearity IC is designed to support DOCSIS 4.0 Amplifiers and Nodes as well as Fiber to The Home (FTTH), Satellite, Terrestrial TV, Home Gateways, and Cable Modems applications from 50 to 1800MHz. The QPL1818 is powered by a single 5 V supply and packaged in a 3 x 3 16-pin QFN.



Functional Block Diagram



3 x 3 16-pin QFN Package

Key Features

- 50 MHz to 1800 MHz Operation
- 5 V Operation
- Gain: 15 dB Typical
- Noise Figure: 2 dB
- Adjustable Bias Using External Resistors
- RoHS Compliant

Applications

- DOCSIS 4.0 Amplifiers
- DOCSIS 4.0 Optical Nodes
- DOCSIS 4.0 Remote PHY Devices
- FTTH GPON and GEAPON
- DOCSIS 4.0 Cable Modem and Home Gateways
- Single Ended Gain Block

Ordering Information

Part Number	Description
QPL1818EVB-01	Evaluation Board
QPL1818SB	Sample bag with 5 pieces
QPL1818SR	7" Reel with 100 pieces
QPL1818TR7	7" Reel with 2500 pieces



QPL1818

75 Ω 15 dB CATV Amplifier (50 – 1800MHz)

Absolute Maximum Ratings

Parameter	Rating
Supply Voltage (V_{DD})	+10 V
Supply Current (I_{DD})	140 mA
Maximum Input Level	+10 dBm
Operating Temperature Range	-40 to +100 °C
Storage Temperature Range	-65 to +150 °C
Maximum Junction Temperature	+150 °C

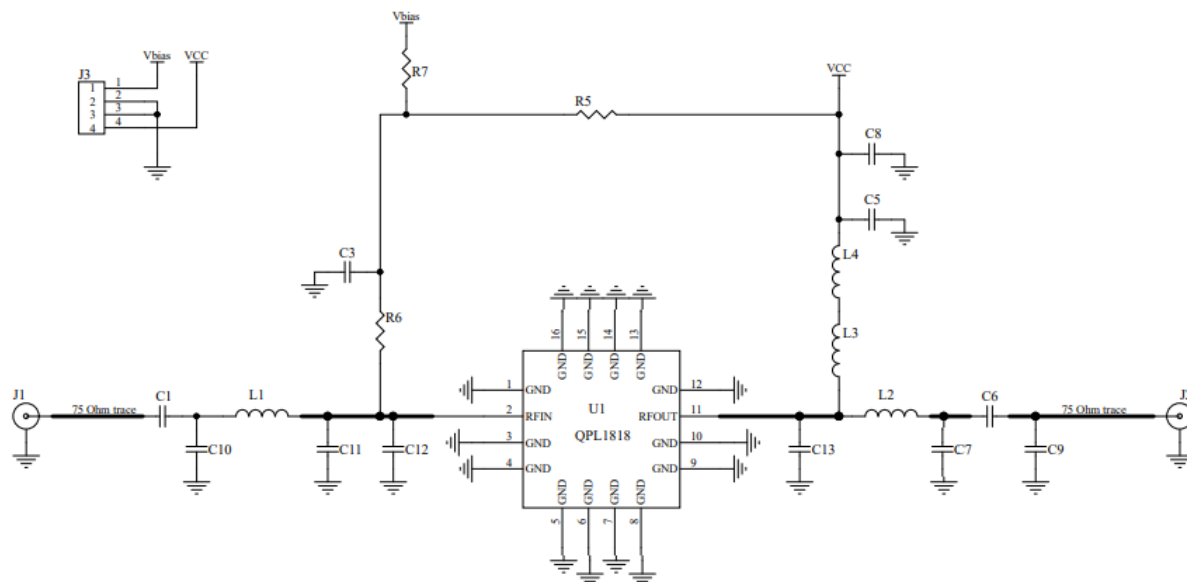
Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

Electrical Specifications at +5 V

Parameter	Condition ⁽¹⁾	Min	Typ	Max	Unit
Supply Voltage (V_{DD})			5		V
Supply Current (I_{DD})			100		mA
Frequency Range		50		1800	MHz
Gain			15		dB
Gain Slope			1		dB
Reverse Isolation			19		dB
Input Return Loss			20		dB
Output Return Loss			18		dB
CCN @ 261MHz	At +55dBmV Total Composite Output power. 108MHz to 1791MHz, 280Ch, SC-QAM, 0dB tilt, 6dB offset		56		dB
CCN @ 987MHz			50		
CCN @ 1773MHz			44		
Noise Figure			2		dB
OIP2L	+5 dBm / tone output		59		dBm
OIP2U	+5 dBm / tone output		45		dBm
OIP3	+5 dBm / tone output		37		dBm
OP1dB			19		dBm
Thermal Resistance	Θ_{JC}		60		°C/W

Note: Typical performance at these conditions: Temp = +25 °C, V_{DD} = +5 V, 75 Ω system, Full band unless otherwise noted

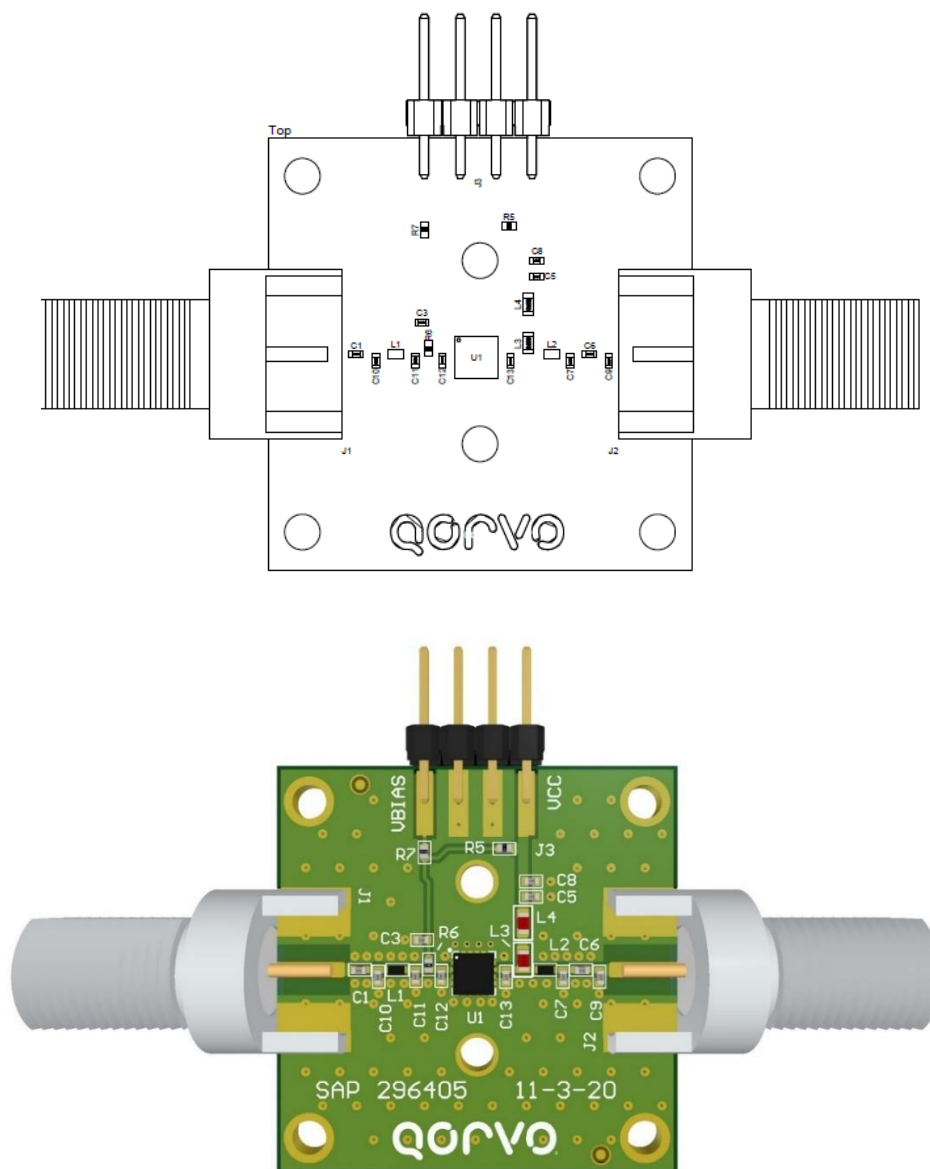
Evaluation Board Schematic 50 MHz – 1800 MHz



Evaluation Board Bill of Materials

Ref Des	Description	Manufacturer	Part Number
U1	1791MHz, Low Noise, High Linearity Amp	Qorvo	QPL1818
PCB	EVb PCB, QPL1818	Qorvo	SAP 296405
C1, C6	CAP, 270 pF, 5%, 0402	Murata	GCM1555C1H271JA16D
C8	CAP, 0.01uF, 10%, 0402	Murata	GCM155R71H103KA55D
L4	RES, 0 Ω , 0603	VISHAY AMERICAS INC	MCT06030Z0000ZP500
L1	IND, 2.6nH, +/-0.1nH, W/W, HI-Q, 0402	Murata	LQW15AN2N6B8ZD
L2	IND, 4.4nH, $\pm$ 0.1nH, W/W, HI-Q, 0402	Murata	LQW15AN4N4G80
C9	CAP, 0.5pF, +/-0.25pF, 50V, HI-Q	Murata	GJM1555C1HR50CB01D
C11	RES, 27 K OHM,5%,1/16W,0402, LEAD FREE	KOA Speer	RK73B1ETTP273J
C12	RES, 1M, 5%, 1/16W, 0402	Kamaya, Inc	RMC1/16S-105JTH
L3	IND, 2200nH, +/-20%, 500mA, 400MHz, 0603	Taiyo Yuden	BRL1608T2R2M
J1, J2	CONN, F FEM, 75 OHMS	Millimeter Wave	MW-846-C-DD-75
J3	CONN, HDR, ST, 4-PIN, 0.100"	Samtec	TSW-104-08-G-S
R5, R6, R7, C3, C5, C7, C10, C13	DNI		

Evaluation Board Assembly Drawing

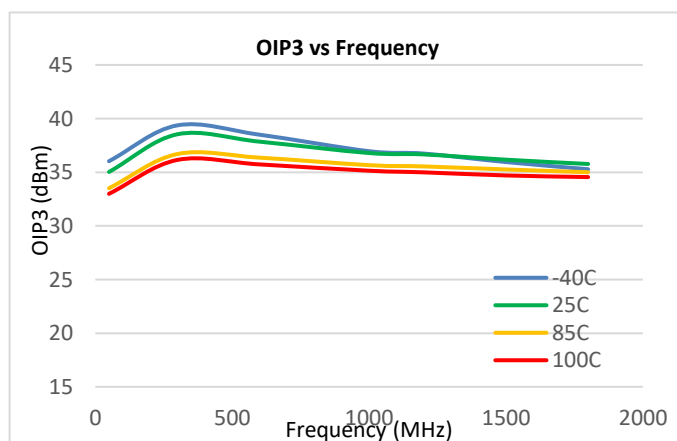
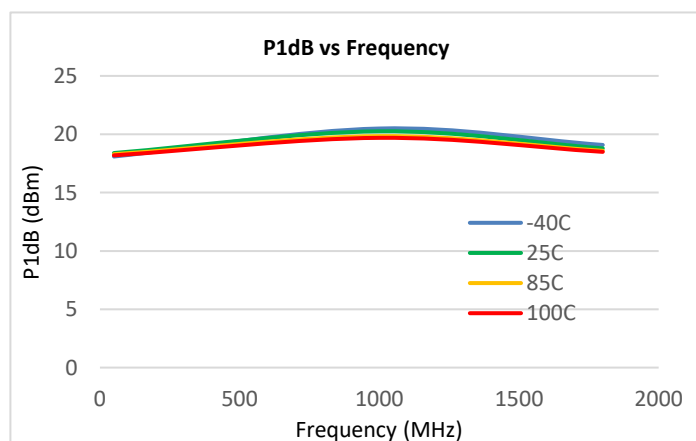
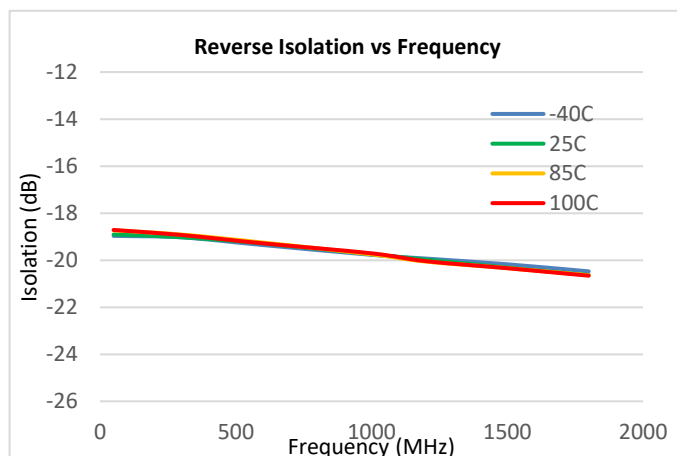
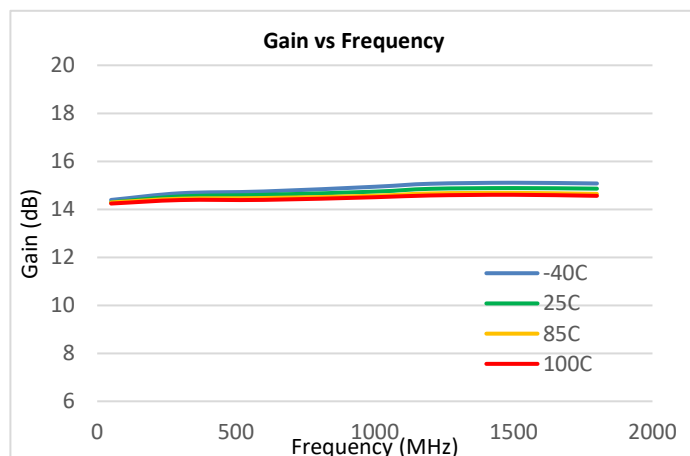
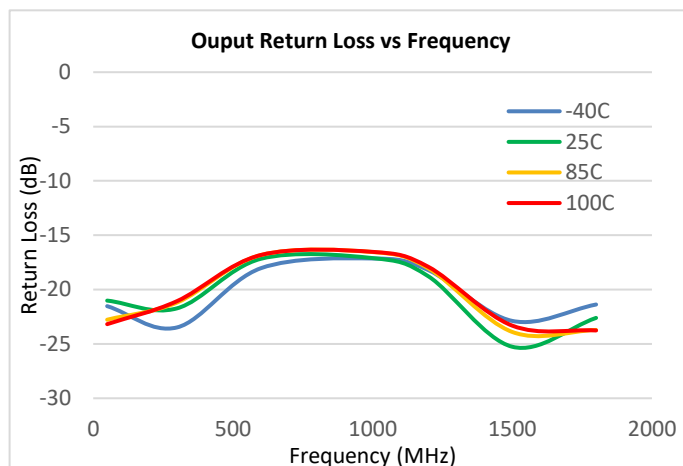
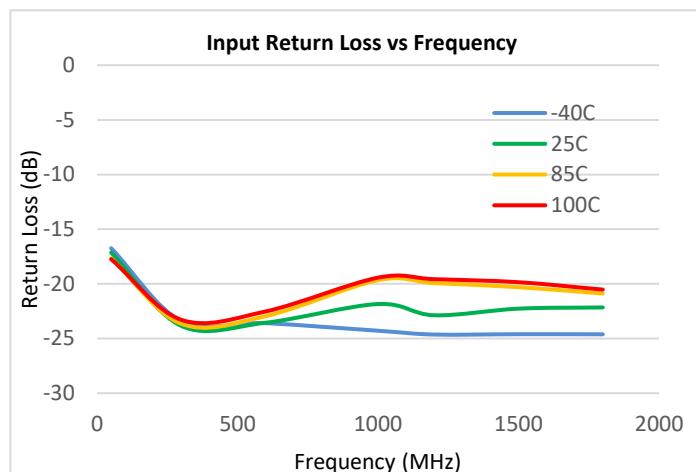


QPL1818 Evaluation Board

LAYER STACK LEGEND

	Material	Layer	Thickness	Dielectric Material
		Top Overlay		
	Surface Material	Top Solder	0.0010in	SM-001
	CF-004	Top Layer	0.0007in	
	Core		0.0580in	FR-4
	CF-004	Bottom Layer	0.0007in	
Total thickness: 0.0604				

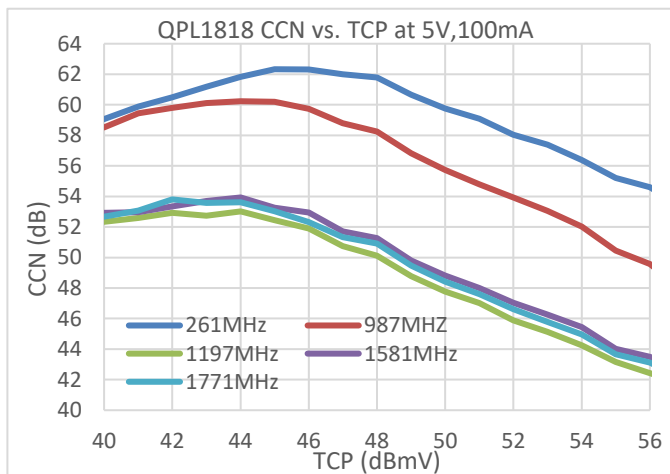
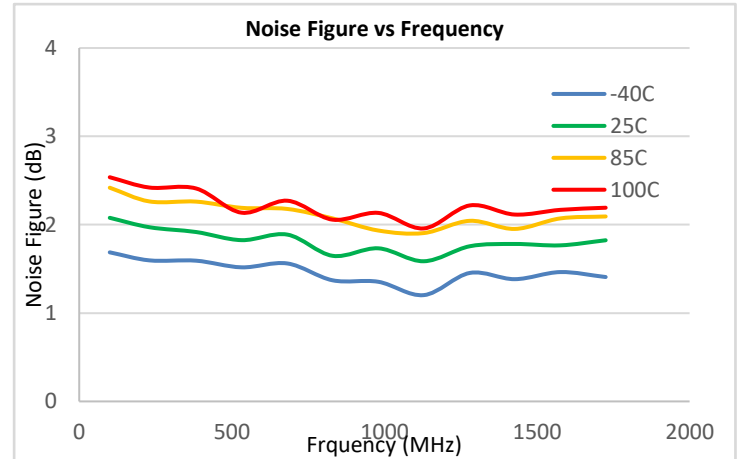
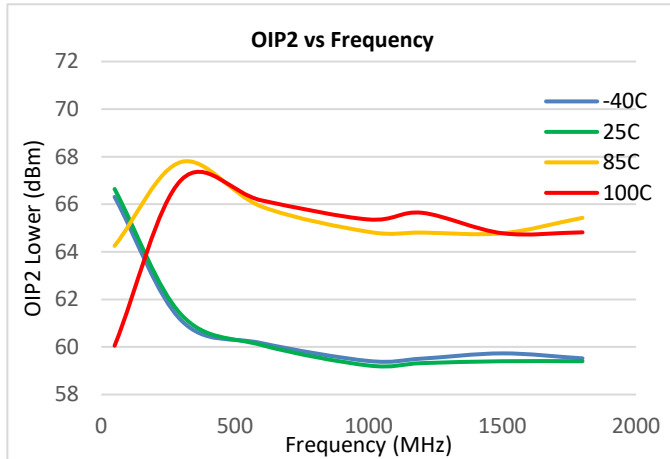
Performance Data at +5V



Notes: (1) OIP3: +5 dBm/tone output

Notes: (2) Test conditions unless otherwise noted: $V_{cc} = +5V$, Temp = +25C, $Z_o = 75\Omega$

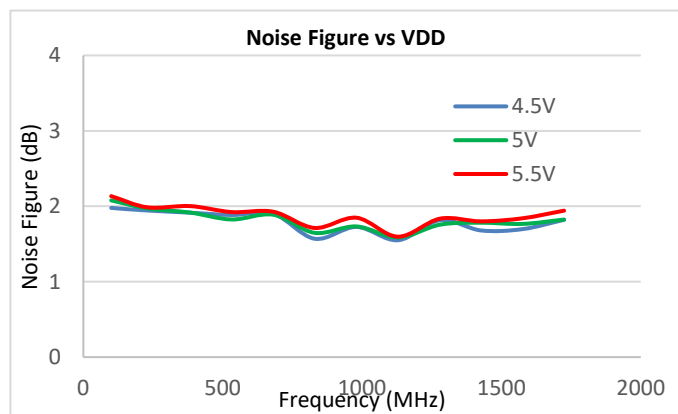
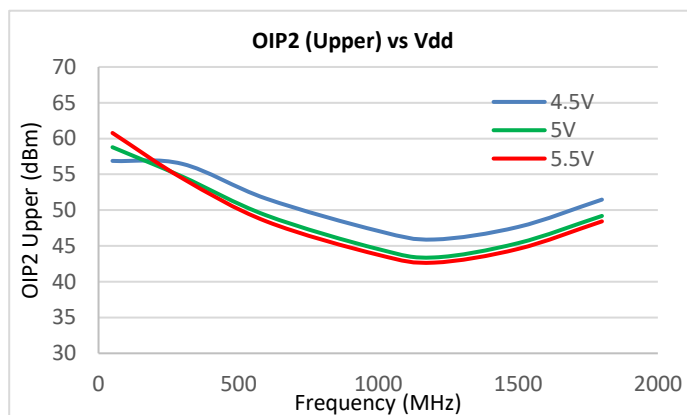
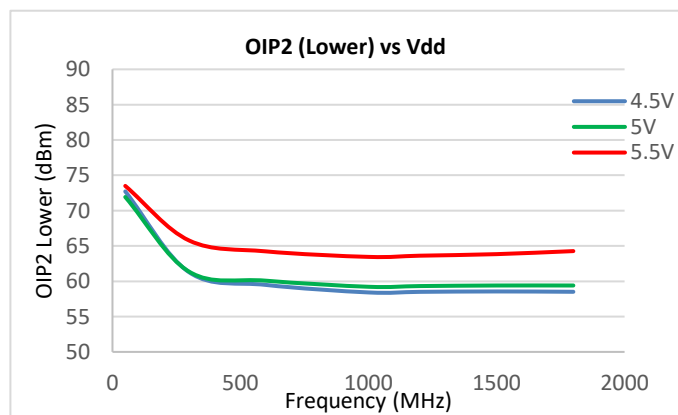
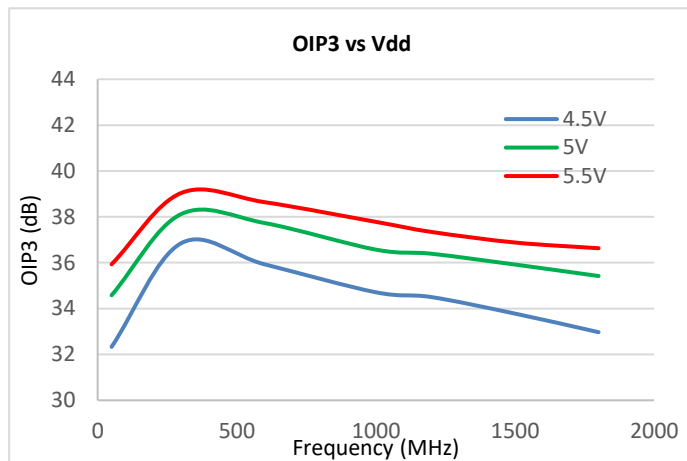
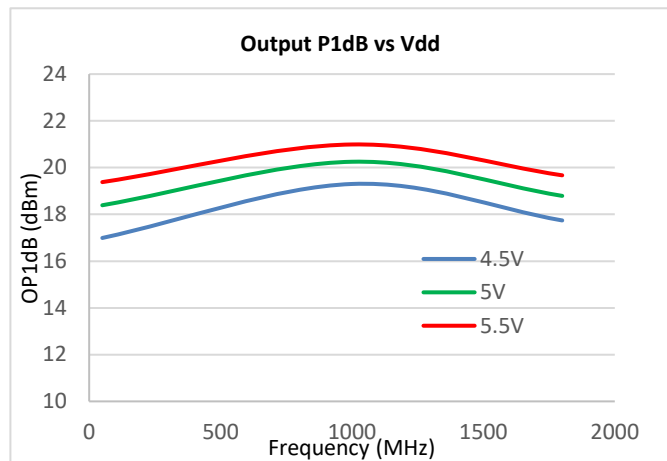
Performance Data at +5 V



Notes:

- (1) OIP2: +5 dBm / tone output
- (2) Notes: (2) Test conditions unless otherwise noted: $V_{cc} = +5V$, Temp = +25C, $Z_o = 75\Omega$
- (3) CCN 258-1794MHz 255ch SC-QAM. OdB tilt, 6dB stepdown @ 1026MHz

Performance Data vs Supply Voltage

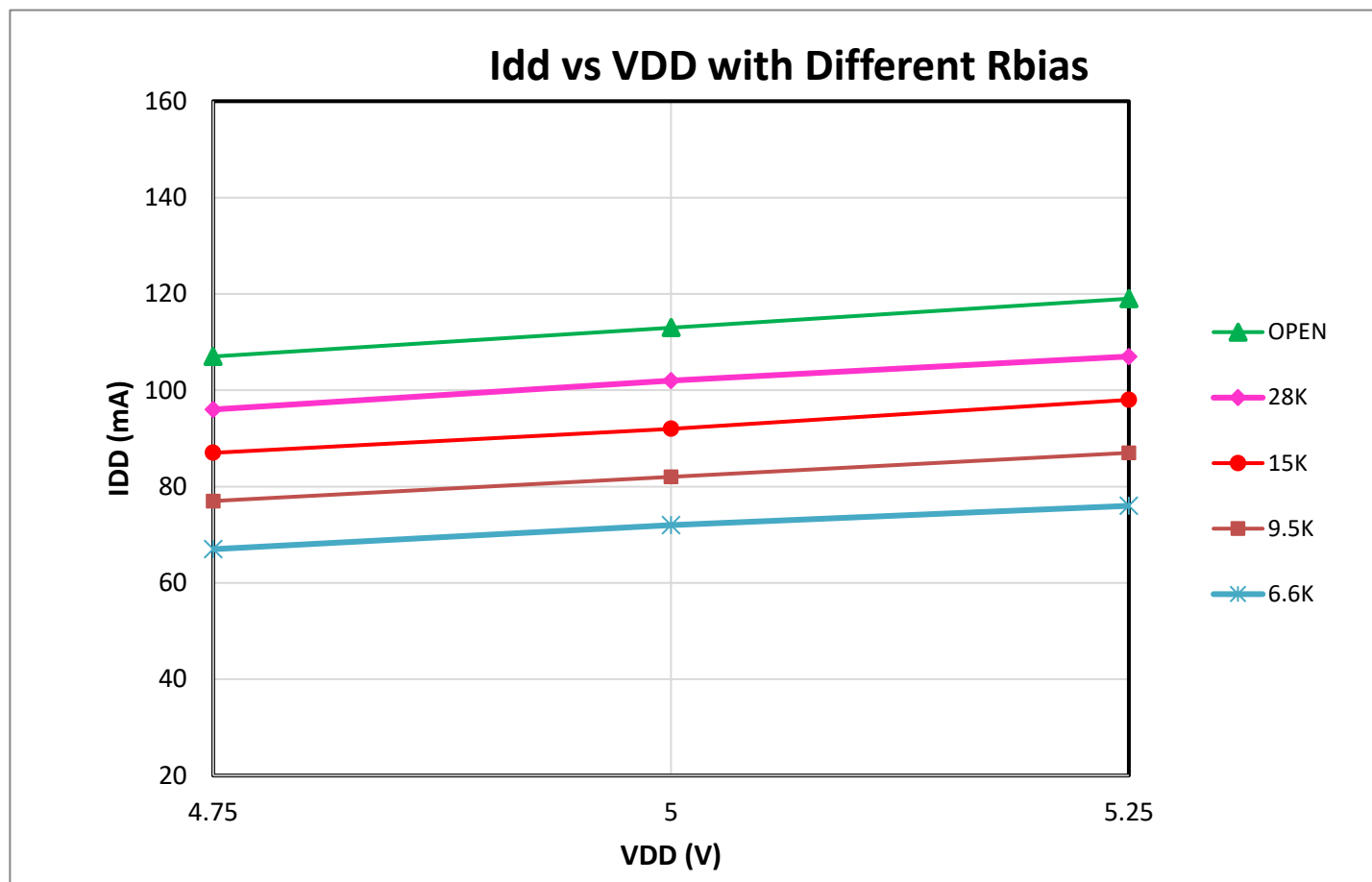


Notes: (1) OIP3: 5 dBm /tone output

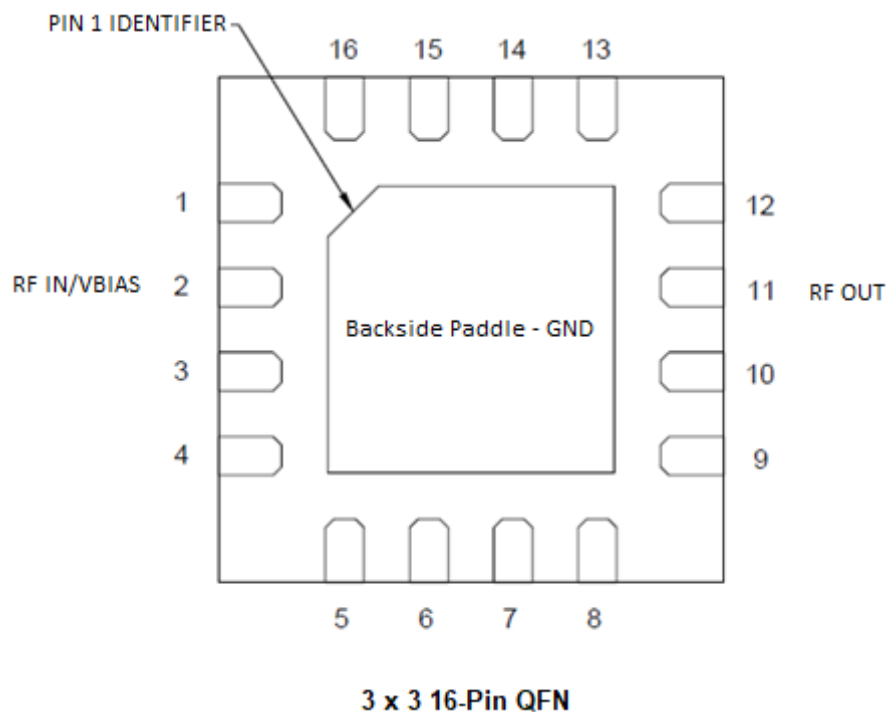
(2) OIP2: 5 dBm /tone output

Adjusting Device Current with R_{bias}

For normal operation Resistors R5, R6, and R7 are not populated. To reduce the device current, connect Vbias to GND, populate R7 with a zero Ohm Resistor, leave R5 as DNI, and vary the value of R6 (R_{bias}). The resulting I_{dd} with variation of R_{bias} is shown the graph below.



Pin Configuration and Description



Pin Number	Label	Description
1	GND	
2	RF IN / VBIAS	RF Input, DC blocking capacitor required
3	GND	
4	GND	
5	GND	
6	GND	
7	GND	
8	GND	
9	GND	
10	GND	
11	RF OUT	RF Output
12	GND	
13	GND	
14	GND	
15	GND	
16	GND	
Backside Paddle	GND	RF/DC/Thermal/Ground. Minimize the inductance and thermal resistance.

Technical drawing of a 16-pin connector showing three views: front, side, and top.

Front View (Left): A square component with a side length of 3.00 ± 0.10 . A feature labeled "PIN 1" is located at the top-left corner.

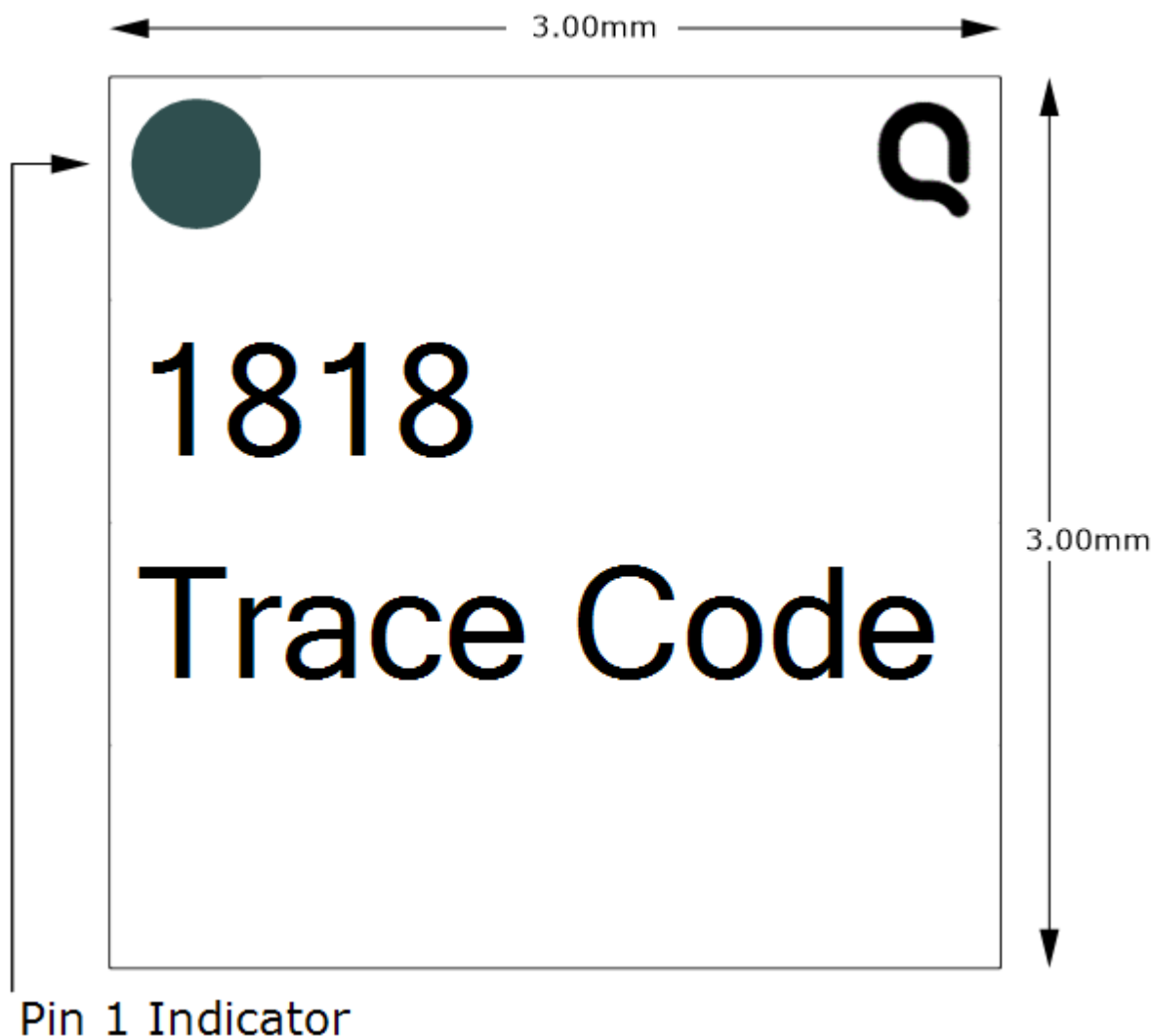
Side View (Middle): Shows the profile of the component. The total width is 0.85 ± 0.05 (0.203). The mounting holes are positioned at $+0.05/-0.00$ from the top edge. A vertical line indicates the "SEATING PLANE".

Top View (Right): A square component with a side length of 1.70 ± 0.10 . The mounting holes are arranged in a grid. The distance between the center of the holes is $16 \times 0.25 \pm 0.05$. The distance from the edge to the center of the holes is $16 \times 0.30 \pm 0.05$. A chamfer is specified as "PIN #1 IDENTIFICATION CHAMFER 0.300x45°".

Notes:

1. Dimensions in millimeters

Package Marking



Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	500V, 1B	ANSI / ESDA / JEDEC JS-001
ESD – Charged Device Model (CDM)	2000V, C3	ANSI / ESDA / JEDEC JS-002
MSL – Moisture Sensitivity Level	MSL 1	IPC / JEDEC J-STD-020



Caution!
ESD-Sensitive Device

Solderability

Compatible with both lead-free (260 °C max. reflow temp.) and tin / lead (245 °C max. reflow temp.) soldering processes.
Solder profiles available upon request.

Contact plating: NiPdAu

RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- PFOS Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- SVHC Free



Contact Information

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Web: www.qorvo.com

Email: customer.support@qorvo.com

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