

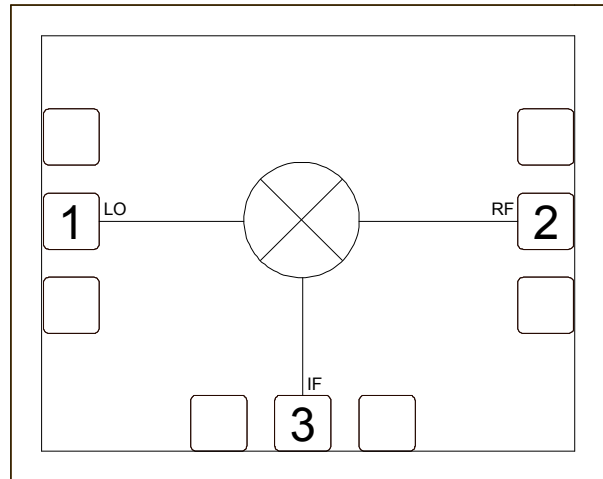
### Features

- ▶ Low conversion loss
- ▶ High isolation
- ▶ Ultra wide IF bandwidth
- ▶ Passive double balanced topology
- ▶ Small die size

### Description

The CMD261 is a general purpose double balanced mixer die with ultra wide IF bandwidth that can be used for up- and downconverting applications between 30 and 46 GHz. The CMD261 has very high isolation to both the RF and IF ports due to the optimized balun structures, and can operate with an LO drive level as low as +15 dBm. The CMD261 can easily be configured as an image reject mixer or single sideband modulator with external hybrids and power splitters.

### Functional Block Diagram



### Electrical Performance – IF = 12 GHz USB, LO = +19 dBm, T<sub>A</sub> = 25 °C, RF = 36 GHz

Parameter	Min	Typ	Max	Units
Frequency Range, RF	30 – 46			GHz
Frequency Range, LO	16 – 31			GHz
Frequency Range, IF	5		20	GHz
Conversion Loss		8		dB
LO to RF Isolation		30		dB
LO to IF Isolation		20		dB
RF to IF Isolation		23		dB
Input IP3		+21		dBm

Unless otherwise noted, all measurements performed as a downconverter, IF = 12 GHz USB

### Specifications

#### Absolute Maximum Ratings

Parameter	Rating
RF / IF Input Power	+24 dBm
LO Drive	+24 dBm
Operating Temperature	-55 to 85 °C
Storage Temperature	-55 to 150 °C
Thermal Resistance, $\Theta_{JC}$	271.9 °C / W
Power Dissipation, P <sub>diss</sub>	240 mW

Exceeding any one or combination of the maximum ratings may cause permanent damage to the device.

#### Electrical Specifications – IF = 12 GHz, LO = +19 dBm, T<sub>A</sub> = 25 °C

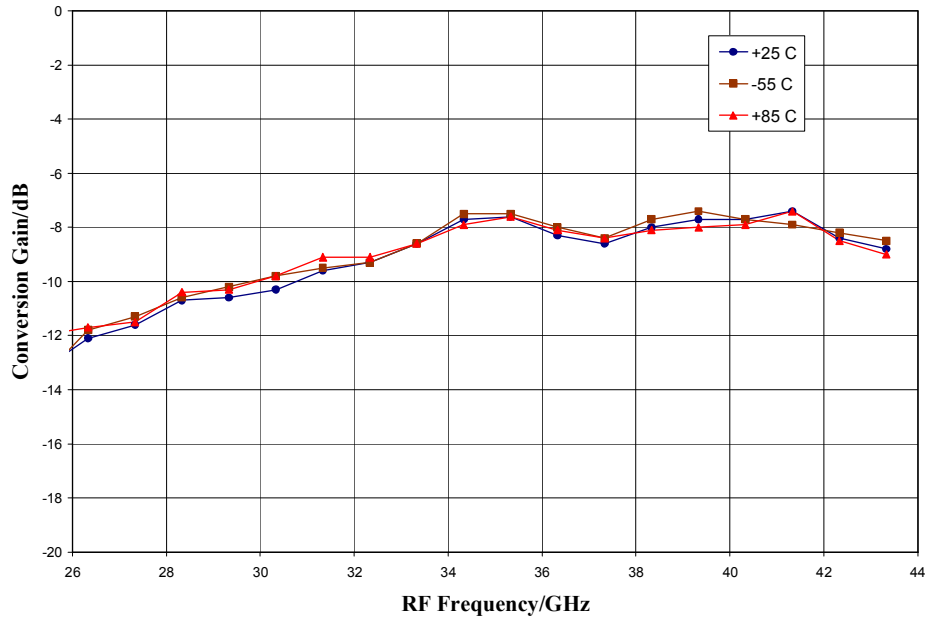
Parameter	Min	Typ	Max	Units
Frequency Range, RF	30 – 46			GHz
Frequency Range, LO	16 – 31			GHz
Frequency Range, IF	5		20	GHz
Conversion Loss		8	11	dB
Noise Figure (SSB)		8	11	dB
LO to RF Isolation	30	35		dB
LO to IF Isolation	13	20		dB
RF to IF Isolation	17	25		dB
Input IP3	17	21		dBm

Unless otherwise noted, all measurements performed as a downconverter, IF = 12 GHz

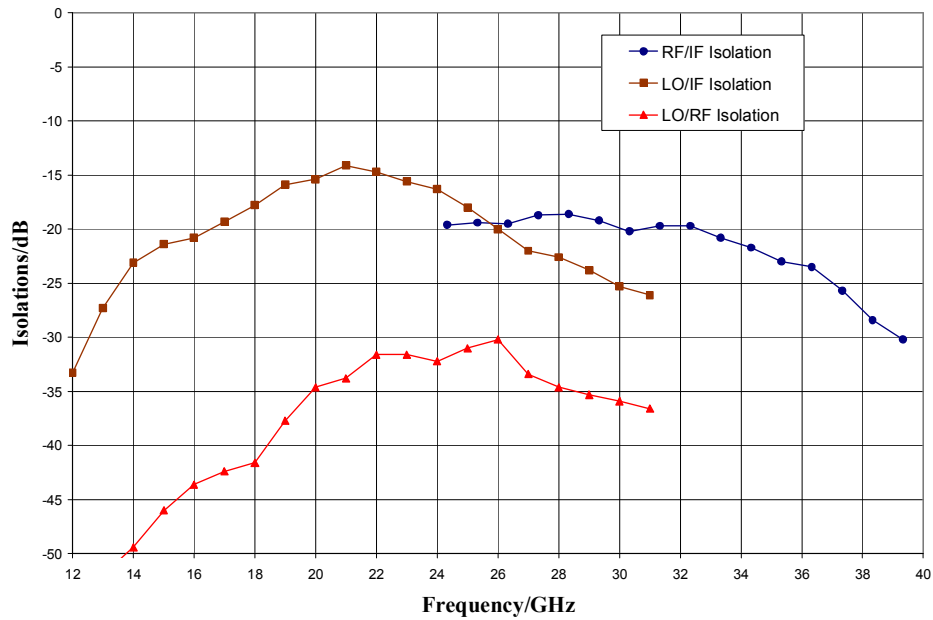
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### Typical Performance

Conversion Gain vs. Temperature, LO = +19 dBm, IF = 12 GHz USB



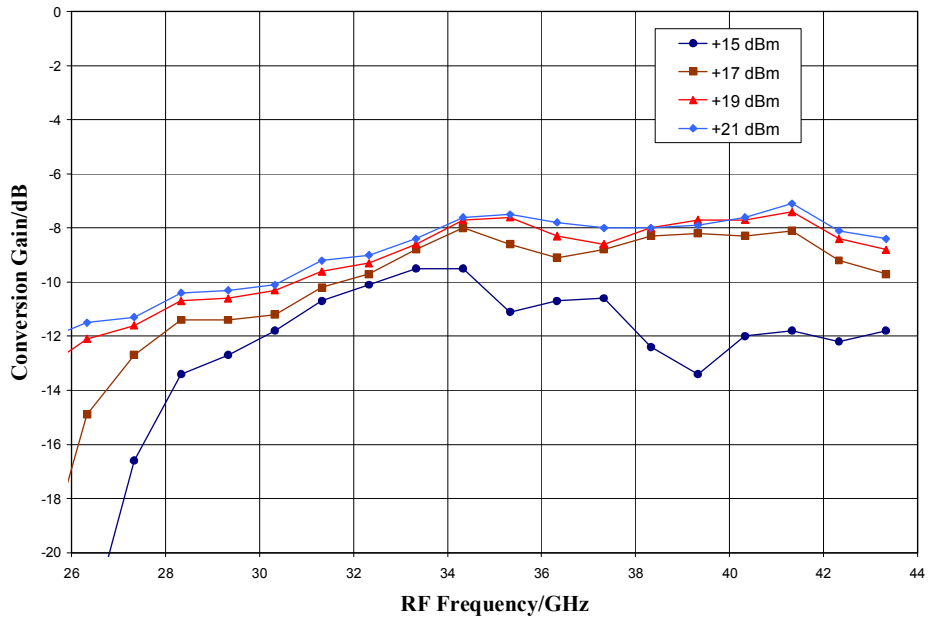
Isolations, LO = +19 dBm



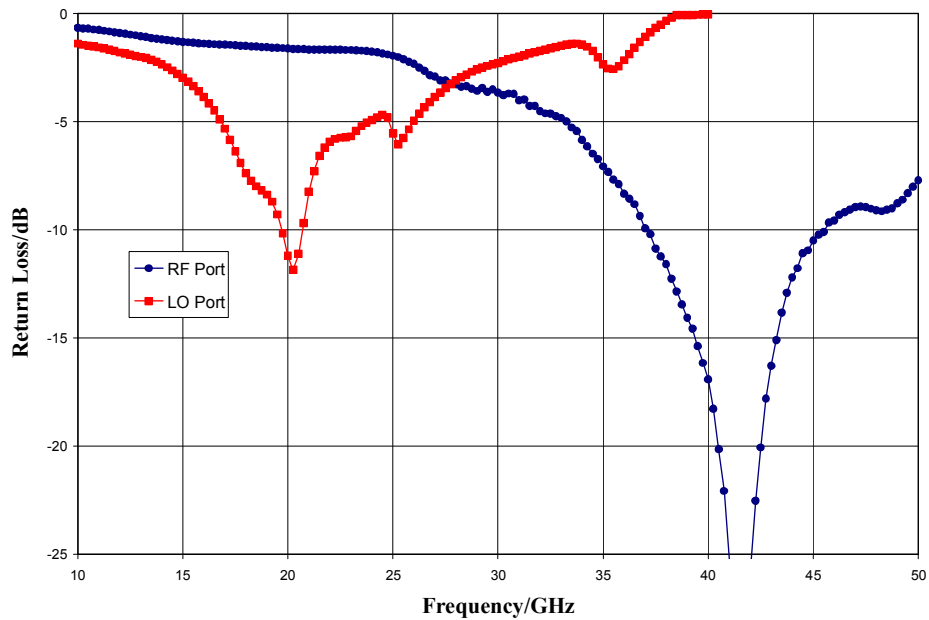
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### Typical Performance

#### Conversion Gain vs. LO Drive, IF = 12 GHz USB



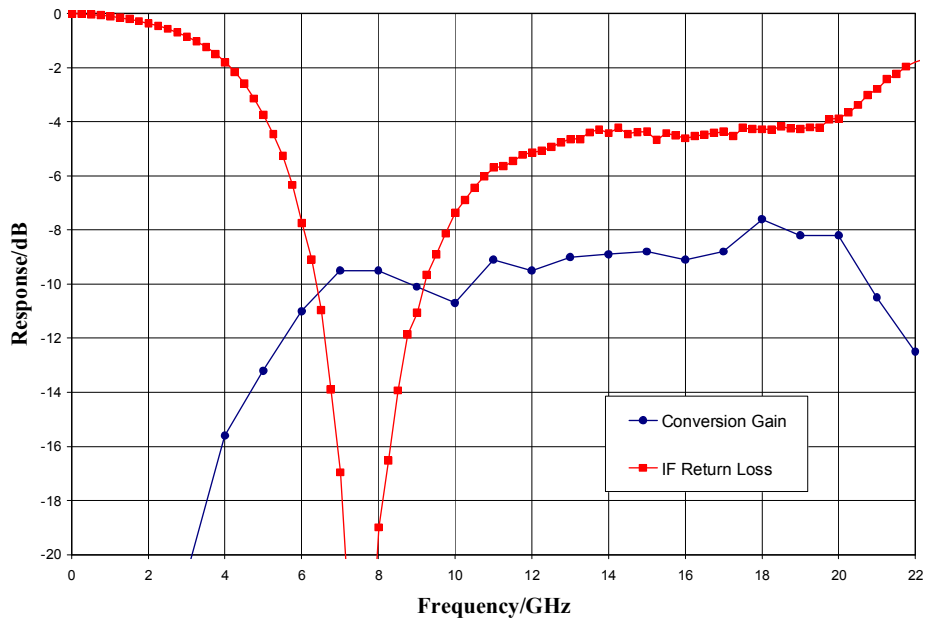
#### Return Loss, LO = + 21 dBm



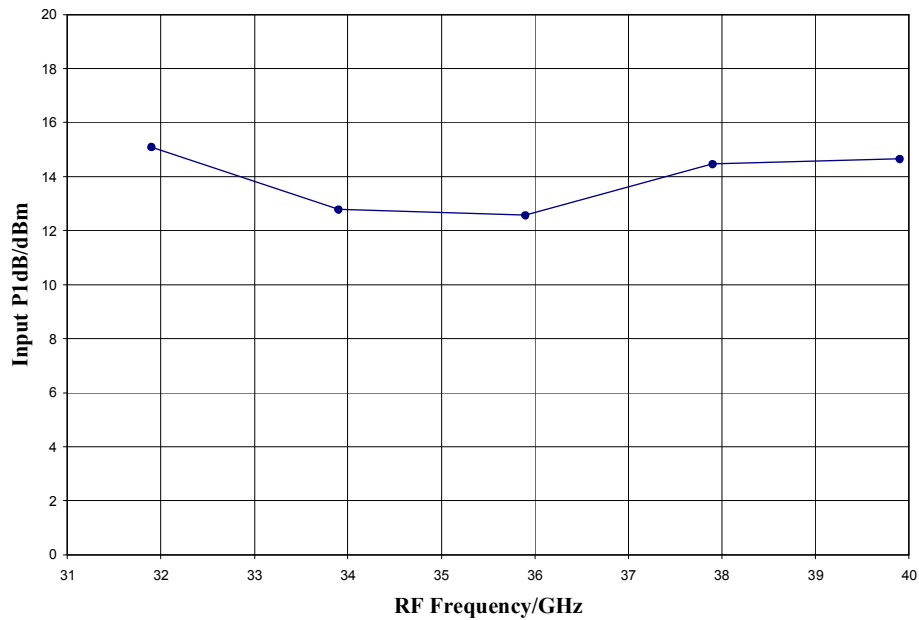
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### Typical Performance

**IF Bandwidth, LO = +17 dBm**



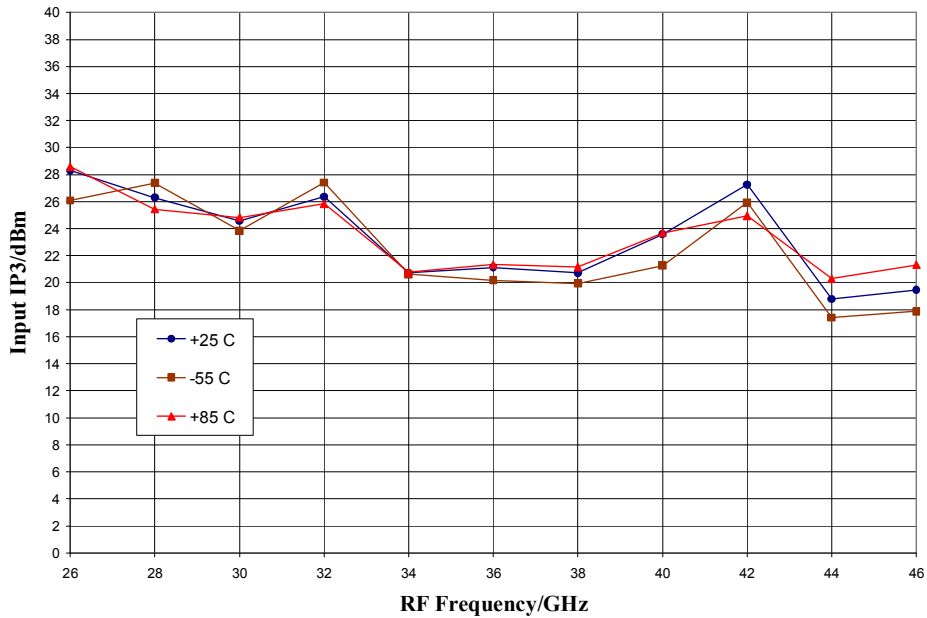
**Input P1dB, LO=+19 dBm, IF = 12 GHz USB**



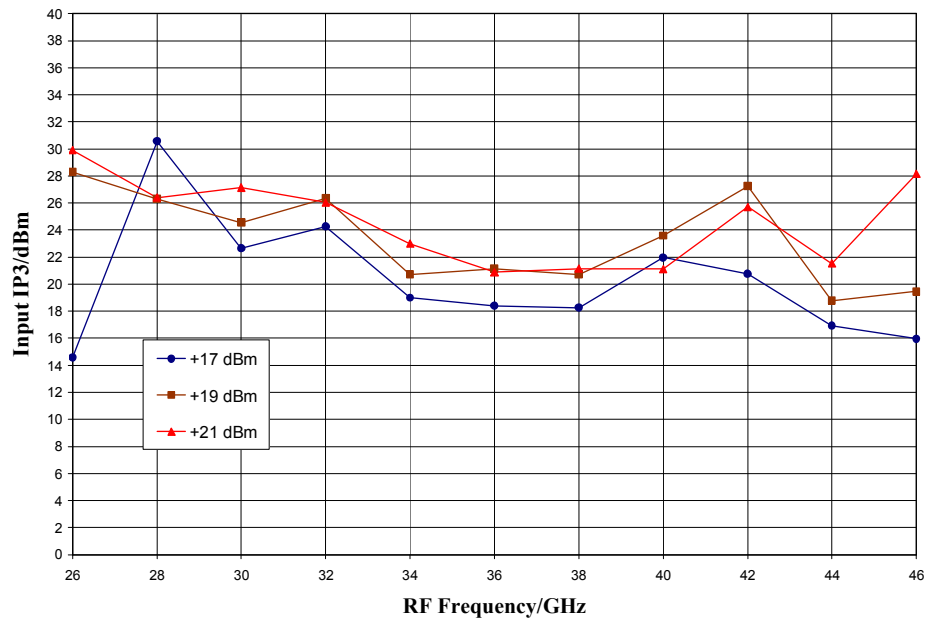
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### Typical Performance

**Input IP3 vs. Temperature, LO = +19 dBm, IF = 12 GHz USB**



**Input IP3 vs. LO Drive, IF = 12 GHz USB**



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### Typical Performance

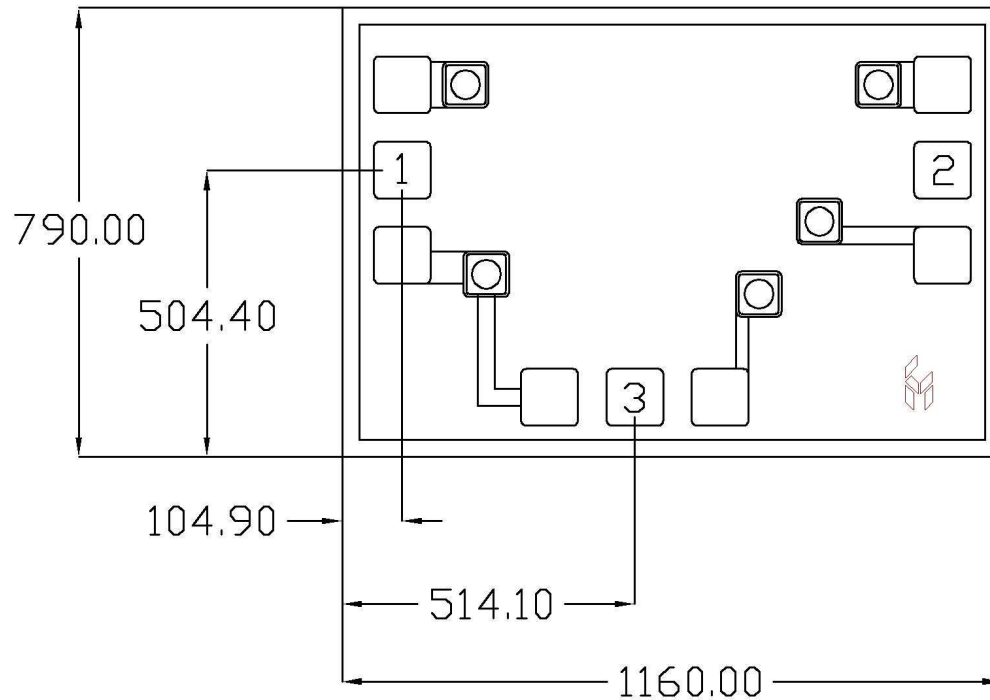
#### M x N Spur Table

	nLO				
mRF	0	1	2	3	4
0	xx	-20			
1	17	0	31	48	
2			68	66	64
3				> 80	> 80
4					

RF = 36.1 GHz @ -10 dBm  
 LO = 24.0 GHz @ +21 dBm  
 All values in dBc below the IF output power level (1RF - 1LO)

## Mechanical Information

### Die Outline (all dimensions in microns)



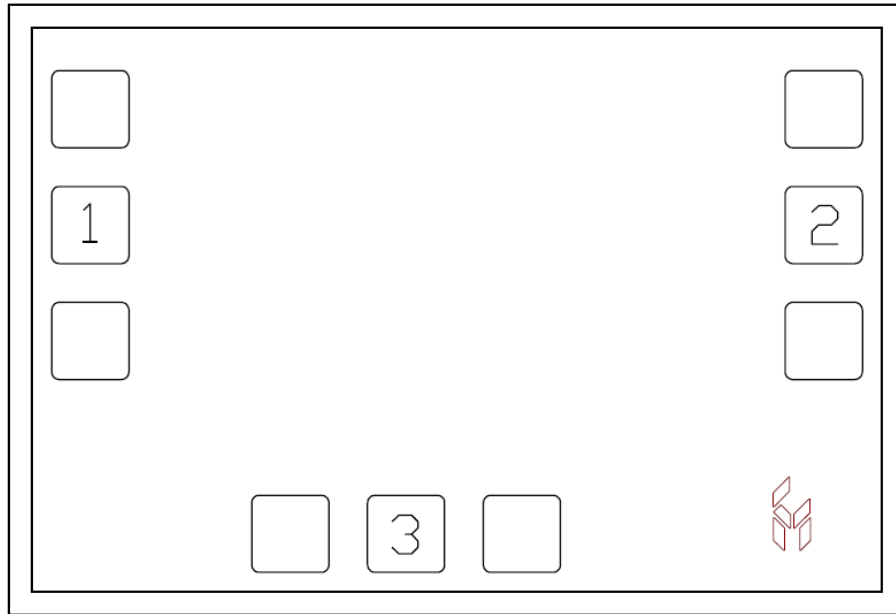
#### Notes:

1. No connection required for unlabeled pads
2. Backside is RF and DC ground
3. Backside and bond pad metal: Gold
4. Die is 100 microns thick
5. All bond pads (1, 2, and 3) are 100 x 100 microns square

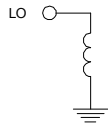
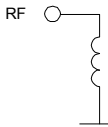
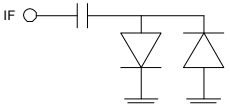



### Pin Description

### Pad Diagram



### Functional Description

Pin	Function	Description	Schematic
1	LO	This pin is DC coupled and matched to 50 ohms.	
2	RF	This pin is DC coupled and matched to 50 ohms.	
3	IF	This pin is AC coupled and matched to 50 Ohms. Operation to DC is not possible.	
Backside	Ground	Connect to RF / DC ground.	

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### Applications Information

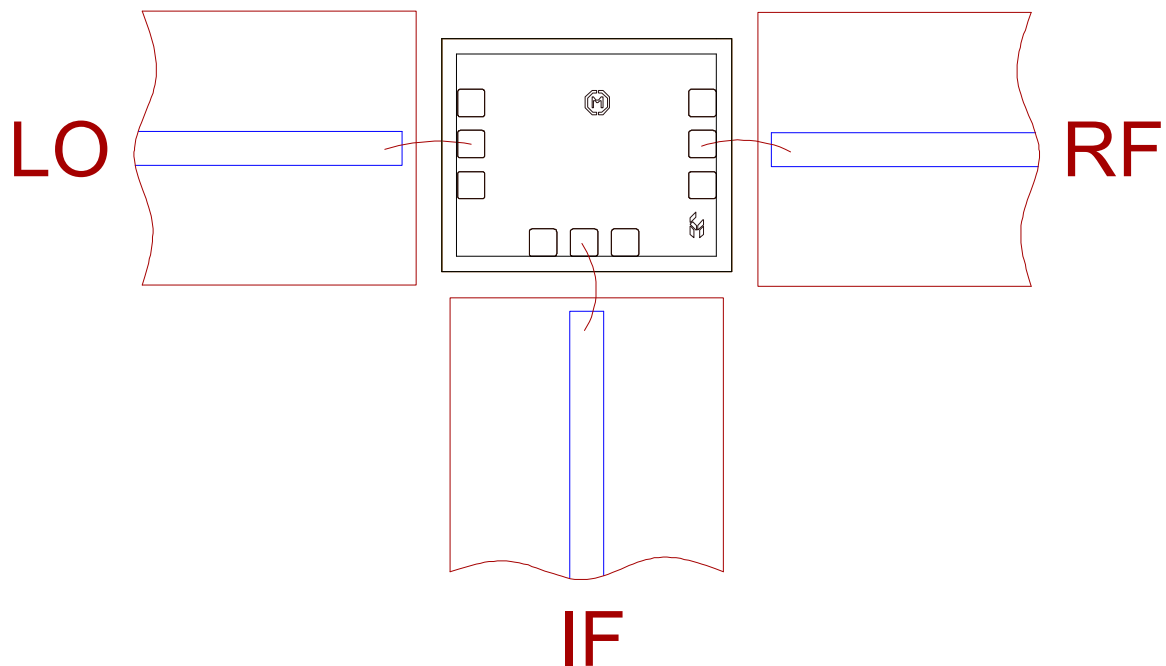
#### Assembly Guidelines

The backside of the CMD261 is RF ground. Die attach should be accomplished with electrically and thermally conductive epoxy only. Eutectic attach is not recommended. Standard assembly procedures should be followed for high frequency devices. The top surface of the semiconductor should be made planar to the adjacent RF transmission lines.

RF connections should be made as short as possible to reduce the inductive effect of the bond wire. Use of a 0.8 mil thermosonic wedge bonding is highly recommended as the loop height will be minimized.

The semiconductor is 100 um thick and should be handled by the sides of the die or with a custom collet. Do not make contact directly with the die surface as this will damage the monolithic circuitry. Handle with care.

#### Assembly Diagram



**GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.**

*Please note, all information contained in this data sheet is subject to change without notice.*

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