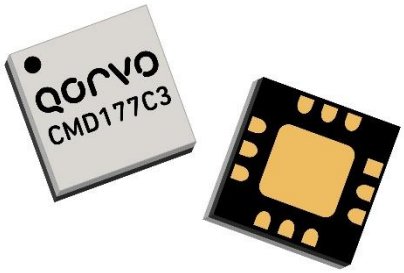
CMD177C3

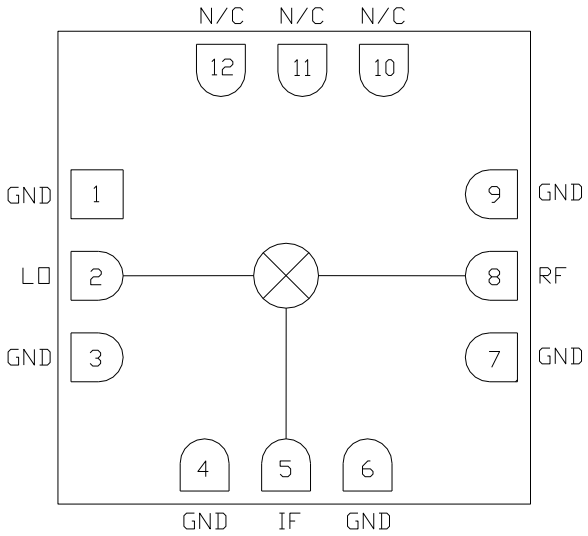
6-14 GHz Fundamental Mixer

Product Overview

The CMD177C3 is a general purpose double balanced mixer in a leadless surface mount package that can be used for up- and downconverting applications between 6 and 14 GHz. The CMD177C3 has very high isolation to both the RF and IF ports due to the optimized balun structures, and can operate with an LO drive level as low as +9 dBm. The CMD177C3 can easily be configured as an image reject mixer or single sideband modulator with external hybrids and power splitters.



Functional Block Diagram



Key Features

- Low Conversion Loss
- High Isolation
- Wide IF Bandwidth
- Passive Double Balanced Topology
- Pb-Free RoHs Compliant 3x3 SMT Package

Ordering Information

Part No.	Description
CMD177C3	100 pcs on 7” reel

Electrical Performance (IF = 100 MHz, LO = +13 dBm, T_A = 25 °C, F = 10 GHz)

Parameter	Min	Typ	Max	Units
Frequency Range, RF & LO		6 - 14		GHz
Frequency Range, IF	DC		5	GHz
Conversion Loss		6.5		dB
LO to RF Isolation		43		dB
LO to IF Isolation		37		dB
RF to IF Isolation		22		dB
Input P1dB		9.5		dBm

Unless otherwise noted, all measurements performed as a downconverter, IF = 100 MHz

Absolute Maximum Ratings

Parameter	Rating
RF / IF Input Power	+23 dBm
LO Drive	+23 dBm
Operating Temperature	-40 to 85 °C
Storage Temperature	-55 to 150 °C
Thermal Resistance, Q_{JC}	345.4 ºC/W
Power Dissipation, Pdiss	188 mW

Exceeding any one or combination of the maximum ratings may cause permanent damage to the device.

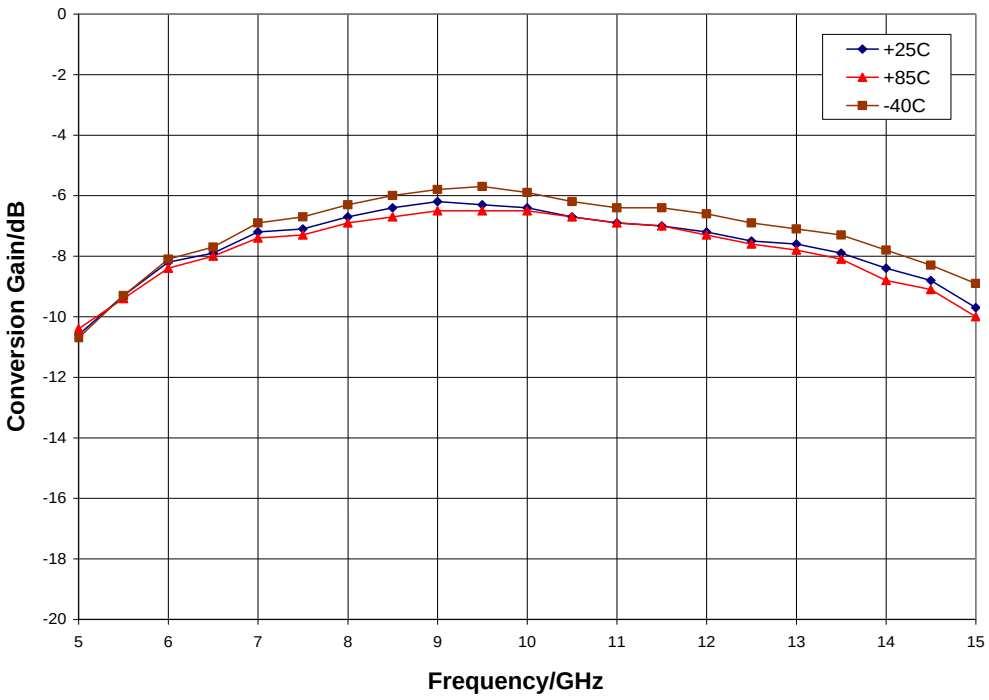
Electrical Specifications (IF = 100 MHz, LO = +13 dBm, T_A = 25º C)

Parameter	Min	Typ	Max	Min	Typ	Max	Units
Frequency Range, RF & LO		8 - 12			6 - 14		GHz
Frequency Range, IF	DC		5	DC		5	GHz
Conversion Loss		6.5	8		7	10	dB
Noise Figure (SSB)		6.5	8		7	10	dB
LO to RF Isolation	37	43		34	43		dB
LO to IF Isolation	28	37		28	37		dB
RF to IF Isolation	15	22		12	22		dB
Input P1dB		9.5			9.5		dBm
Input IP3		16			16		dBm

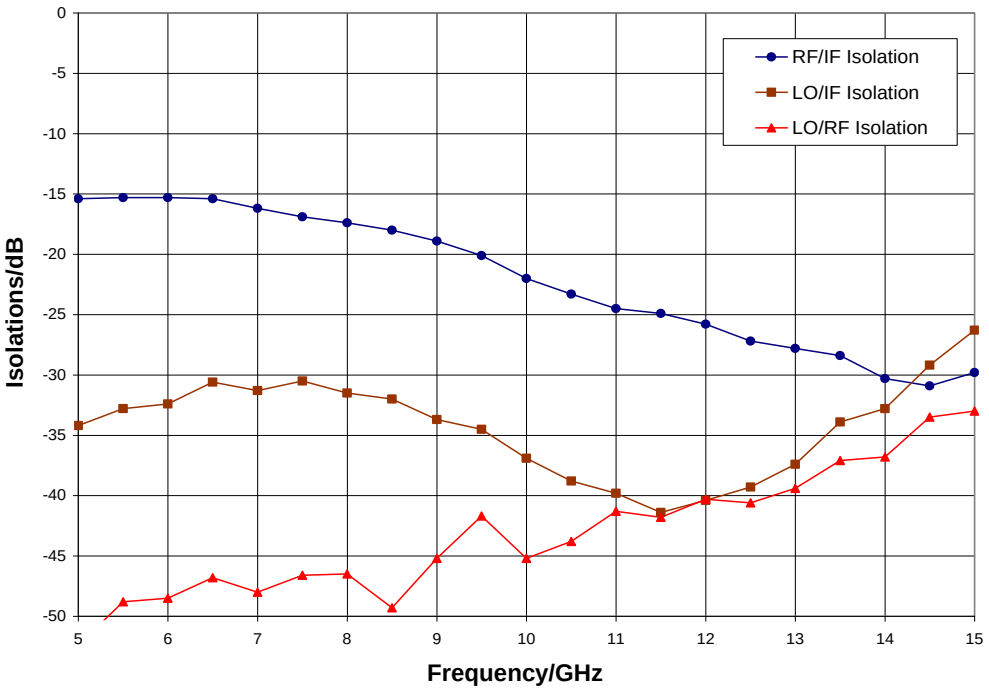
Unless otherwise noted, all measurements performed as a downconverter, IF = 100 MHz

Typical Performance

Conversion Gain vs. Temperature, LO = +13 dBm, IF = 100 MHz USB

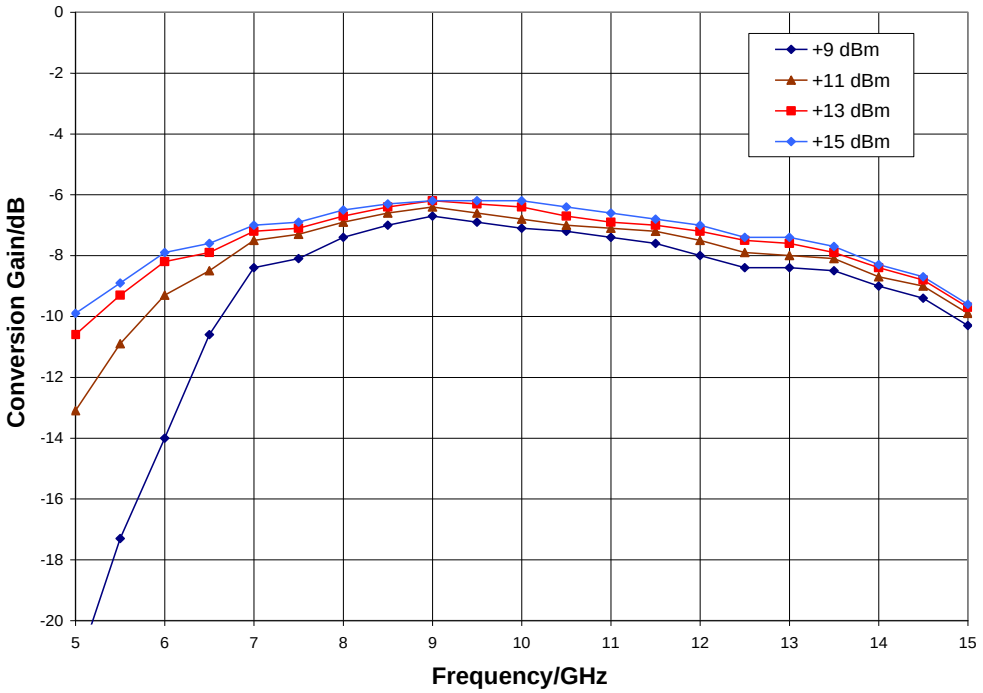


Isolation, LO = +13 dBm

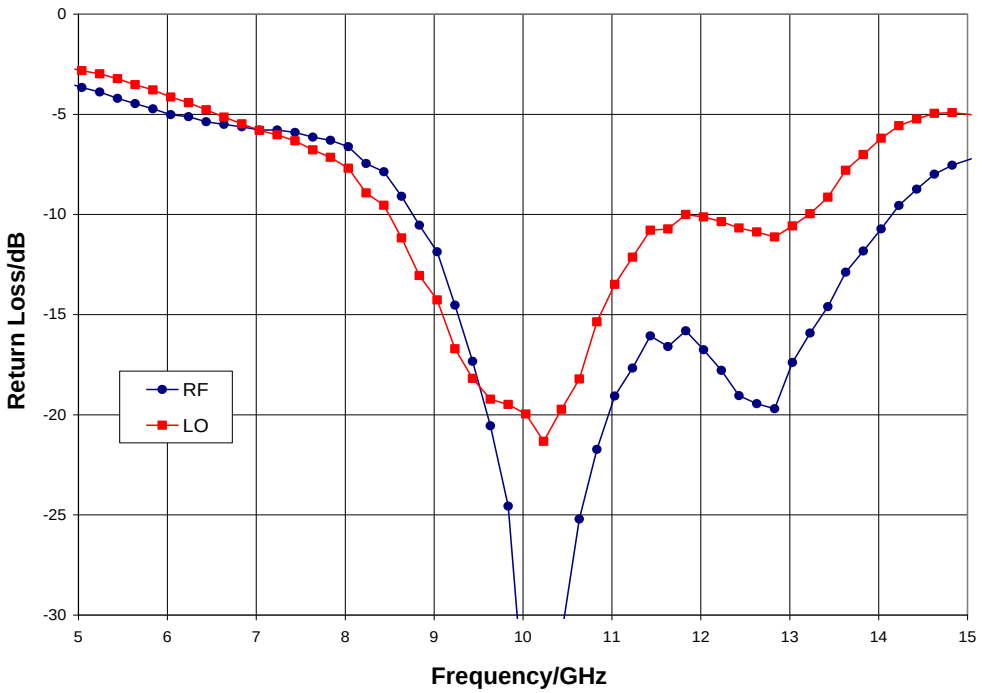


Typical Performance

Conversion Gain vs. LO Drive, IF = 100 MHz USB

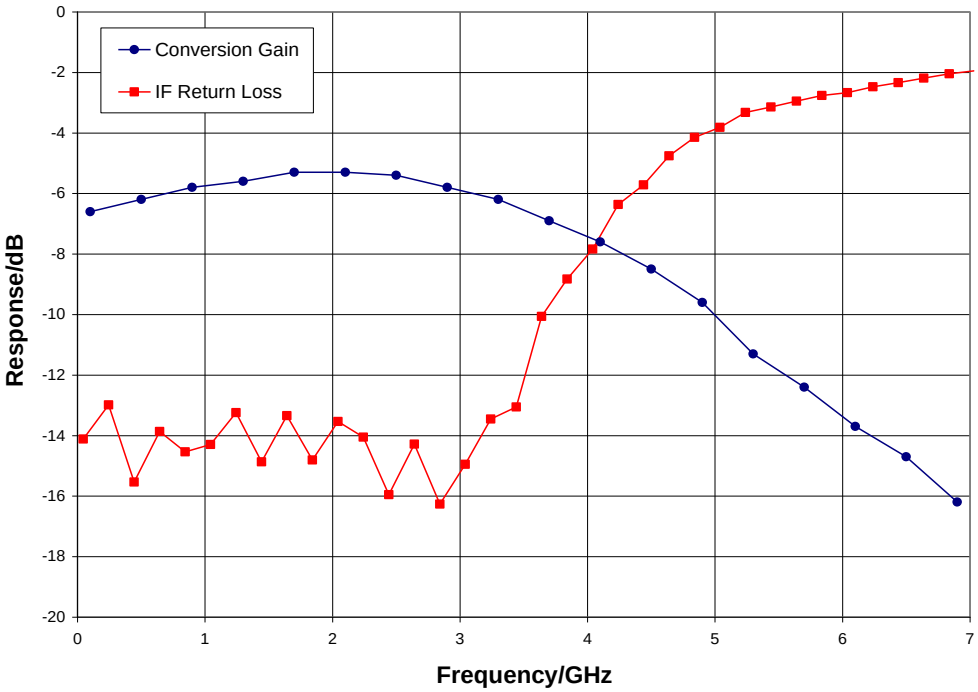


Return Loss, LO = + 13 dBm

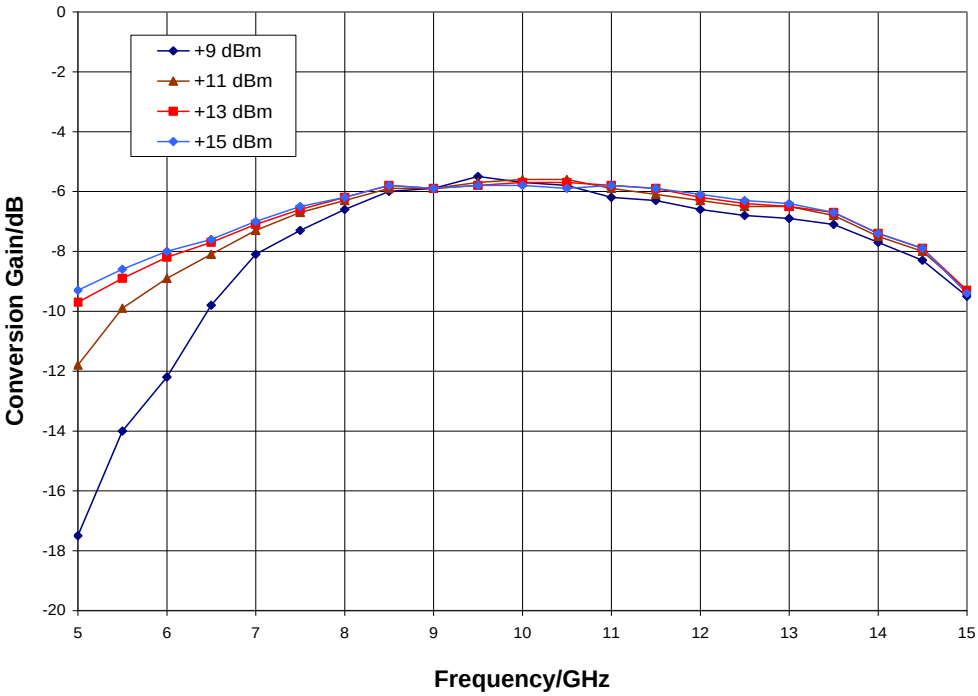


Typical Performance

IF Bandwidth, LO = +13 dBm

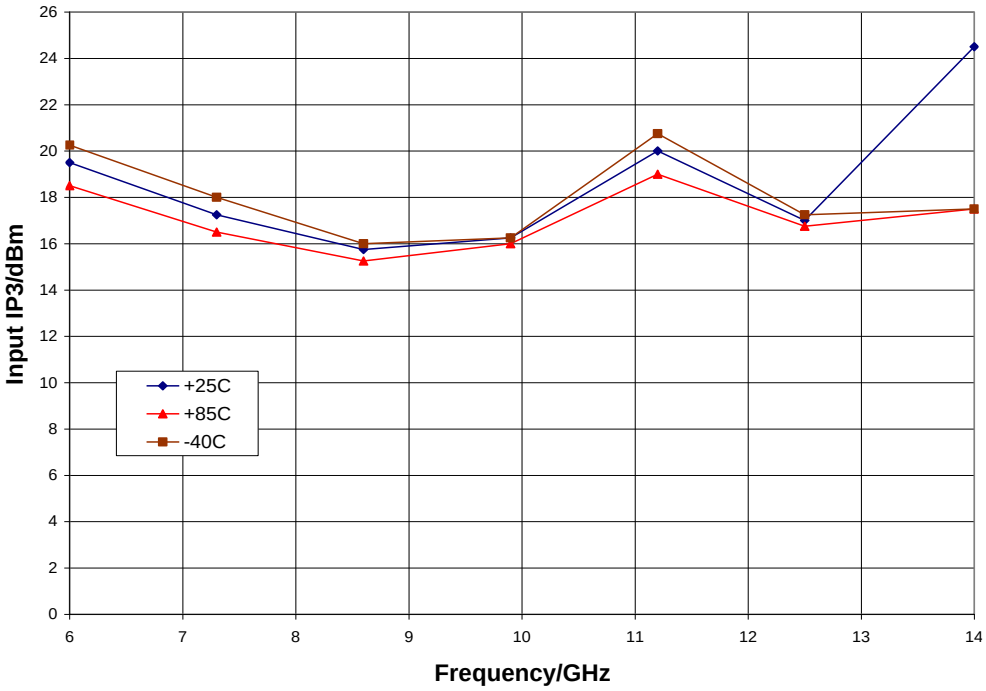


Upconverter Performance, Conversion Gain vs. LO Drive, IF Input = 100 MHz

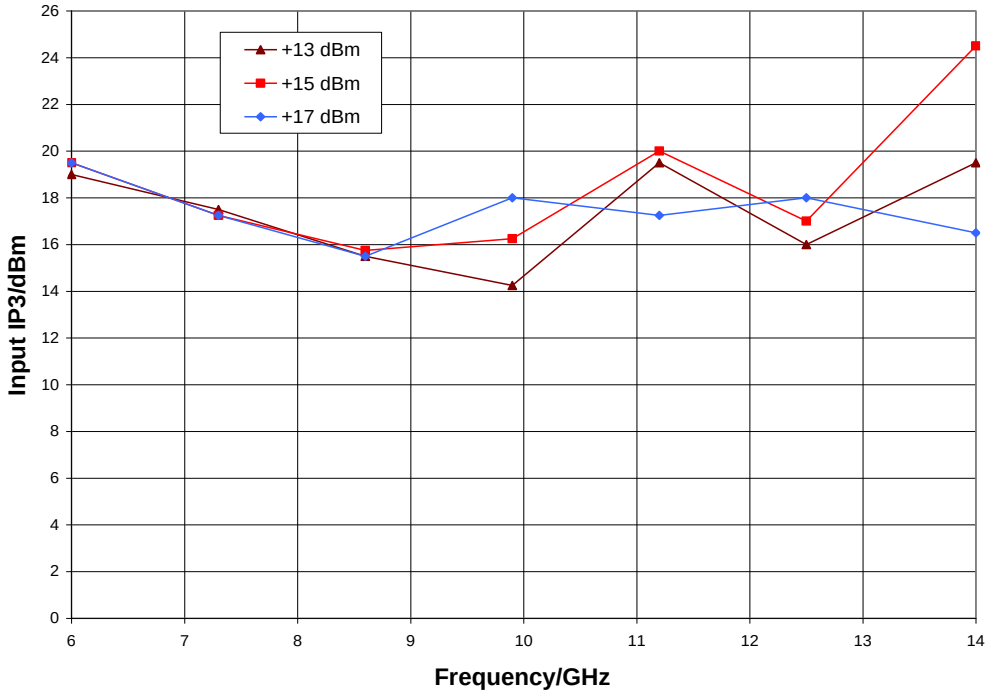


Typical Performance

Input IP3 vs. Temperature, LO = +15 dBm, IF = 100 MHz

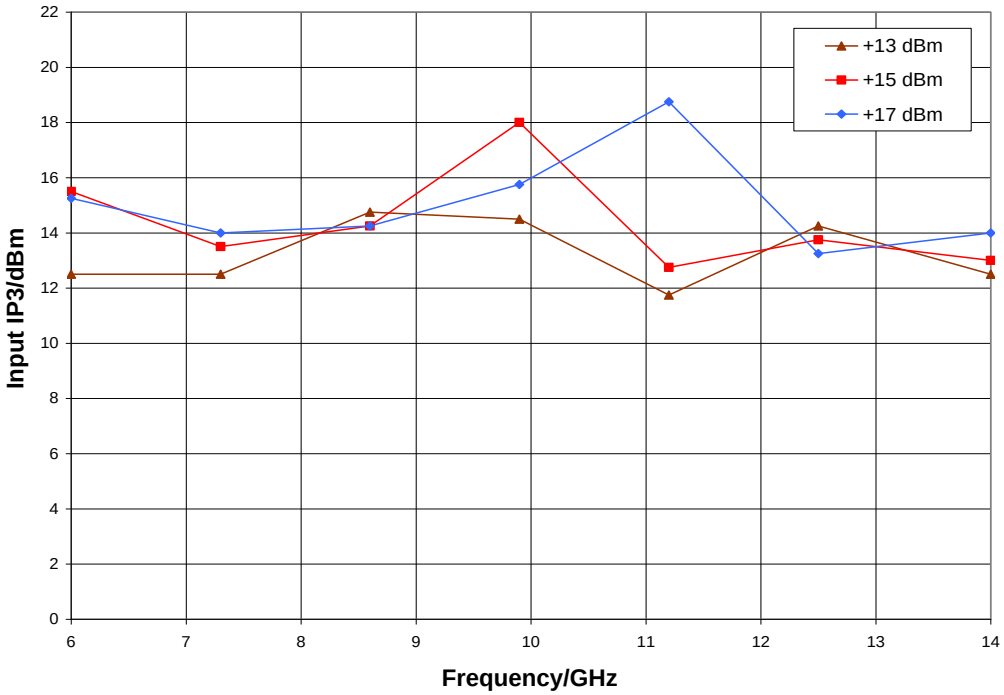


Input IP3 vs. LO Drive, IF = 100 MHz

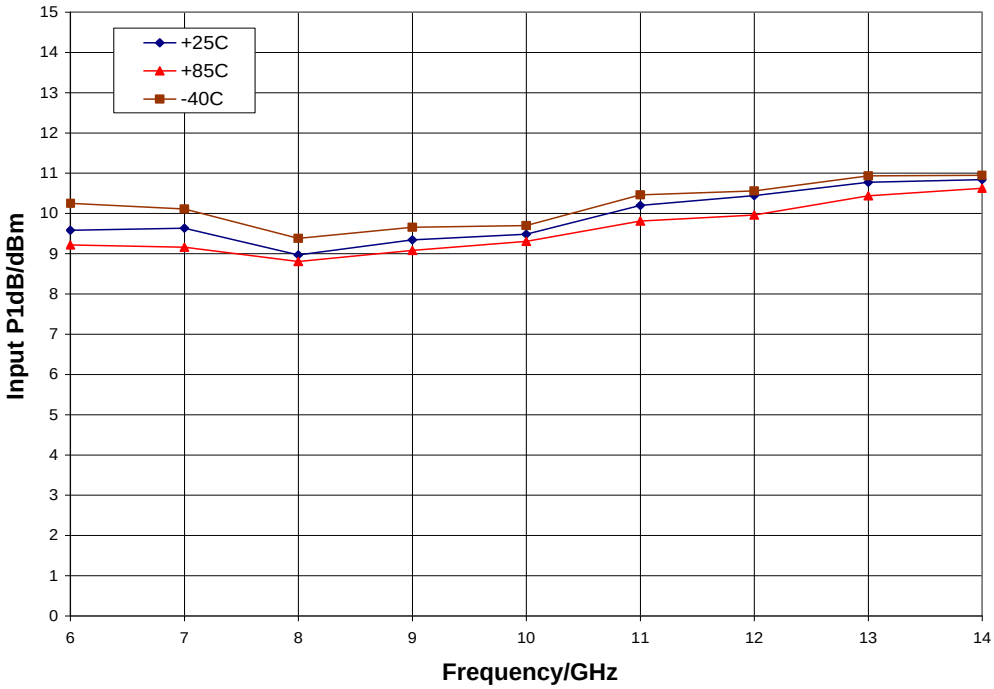


Typical Performance

Upconverter Performance, Input IP3 vs. LO Drive, IF = 100 MHz



Input P1dB vs. Temperature, LO = +13 dBm, IF = 100 MHz USB



Typical Performance

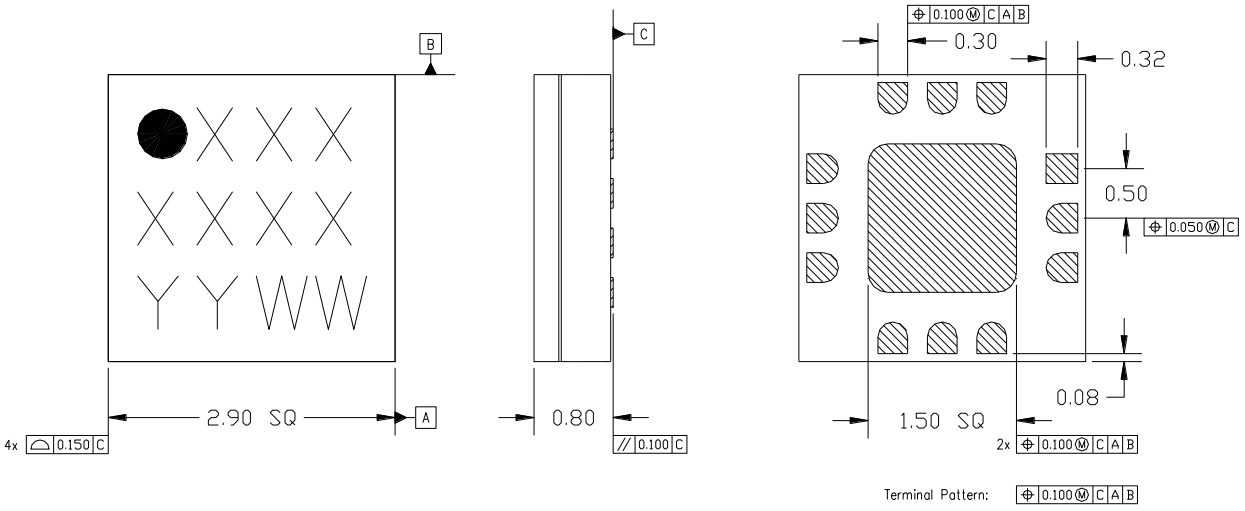
MxN Spurious Outputs

mRF	nLO				
	0	1	2	3	4
0	xx	6.5	33.5	30.5	29.5
1	17.5	0	37.5	38.5	57.5
2	> 75	69.5	66.5	66.5	> 75
3	> 75	> 75	> 75	62.5	> 75
4	> 75	> 75	> 75	> 75	> 75

RF = 10.1 GHz @ -10 dBm
LO = 10.0 GHz @ +13 dBm
All values in dBc below the IF output power level (1RF - 1LO)

Mechanical Information

Package Information and Dimensions



Notes:

1. All dimensions shown in mm.
2. Material: Black alumina
3. Lead finish
   - 3.1. Ni: 8.89um max, 1.27um min
   - 3.2. Pd: 0.17um max, 0.07um min
   - 3.3. Au: 0.254um max, 0.03um min
4. Marking
   - 4.1. Line 1: Part number
     - 4.1.1. Example: CMD177C3 shall be marked as 177
   - 4.2. Line 2: Lot number
   - 4.3. Line 3: Date code – Last 2 digits of the year of manufacture followed by a 2 digit week code
5. Alternate pin #1 identifier is a single square pad
6. Alternate die paddle may have chamfered corners

Recommended PCB Land Pattern

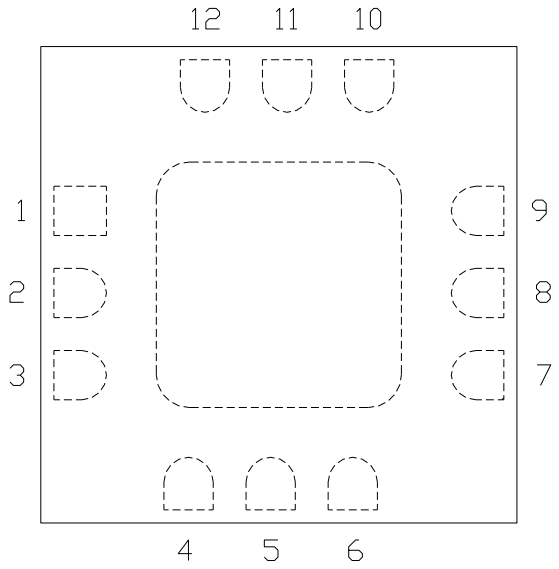
Qorvo recommends that the user develop the land pattern that will provide the best design for proper solder reflow and device attach for their specific application. Please review Qorvo Application Note AN 105 for a recommended land pattern approach.

Recommended Solder Reflow Profile

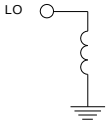
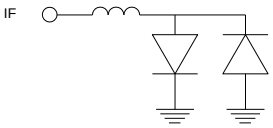
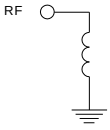
Qorvo recommends screen printing with belt furnace reflow to ensure proper solder reflow and device attach. Please review Qorvo Application Note AN 102 for a recommended solder reflow profile.

Pin Description

Pin Diagram



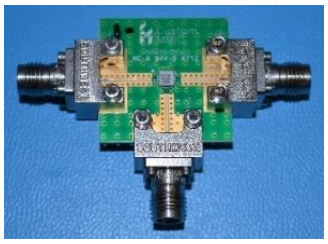
Functional Description

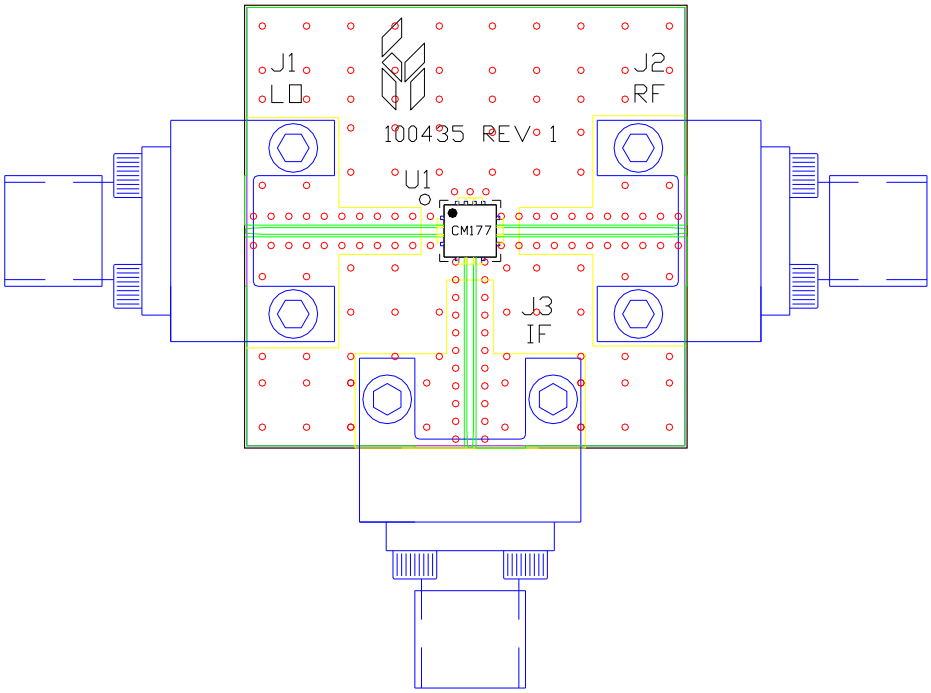
Pin	Function	Description	Schematic
1, 3, 4, 6, 7, 9 and die paddle	Ground	Connect to RF / DC ground	
2	LO	This pin is DC coupled and matched to 50 ohms.	
5	IF	This pin is DC coupled. For applications not requiring operation to DC, this port should be DC blocked externally using a series capacitor whose value has been chosen to pass the necessary IF frequency range. For operation to DC, this pin must not source or sink more than 16 mA of current or part non-function or part failure may result.	
8	RF	This pin is DC coupled and matched to 50 ohms.	
10 - 12	N/C	No connections required. These pins may be connected to RF / DC ground	

Applications Information

Evaluation Board

The circuit board shown has been developed for optimized assembly at Qorvo. A sufficient number of via holes should be used to connect the top and bottom ground planes. As surface mount processes vary, careful process development is recommended.





Designator	Description
J1 - J3	SMA End Launch Connector
U1	CMD177C3 Fundamental Mixer
PCB	100435 Evaluation PCB

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1A	ESDA / JEDEC JS-001-2012
MSL – Moisture Sensitivity Level	Level 1	IPC/JEDEC J-STD-020

Caution!
ESD-Sensitive Device

RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Antimony Free
- TBBP-A ($C_{15}H_{12}Br_40_2$) Free
- SVHC Free
- PFOS Free
- Halogen Free

Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

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Email: customer.support@qorvo.com

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