SiC Schottky Barrier Diode

TRS10V65H

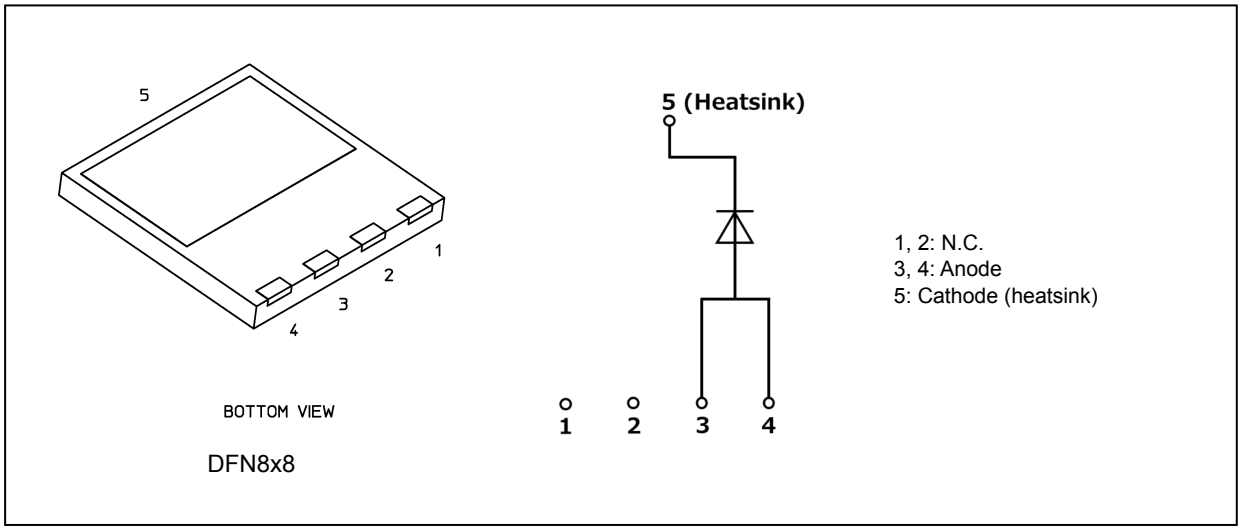
1. Applications

- Power Factor Correction
- Solar Inverters
- Uninterruptible Power Supplies
- DC-DC Converters

2. Features

(1) Chip design of 3rd generation
(2) Low forward voltage : V_F = 1.2 V (typ.)
(3) Low total capacitive charge: Q_c = 27 nC (typ.)
(4) Low reverse current: I_R = 2.0 μA (typ.)

3. Packaging and Internal Circuit



Start of commercial production
2023-05

4. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Repetitive peak reverse voltage	V_{RRM}		650	V
Forward DC current	$I_{F(DC)}$	(Note 1)	10	A
		(Note 2)	26	
Non-repetitive peak forward surge current	I_{FSM}	(Note 3)	54	A
		(Note 4)	46	
		(Note 5)	510	
Power dissipation	P_D	(Note 2)	78	W
Junction temperature	T_j		175	°C
Storage temperature	T_{stg}		-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: T_c = 145 °C
Note 2: T_c = 25 °C
Note 3: f = 50 Hz (half-sine wave, t = 10 ms), T_c = 25 °C
Note 4: f = 50 Hz (half-sine wave, t = 10 ms), T_c = 150 °C
Note 5: Square wave, t = 10 μs, T_c = 25 °C

5. Thermal Characteristics

Characteristics	Symbol	Note	Max	Unit
Thermal resistance (junction-to-case)	$R_{th(j-c)}$	(Note 1)	1.90	°C/W

Note 1: T_c = 25°C

6. Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Forward voltage(pulse measurement)	V_F	I_F = 5 A	—	1.0	—	V
		I_F = 10 A	—	1.2	1.35	
		I_F = 10 A, T_a = 150°C	—	1.36	—	
Reverse current(pulse measurement)	I_R	V_R = 650 V	—	2.0	100	μA
		V_R = 650 V, T_a = 150°C	—	18	—	
Total capacitance	C_t	V_R = 1 V, f = 1 MHz	—	649	—	pF
		V_R = 400 V, f = 1 MHz	—	38	—	
		V_R = 650 V, f = 1 MHz	—	36	—	
Total capacitive charge	Q_c	V_R = 400 V, f = 1 MHz	—	27	—	nC

7. Marking

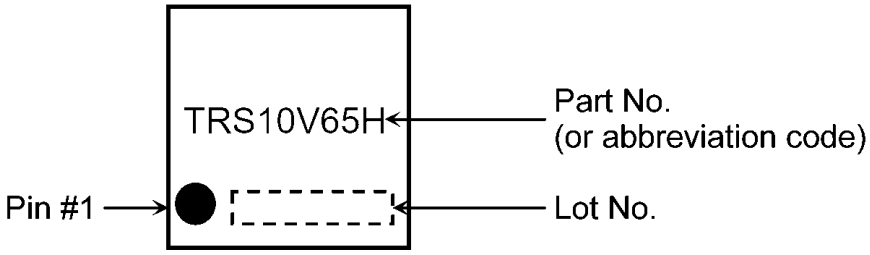


Fig. 7.1 Marking

8. Usage Considerations

For other design considerations, see the Toshiba website.

9. Characteristics Curves (Note)

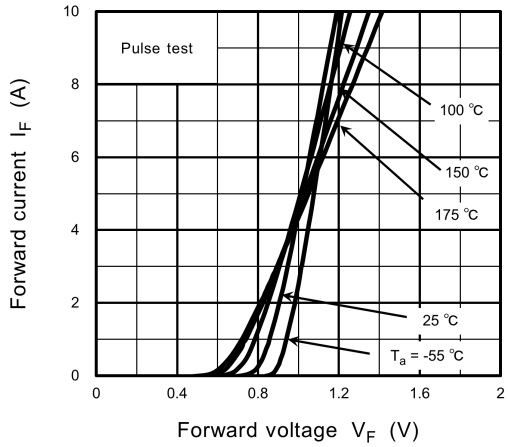


Fig. 9.1 I_F - V_F

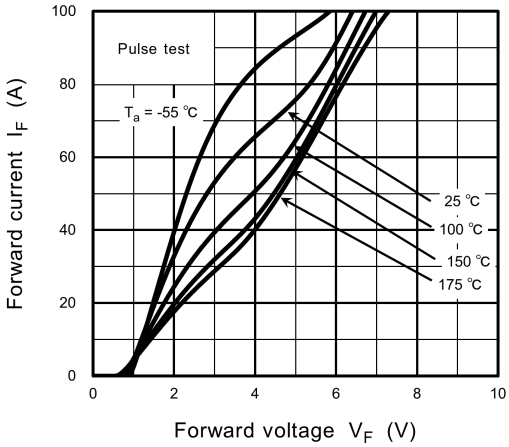


Fig. 9.2 I_F - V_F

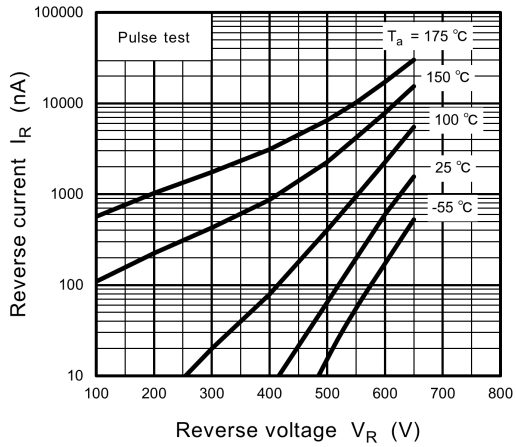


Fig. 9.3 I_R - V_R

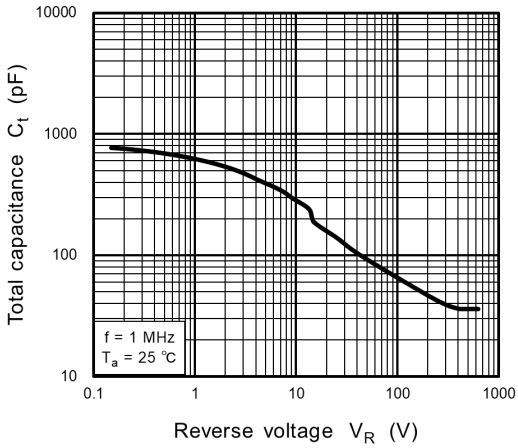


Fig. 9.4 C_t - V_R

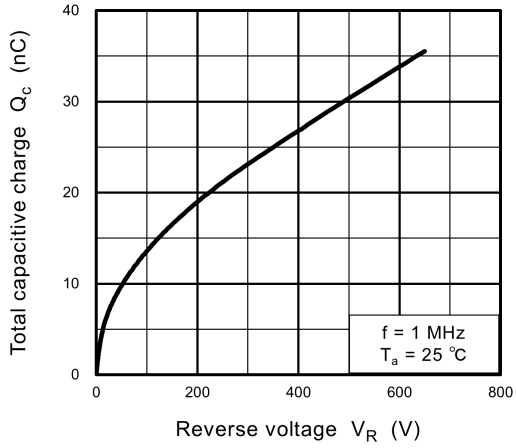


Fig. 9.5 Q_c - V_R

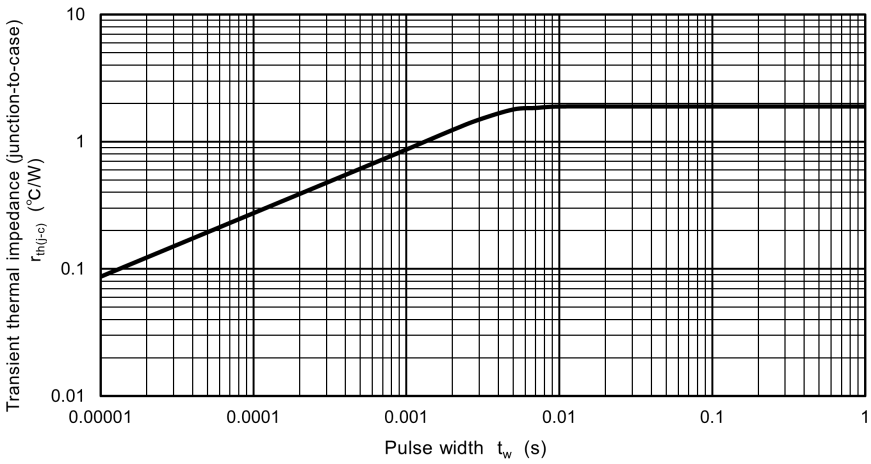


Fig. 9.6 $r_{th(j-c)}$ - t_w
(Guaranteed Maximum)

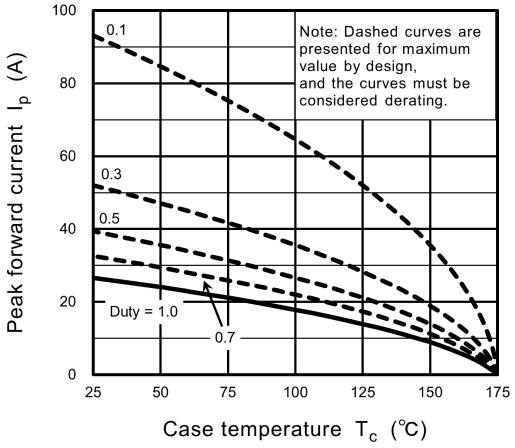


Fig. 9.7 I_p - T_c

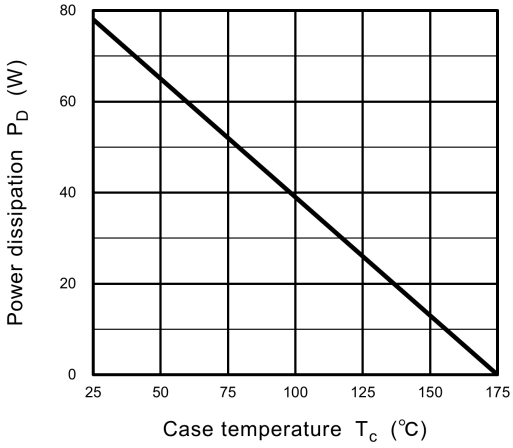
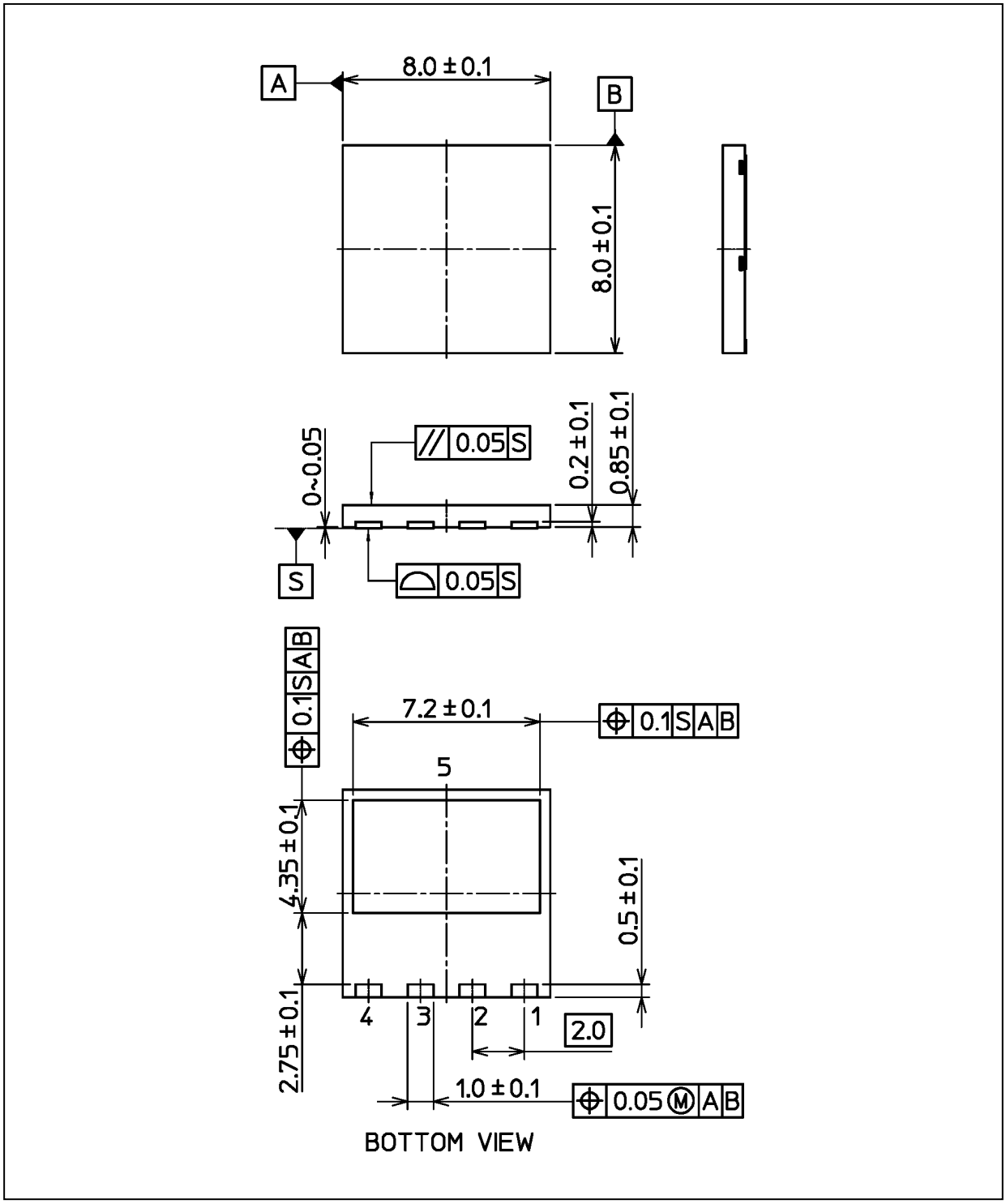


Fig. 9.8 PD - T_c
(Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

Unit: mm



Weight: 0.175 g (typ.)

Package Name(s)
TOSHIBA: 2-8T1A
Nickname: DFN8x8

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