SiC Schottky Barrier Diode

# TRS20N65FB

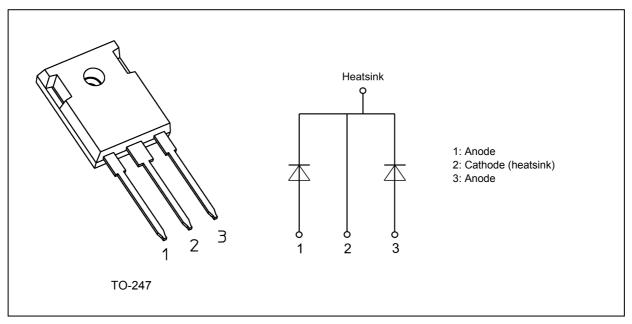
### 1. Applications

- Power Factor Correction
- Solar Inverters
- Uninterruptible Power Supplies
- DC-DC Converters

### 2. Features

- (1) Chip design of 2nd generation
- (2) High non-repetitive peak forward surge current:  $I_{FSM}$  (Per Leg) / (Both Legs) = 79 A / 158 A
- (3) Low junction capacitance:  $C_j$  (Per Leg) = 38 pF (typ.)
- (4) Low reverse current:  $I_R$  (Per Leg) = 0.5  $\mu$ A (typ.)

### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Repetitive peak reverse voltage	V <sub>RRM</sub>			650	V
Forward DC current	I <sub>F(DC)</sub>		Per Leg	10	A
			Both Legs	20	1
Forward pulse current	I <sub>FP</sub>	(Note 1)	Per Leg	100	1
			Both Legs	200	1
Power dissipation	PD	(Note 2)	Per Leg	107	W
			Both Legs	214	1
Non-repetitive peak forward surge current	I <sub>FSM</sub>	(Note 3)	Per Leg	79	A
			Both Legs	158	1
Junction temperature	Tj			175	°C
Storage temperature	T <sub>stg</sub>			-55 to 175	1
Mounting torque	TOR			0.8	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: t = 50 μs

Note 2:  $T_c$  = 25 °C

Note 3: f = 50 Hz (half-sine wave, t = 10 ms)

#### 5. Thermal Characteristics

Characteristics	Symbol	Note	Test Condition	Max	Unit
Thermal resistance (junction-to-case)	R <sub>th(j-c)</sub>	(Note 1)	Per Leg	1.4	°C/W
			Both Legs	0.7	
Thermal resistance (junction-to-ambient)	R <sub>th(j-a)</sub>	(Note 2)		50	

Note 1:  $T_c = 25 \degree C$ 

Note 2: T<sub>a</sub> = 25 °C

### 6. Electrical Characteristics (Unless otherwise specified, Ta = 25 °C ) (Per Leg)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	(pulse measurement)	V <sub>F</sub>	I <sub>F</sub> = 5 A	_	1.2	_	V
			I <sub>F</sub> = 10 A	_	1.45	1.6	
Reverse current	(pulse measurement)	I <sub>R</sub>	V <sub>R</sub> = 650 V	_	0.5	50	μA
Junction capacitance		Cj	V <sub>R</sub> = 400 V, f = 1 MHz	_	38	_	pF
Total junction capacitive charge		Q <sub>cj</sub>	V <sub>R</sub> = 0.1 to 400 V		24	_	nC

### 7. Marking (Note)

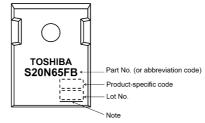


Fig. 7.1 Marking

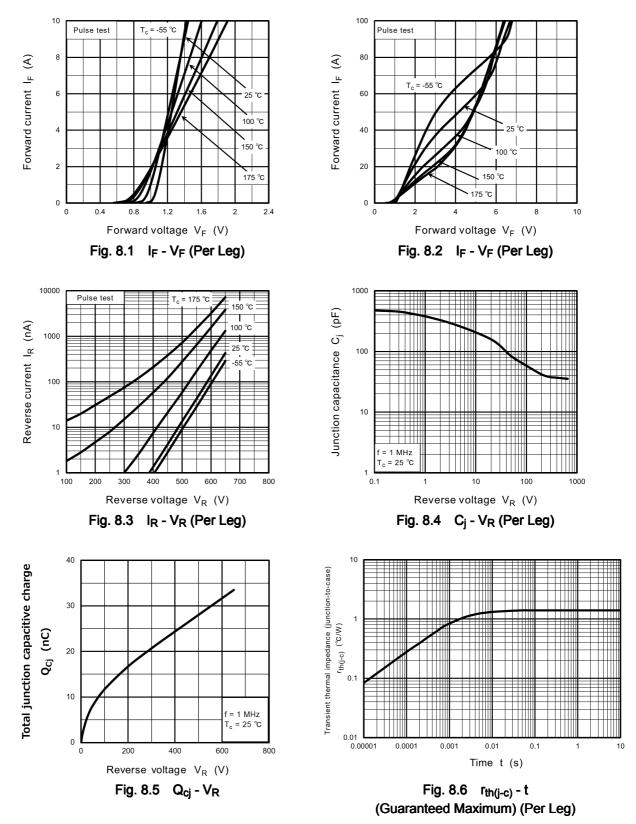
- Note: A line under a Lot No. identifies the indication of product Labels.
  - [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Abbreviation Code	Part Number		
S20N65FB	TRS20N65FB		

### 8. Characteristics Curves (Note)

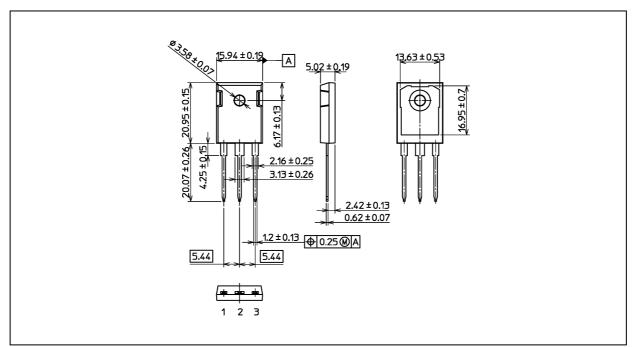


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### TRS20N65FB

### **Package Dimensions**

Unit: mm



#### Weight: 6.15 g (typ.)

Package Name(s)
TOSHIBA: 2-16L1A
Nickname: TO-247

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